Lecture 11: Pointer Analysis

17-355/17-665/17-819: Program Analysis
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Extending WHILE3ADDR with Pointers

```
I ::= ...
| p := \&x | taking the address of a variable
| p := q | copying a pointer from one variable to another
| *p := q | assigning through a pointer
| p := *q | dereferencing a pointer
```

Consider Constant Propagation

$$1: z := 1$$

$$2: p := \&z$$

3: *p := 2

4: print z

Need to know that line 3 changes variable z!

Consider Constant Propagation

- 1: z := 1
- 2: **if** (cond) p := &y**else** p := &z
- 3: *p := 2
- 4: print z



Points-To Analysis: May vs. Must and Strong Updates

$$f_{CP}[\![*p := y]\!](\sigma) =$$

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Points-To Analysis: May vs. Must and Strong Updates

$$f_{CP}[\![*p := y]\!](\sigma) = \sigma[z \mapsto \sigma(y) \mid z \in must-point-to(p)]$$

$$f_{CP}[\![*p := y]\!](\sigma) = \sigma[z \mapsto \sigma(z) \sqcup \sigma(y) \mid z \in may-point-to(p)]$$

Pointer Analysis

- Two common relations used as abstract values
 - Alias analysis: (x, y) alias pairs
 - Points-to analysis: p --> q // or sets for points-to(p)
 - Both have may and must versions
- Very expensive to run precisely as data-flow analysis
 - o Lattice is 2^{Var x Var}. Yikes!
 - Almost always needs to be inter-procedural
 - (even if used for intra-procedural optimizations)
 - Context-sensitivity is often important for adequate precision

Andersen's Analysis

- Flow-insensitive analysis
 - Considers only nodes of a CFG (i.e., instructions) and ignores all edges
 - What? Yes, really.
 - Trades-off precision for tractability
 - Can be combined with context-sensitive techniques
- Key idea: cast as constraint-solving problem
 - Abstract model of memory locations and points-to sets
 - Let l_x represent location of var x
 - Let p be the set of locations pointed-to by var p
 - One subset constraint per instruction
 - Invoke constraint solver. Done!

Andersen's Analysis

$$\boxed{\llbracket p := \&x \rrbracket \hookrightarrow l_x \in p} \ \textit{address-of}$$

$$\overline{[\![p:=q]\!]} \hookrightarrow p \supseteq q$$

$$\boxed{\llbracket *p := q \rrbracket \hookrightarrow *p \supseteq q} \ \textit{assign}$$

$$\frac{}{\llbracket p := *q \rrbracket \hookrightarrow p \supseteq *q} \ \textit{dereference}$$

Andersen's Analysis

$$\overline{[\![p:=\&x]\!]\hookrightarrow l_x\in p} \ address-of$$

$$\overline{[p:=q]\hookrightarrow p\supseteq q}$$
 copy

$$\boxed{\lVert *p := q \rVert \hookrightarrow *p \supseteq q}$$
 assign

$$n := *a \longrightarrow n \longrightarrow *a$$
 dereference

$$\frac{p \supseteq q \quad l_x \in q}{l_x \in p} \ copy$$

$$rac{st p\supseteq q \quad l_r\in p \quad l_x\in q}{l_x\in r}$$
 assign

$$\frac{p \supseteq *q \quad l_r \in q \quad l_x \in r}{l_x \in p} \ \textit{dereference}$$

Example

```
x := 42
y := 108
q := &x
if (...)
  p := q
else
  p := &y
r = &p
s = *r
print(*s)
print(*q)
```

$$\overline{\llbracket p := \&x \rrbracket} \hookrightarrow l_x \in p \quad address\text{-}of$$

$$\overline{\llbracket p := q \rrbracket} \hookrightarrow p \supseteq q \quad copy$$

$$\overline{\llbracket *p := q \rrbracket} \hookrightarrow *p \supseteq q \quad assign$$

$$\overline{\llbracket *p := *q \rrbracket} \hookrightarrow p \supseteq *q \quad dereference$$

$$\underline{p \supseteq q \quad l_x \in q \quad copy}$$

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 assign

$$\frac{p \supseteq *q \quad l_r \in q \quad l_x \in r}{l_x \in p} \ dereference$$

Dynamic Memory Allocation?

```
1: q := malloc()
2: p := malloc()
3: p := q
4: r := &p
5: s := malloc()
6: *r := s
7: t := &s
8: u := *t
```

Dynamic Memory Allocation

```
egin{array}{ll} 1: & q := malloc() \ 2: & p := malloc() \ 3: & p := q \ 4: & r := \&p \ 5: & s := malloc() \ 6: & *r := s \ 7: & t := \&s \ \end{array}
```

8: u := *t

$$\boxed{n: p := malloc() \mid \hookrightarrow l_n \in p} \quad malloc$$

Exercise

$$1: q := malloc()$$

$$2: p := malloc()$$

$$3: p := q$$

$$4: r := \& p$$

$$5: s := malloc()$$

$$6: *r := s$$

$$7: t := \&s$$

$$8: u := *t$$

$$\overline{\llbracket p := \&x \rrbracket} \hookrightarrow l_x \in p \quad address\text{-}of$$

$$\overline{\llbracket p := q \rrbracket} \hookrightarrow p \supseteq q \quad copy$$

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$$\underline{p \supseteq q \quad l_x \in q \quad copy}$$

$$l_x \in p \quad copy$$

$$rac{st p\supseteq q \quad l_r\in p \quad l_x\in q}{l_x\in r}$$
 assign

$$\frac{p \supseteq *q \quad l_r \in q \quad l_x \in r}{l_x \in p} \ \textit{dereference}$$

Efficiency

- O(n) constraints
- O(n) firings per copy-constraint
- O(n²) firings per assign/deref-constraint
- Worst-case O(n³) firings
- Can be solved in O(n³) time
 - McAllester [SAS'99]
- O(n²) in practice
 - Sridharan et al. [SAS'09]
 - K-sparseness property

$$\boxed{\llbracket p := \&x \rrbracket \hookrightarrow l_x \in p} \ \textit{address-of}$$

$$\overline{[\![p:=q]\!]\hookrightarrow p\supseteq q}$$
 copy

$$\boxed{\llbracket *p := q \rrbracket \hookrightarrow *p \supseteq q} \ assign$$

$$\boxed{\llbracket p := *q \rrbracket \hookrightarrow p \supseteq *q} \ \textit{dereference}$$

$$\frac{p \supseteq q \quad l_x \in q}{l_x \in p} \ copy$$

$$*p \supseteq q \quad l_r \in p \quad l_x \in q \ assign$$

$$\frac{p \supseteq *q \quad l_r \in q \quad l_x \in r}{l_x \in p} \ \textit{dereference}$$

$$\boxed{[n:p:=malloc()] \hookrightarrow l_n \in p} \quad malloc$$

Field-Sensitivity

- 1: p.f := &x
- 2: p.g := &y

Field-Sensitivity

1:
$$p.f := \&x$$

$$2: p.g := \&y$$

A field-insensitive approach just treats fields `.f` as dereferences `*`.

Field-Sensitive Analysis

$$\overline{[\![p:=q.f]\!]} \hookrightarrow p \supseteq q.f$$
 field-read

$$\llbracket p.f := q \rrbracket \hookrightarrow p.f \supseteq q$$
 field-assign

Field-Sensitive Analysis

$$\llbracket p.f := q \rrbracket \hookrightarrow p.f \supseteq q$$
 field-assign

$$rac{p\supseteq q.f \quad l_q\in q \quad l_f\in l_q.f}{l_f\in p}$$
 field-read

$$\frac{p.f\supseteq q\quad l_p\in p\quad l_q\in q}{l_q\in l_p.f}$$
 field-assign

Field-Sensitive Analysis

$$\overline{\llbracket p := q.f
\rrbracket} \hookrightarrow p \supseteq q.f$$
 field-read
$$\overline{\llbracket p.f := q
\rrbracket} \hookrightarrow p.f \supseteq q$$
 field-assign

$$\frac{p\supseteq q.f \quad l_q\in q \quad l_f\in l_q.f}{l_f\in p}$$
 field-read

$$rac{p.f\supseteq q\quad l_p\in p\quad l_q\in q}{l_q\in l_p.f}$$
 field-assign

- Problem: Quadratic-in-practice is still not ultra-scalable
- Challenge: Need ~LINEAR. How?
 - Solution space of pointer analysis (e.g. points-to sets) itself is O(n²).
- Key idea: Use constant-space per pointer. Merge aliases and alternates into the same equivalence class.
 - p can point to q or r? Let's treat q and r as the same pseudo-var and merge everything we know about q and r.
 - Points-to "sets" are basically singletons

Steensgaard's Analysis - Example

 $egin{array}{lll} 1: & p := \&x \ 2: & r := \&p \ 3: & q := \&y \ 4: & s := \&q \ 5: & r := s \end{array}$

Steensgaard's Analysis - Example

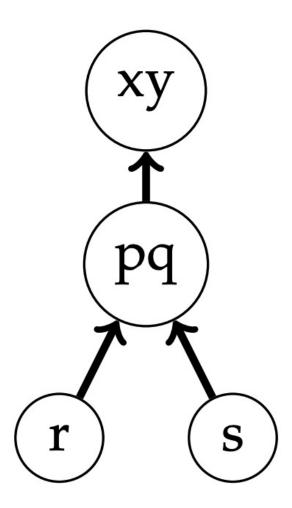
$$1: p := \&x$$

$$2: r := \& p$$

$$3: q := \& y$$

$$4: s := \&q$$

$$5: r := s$$



Steensgaard's Analysis - Exercise

1: a := &x2: b := & y3: if p then4: y := &z5: else 6: y := &x7: c := & y

$$\frac{}{\llbracket p := q \rrbracket \hookrightarrow join(*p, *q)} \stackrel{copy}{}$$

$$\frac{}{\llbracket p := \&x \rrbracket \hookrightarrow join(*p, x)} \stackrel{address-of}{}$$

$$\frac{}{\llbracket p := *q \rrbracket \hookrightarrow join(*p, **q)} \stackrel{dereference}{}$$

$$\frac{}{\llbracket *p := q \rrbracket \hookrightarrow join(**p, *q)} \stackrel{assign}{}$$

 $\boxed{\llbracket *p := q \rrbracket \hookrightarrow join(**p, *q)}$

```
join (\ell_1,\ell_2)
                                                            if (find(\ell_1) = find(\ell_2))
      \overline{[p := q] \hookrightarrow join(*p, *q)} copy
                                                                      return
                                                            n_1 \leftarrow *\ell_1
 \overline{[\![p:=\&x]\!]\hookrightarrow join(*p,x)} \ address-of
                                                            n_2 \leftarrow *\ell_2
                                                            union (\ell_1, \ell_2)
||p| := *q|| \hookrightarrow join(*p, **q)| dereference
                                                             join (n_1, n_2)
```

- Abstract locations implemented as union-find data structure
 - \circ Each union and find operation takes $O(\alpha(n))$ time each
 - o Total algorithm running time is $O(n * \alpha(n)) \sim almost linear$
 - Space consumption is linear
- In practice: very scalable
 - Millions of LoC

OOP: Dynamic Dispatch

```
class A { A foo(A x) { return x; } }
class B extends A { A foo(A x) { return new D(); } }
class D extends A { A foo(A x) { return new A(); } }
class C extends A { A foo(A x) { return this; } }

// in main()
A x = new A();
while (...)
    x = x.foo(new B()); // may call A.foo, B.foo, or D.foo
A y = new C();
y.foo(x); // only calls C.foo
```