R10\_R20 <=10\_20

M10\_M20 <=16\_26

LW格式：“Ri Rj imm”表示mem[Ri+imm]->Rj

伪指令 结果

LW R10 R11 1 R10<=18

LW R12 R13 1 R12<=20

ADD R14 R12 R12 R14<=40

LW R10 R11 2 R10<=19

LW R12 R13 2 R12<=21

ADD R14 R12 R10 R14<=40

LW R10 R11 3 R10<=20

LW R12 R13 3 R12<=22

ADD R14 R10 R12 R14<=42

LW R10 R11 4 R10<=21

LW R12 R13 4 R12<=23

ADD R14 R10 R10 R14<=42

ADD R15 R16 R17 R15<=33

ADD R18 R19 R20 R18<=39

ADD R15 R18 R18 R15<=78

ADD R15 R16 R17 R15<=33

ADD R18 R19 R20 R18<=39

ADD R15 R18 R15 R15<=72

ADD R15 R16 R17 R15<=33

ADD R18 R19 R20 R18<=39

ADD R18 R15 R18 R18<=72

ADD R15 R16 R17 R15<=33

ADD R18 R19 R20 R18<=39

ADD R18 R15 R15 R18<=66