

# Hardware In Loop Project Report

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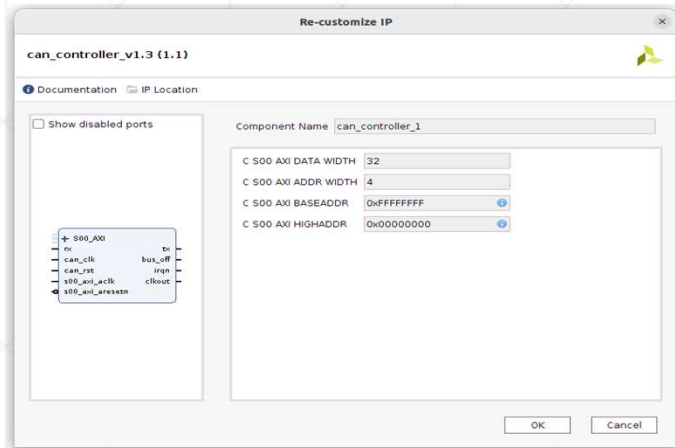
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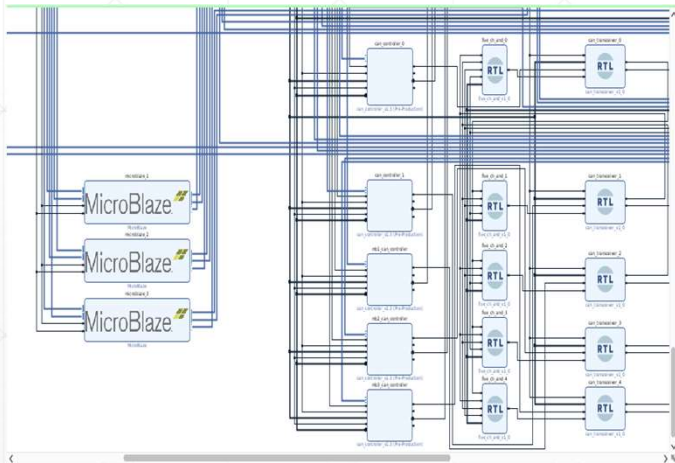
# Current Progress

Work	Description	Status
Build CAN Controller IP	pack the can controller to an AXI IP core and build its driver	Finished, could be called by microblaze or risc-v core
Build CAN Network	Build a CAN network with multiple ECUs and a control node	Finished, 3 ECU(based on microblaze) work together on one Nexys Board connected by CAN bus
Build ECU basic logic	build some basic logic of ECUS	Finished, ECUs could interact with physical switches and leds to simulate real devices and communicate with CAN messages
Build Control program on PC	build an UI program to control the HWIL process , monitor status and execute test	Finished, based on PyQt, could monitor and send command to the Control node and ECUs
Implement Dos, Replay and Spoofing Attack	Use the user interface to inject attacks to the CAN network	In progress, could inject Spoofing and Replay attack to the CAN network

## Current Progress - CAN Control IP and Network



Pack the can controller to an AXI IP core that could be used by microblaze the risc-v core instead of using gpio control.



Build the On board CAN bus network, could be extended with physical CAN network or other simulation instance boards, include risc-v based ECU controller

# Current Progress - ECU logics

Build up 3 ECUs with different logic for the test.  
All ECUs: Generate Life signal to indicate that they are alive.  
periodically send status messages to report their status.

ECU1: Control Engines, transmission unit and brake unit.

ECU2: Control Airbags and Steering Lights.

ECU3: Physical IO inputs, receive the signal from the sensors like Collision detector, brake detector, Lights control switches

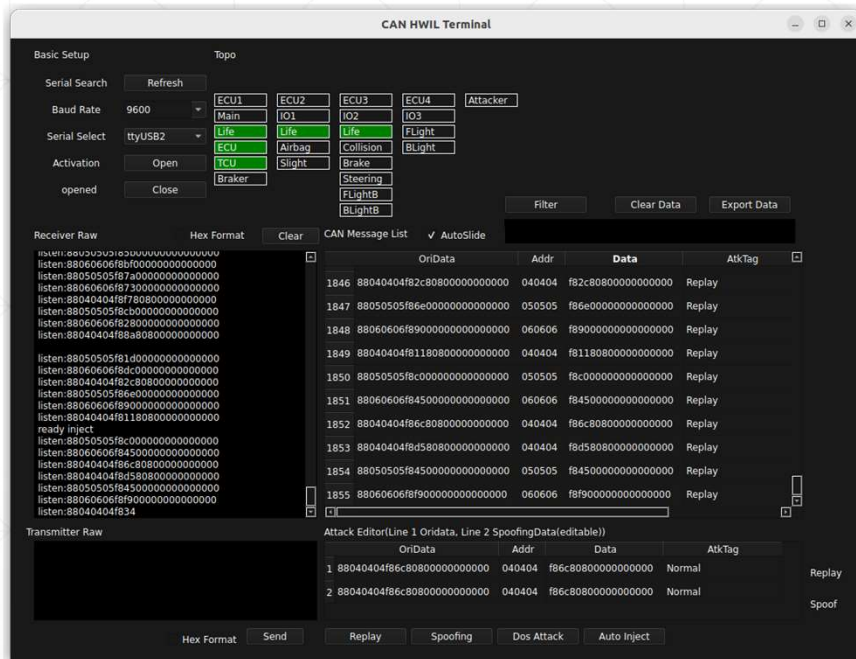
ECU4: Control Front light and back light of the car.

All the ECUs communicate with CAN bus.



Test

# Current Progress - User Interface



GUI Test terminal built by PyQt, could implement the functions as below:

1. Monitor and analysis the status of each ECU
2. Monitor and send raw data to the CAN bus
3. Excute Replay, Spoofing and Dos Attack(Dos Attack in under developing) and mark the messages with attack tags
4. Record and export the CAN messages to csv files for model training.



## Next Step

Work	Description	Deadline
Dos Attack	Implement dos attack with GUI command	3/22
Auto Injection	GUI could accept injection test sequence and execute them automatically and periodically	3/29
Object code download	GUI could download the elf file to the ECUs	4/5
Data Sorting	Sort the data generated by the auto injection	4/5
Model Training	Train the model to detect the injection(Existing dataset[1] and generated dataset, generated dataset for flexible specific and reconfigurable application scenarios)	4/15
Interface expansion(To be discussed)	Extend the interface to be compatible for physical can bus interface and other boards with risc-v cores	4/30

[1] Car Hacking dataset mentioned in "Exploring Highly Quantised Neural Networks for Intrusion Detection in Automotive CAN" from "CAR Hacking dataset, "https://ocslab.hksecurity.net/datasets/can-intrusion-dataset," 2020."

# Needed Support

Support	Description
Dos Attack method	Regarding the DoS attack implemented by a message mentioned earlier, could you please provide me more relevant reference materials?
Advice on model training	For model training, I am thinking about this CAN bus hardware-in-the-loop simulation network that can quickly expand configuration changes. This application can quickly establish an attack diagnosis model adapted to the established network. Should we produce data sets based on this idea or still use existing data sets as we mentioned before. Or should I do both?
Advice on interface expansion	Regarding the interface, in order to meet the scalability of the simulation environment, including adapting to the Risc-V core or actual CAN devices, do we need to expand the physical CAN interface, or expand another FPGA with Risc-V (as I mentioned before Yes, it is challenging to implement the entire project in a short time using Risc-V without BSP, but it is possible to make it only serve as an ECU node and download the application, but it needs to be on another board. Because jtag will conflict with microblaze)
Ideas about model hardware deployment	If we have time, could we consider deploying the model to a hardware environment? Referring to previous work, maybe we can use HLS to build the basic neural network layer, or is there a better solution?(Of course, if time permits)