

sg13g2_stdcell_slow_1p35V_125C Library

Cell Groups
AND2
AND3
AND4
AO21
BTLx
BUx
DECAPx
DDFRRx
DLHQ
DLHRQ
DLHR
DLLRQ
DLLR
DLY1
DLY2
DLY4
EINVINx
FILLx
GCLK
INx
ITL
KEEPSTATE
MUX2

MUX4
NAND2B1
NAND2
NAND3B1
NOR2
NOR3
NOR4
NP_ANT
OR2
OR3
OR4
SDFRRS
SGCLK
TIE0
TIE1
XNOR2_1
XOR2_1

AND2



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT		OUTPUT
A	B	X
0	x	0
1	0	0
1	1	1

Footprint

Cell Name	Area
sg13g2_and2_1	9.07200

Pin Capacitance Information

Cell Name	Pin Cap(pf)		Max Cap(pf)
	A	B	X
sg13g2_and2_1	0.00235	0.00228	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_and2_1	823.86600	1010.78000	1352.79000

Delay Information

Delay(ns) to X rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_and2_1	A->X (RR)	0.01860	0.00100	0.06356	0.32940	0.06480	0.33581	2.50740	0.30000	1.21362
	B->X (RR)	0.01860	0.00100	0.06894	0.32940	0.06480	0.33690	2.50740	0.30000	1.21150

Delay(ns) to X falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_and2_1	A->X (FF)	0.01860	0.00100	0.05492	0.32940	0.06480	0.30579	2.50740	0.30000	1.08996
	B->X (FF)	0.01860	0.00100	0.06033	0.32940	0.06480	0.32123	2.50740	0.30000	1.12961

Power Information

Internal switching power(pJ) to X rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_and2_1	A	0.01860	0.00100	0.00792	0.32940	0.06480	0.00892	2.50740	0.30000	0.02180
	B	0.01860	0.00100	0.00959	0.32940	0.06480	0.01016	2.50740	0.30000	0.02295

Internal switching power(pJ) to X falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_and2_1	A	0.01860	0.00100	0.00695	0.32940	0.06480	0.00795	2.50740	0.30000	0.02100
	B	0.01860	0.00100	0.00726	0.32940	0.06480	0.00834	2.50740	0.30000	0.02150

AND3



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT			OUTPUT
A	B	C	X
0	x	x	0
1	0	x	0
1	1	0	0
1	1	1	1

Footprint

Cell Name	Area
sg13g2_and3_1	14.51520

Pin Capacitance Information

Cell Name	Pin Cap(pf)			Max Cap(pf)
	A	B	C	X
sg13g2_and3_1	0.00235	0.00225	0.00227	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_and3_1	822.26300	1009.25000	1926.14000

Delay Information

Delay(ns) to X rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_and3_1	A->X (RR)	0.01860	0.00100	0.08526	0.32940	0.06480	0.37166	2.50740	0.30000	1.29997
	B->X (RR)	0.01860	0.00100	0.09493	0.32940	0.06480	0.37529	2.50740	0.30000	1.30179
	C->X (RR)	0.01860	0.00100	0.09947	0.32940	0.06480	0.37122	2.50740	0.30000	1.26711

Delay(ns) to X falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_and3_1	A->X (FF)	0.01860	0.00100	0.05884	0.32940	0.06480	0.31513	2.50740	0.30000	1.09144
	B->X (FF)	0.01860	0.00100	0.06454	0.32940	0.06480	0.32902	2.50740	0.30000	1.13079
	C->X (FF)	0.01860	0.00100	0.06822	0.32940	0.06480	0.33994	2.50740	0.30000	1.16442

Power Information

Internal switching power(pJ) to X rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_and3_1	A	0.01860	0.00100	0.00917	0.32940	0.06480	0.00987	2.50740	0.30000	0.02290
	B	0.01860	0.00100	0.01081	0.32940	0.06480	0.01097	2.50740	0.30000	0.02319
	C	0.01860	0.00100	0.01242	0.32940	0.06480	0.01245	2.50740	0.30000	0.02389

Internal switching power(pJ) to X falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_and3_1	A	0.01860	0.00100	0.00703	0.32940	0.06480	0.00790	2.50740	0.30000	0.01989
	B	0.01860	0.00100	0.00745	0.32940	0.06480	0.00816	2.50740	0.30000	0.02062
	C	0.01860	0.00100	0.00769	0.32940	0.06480	0.00840	2.50740	0.30000	0.02065

AND4



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT				OUTPUT
A	B	C	D	X
0	x	x	x	0
1	0	x	x	0
1	1	0	x	0
1	1	1	0	0
1	1	1	1	1

Footprint

Cell Name	Area
sg13g2_and4_1	14.51520

Pin Capacitance Information

Cell Name	Pin Cap(pf)				Max Cap(pf)
	A	B	C	D	X
sg13g2_and4_1	0.00200	0.00194	0.00225	0.00227	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_and4_1	824.35200	969.92000	2499.70000

Delay Information

Delay(ns) to X rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_and4_1	A->X (RR)	0.01860	0.00100	0.10832	0.32940	0.06480	0.40706	2.50740	0.30000	1.37741
	B->X (RR)	0.01860	0.00100	0.12163	0.32940	0.06480	0.41411	2.50740	0.30000	1.38557
	C->X (RR)	0.01860	0.00100	0.12943	0.32940	0.06480	0.41318	2.50740	0.30000	1.35745
	D->X (RR)	0.01860	0.00100	0.13400	0.32940	0.06480	0.41166	2.50740	0.30000	1.31776

Delay(ns) to X falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_and4_1	A->X (FF)	0.01860	0.00100	0.06241	0.32940	0.06480	0.32158	2.50740	0.30000	1.08789
	B->X (FF)	0.01860	0.00100	0.06799	0.32940	0.06480	0.33398	2.50740	0.30000	1.12399
	C->X (FF)	0.01860	0.00100	0.07198	0.32940	0.06480	0.34444	2.50740	0.30000	1.15948
	D->X (FF)	0.01860	0.00100	0.07479	0.32940	0.06480	0.35423	2.50740	0.30000	1.19118

Power Information

Internal switching power(pJ) to X rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_and4_1	A	0.01860	0.00100	0.01063	0.32940	0.06480	0.01105	2.50740	0.30000	0.02404
	B	0.01860	0.00100	0.01274	0.32940	0.06480	0.01278	2.50740	0.30000	0.02440
	C	0.01860	0.00100	0.01348	0.32940	0.06480	0.01337	2.50740	0.30000	0.02466
	D	0.01860	0.00100	0.01382	0.32940	0.06480	0.01364	2.50740	0.30000	0.02511

Internal switching power(pJ) to X falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_and4_1	A	0.01860	0.00100	0.00650	0.32940	0.06480	0.00722	2.50740	0.30000	0.01856
	B	0.01860	0.00100	0.00692	0.32940	0.06480	0.00743	2.50740	0.30000	0.01896
	C	0.01860	0.00100	0.00801	0.32940	0.06480	0.00850	2.50740	0.30000	0.02043
	D	0.01860	0.00100	0.00827	0.32940	0.06480	0.00897	2.50740	0.30000	0.02171

Passive power(pJ) for A rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_and4_1	0.01860	-0.00052	0.32940	-0.00052	2.50740	-0.00051

Passive power(pJ) for A falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_and4_1	0.01860	0.00112	0.32940	0.00114	2.50740	0.00115

Passive power(pJ) for A rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_and4_1	$(B * C * !D) + (B * !C)$	0.01860	-0.00052	0.32940	-0.00052	2.50740	-0.00051

Passive power(pJ) for A falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_and4_1	$(B * C * !D) + (B * !C)$	0.01860	0.00112	0.32940	0.00114	2.50740	0.00115

Passive power(pJ) for B rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_and4_1	0.01860	-0.00086	0.32940	-0.00088	2.50740	-0.00087

Passive power(pJ) for B falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_and4_1	0.01860	0.00089	0.32940	0.00092	2.50740	0.00092

Passive power(pJ) for B rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_and4_1	$(A * C * !D) + (A * !C)$	0.01860	-0.00086	0.32940	-0.00088	2.50740	-0.00087

Passive power(pJ) for B falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_and4_1	$(A * C * !D) + (A * !C)$	0.01860	0.00089	0.32940	0.00092	2.50740	0.00092

Passive power(pJ) for C rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_and4_1	0.01860	0.00000	0.32940	0.00000	2.50740	0.00000

Passive power(pJ) for C falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_and4_1	0.01860	0.00000	0.32940	0.00000	2.50740	0.00000

Passive power(pJ) for C rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_and4_1	$(A * !B * D) + (!A * D)$	0.01860	0.00000	0.32940	0.00000	2.50740	0.00000

Passive power(pJ) for C falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_and4_1	$(A * !B * D) + (!A * D)$	0.01860	0.00000	0.32940	0.00000	2.50740	0.00000

Passive power(pJ) for D rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_and4_1	0.01860	0.00125	0.32940	0.00128	2.50740	0.00126

Passive power(pJ) for D falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_and4_1	0.01860	0.00001	0.32940	-0.00008	2.50740	-0.00010

Passive power(pJ) for D rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_and4_1	$(A * !B * C) + (!A * C)$	0.01860	0.00125	0.32940	0.00128	2.50740	0.00126

Passive power(pJ) for D falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_and4_1	$(A * !B * C) + (!A * C)$	0.01860	0.00001	0.32940	-0.00008	2.50740	-0.00010

A021



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT			OUTPUT
A1	A2	B1	X
0	x	0	0
x	x	1	1
1	0	0	0
1	1	x	1

Footprint

Cell Name	Area
sg13g2_a21o_1	12.70080

Pin Capacitance Information

Cell Name	Pin Cap(pf)			Max Cap(pf)
	A1	A2	B1	X
sg13g2_a21o_1	0.00244	0.00254	0.00225	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_a21o_1	661.76800	1032.41000	1627.97000

Delay Information

Delay(ns) to X rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_a21o_1	A1->X (RR)	0.01860	0.00100	0.07901	0.32940	0.06480	0.36648	2.50740	0.30000	1.29152
	A2->X (RR)	0.01860	0.00100	0.08371	0.32940	0.06480	0.36260	2.50740	0.30000	1.27470
	B1->X (RR)	0.01860	0.00100	0.05197	0.32940	0.06480	0.32527	2.50740	0.30000	1.18123

Delay(ns) to X falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_a21o_1	A1->X (FF)	0.01860	0.00100	0.08932	0.32940	0.06480	0.34042	2.50740	0.30000	1.13213
	A2->X (FF)	0.01860	0.00100	0.09831	0.32940	0.06480	0.35568	2.50740	0.30000	1.16507
	B1->X (FF)	0.01860	0.00100	0.08747	0.32940	0.06480	0.35741	2.50740	0.30000	1.21105

Delay(ns) to X rising (conditional):

Cell Name	Timing Arc(Dir)	When	Delay(ns)								
			Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_a21o_1	B1->X (RR)	(A1 * !A2)	0.01860	0.00100	0.05197	0.32940	0.06480	0.32527	2.50740	0.30000	1.18123
	B1->X (RR)	(!A1 * A2)	0.01860	0.00100	0.04871	0.32940	0.06480	0.31153	2.50740	0.30000	1.14469

Delay(ns) to X falling (conditional):

Cell Name	Timing Arc(Dir)	When	Delay(ns)								
			Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_a21o_1	B1->X (FF)	(A1 * !A2)	0.01860	0.00100	0.08747	0.32940	0.06480	0.35741	2.50740	0.30000	1.21105
	B1->X (FF)	(!A1 * A2)	0.01860	0.00100	0.07693	0.32940	0.06480	0.33856	2.50740	0.30000	1.17471

Power Information

Internal switching power(pJ) to X rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_a21o_1	A1	0.01860	0.00100	0.00919	0.32940	0.06480	0.00978	2.50740	0.30000	0.02369
	A2	0.01860	0.00100	0.01092	0.32940	0.06480	0.01112	2.50740	0.30000	0.02351
	B1	0.01860	0.00100	0.00724	0.32940	0.06480	0.00804	2.50740	0.30000	0.02359

Internal switching power(pJ) to X falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_a21o_1	A1	0.01860	0.00100	0.01002	0.32940	0.06480	0.01025	2.50740	0.30000	0.02310
	A2	0.01860	0.00100	0.01012	0.32940	0.06480	0.01037	2.50740	0.30000	0.02245
	B1	0.01860	0.00100	0.00685	0.32940	0.06480	0.00795	2.50740	0.30000	0.02221

Internal switching power(pJ) to X rising (conditional):

Cell Name	Input	When	Power(pJ)								
			Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_a21o_1	B1	(A1 * !A2)	0.01860	0.00100	0.00897	0.32940	0.06480	0.00996	2.50740	0.30000	0.02502
	B1	(!A1 * A2)	0.01860	0.00100	0.00724	0.32940	0.06480	0.00804	2.50740	0.30000	0.02359

Internal switching power(pJ) to X falling (conditional):

Cell Name	Input	When	Power(pJ)								
			Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_a21o_1	B1	(A1 * !A2)	0.01860	0.00100	0.00710	0.32940	0.06480	0.00803	2.50740	0.30000	0.02188
	B1	(!A1 * A2)	0.01860	0.00100	0.00685	0.32940	0.06480	0.00795	2.50740	0.30000	0.02221

Passive power(pJ) for A1 rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_a21o_1	0.01860	-0.00023	0.32940	-0.00022	2.50740	-0.00022

Passive power(pJ) for A1 falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_a21o_1	0.01860	0.00023	0.32940	0.00022	2.50740	0.00022

Passive power(pJ) for A1 rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_a21o_1	(A2 * B1)	0.01860	0.00003	0.32940	-0.00016	2.50740	-0.00022
	(!A2 * B1)	0.01860	-0.00023	0.32940	-0.00022	2.50740	-0.00022

Passive power(pJ) for A1 falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_a21o_1	(A2 * B1)	0.01860	0.00025	0.32940	0.00023	2.50740	0.00023
	(!A2 * B1)	0.01860	0.00023	0.32940	0.00022	2.50740	0.00022

Passive power(pJ) for A2 rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_a21o_1	0.01860	-0.00015	0.32940	-0.00016	2.50740	-0.00016

Passive power(pJ) for A2 falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_a21o_1	0.01860	0.00015	0.32940	0.00016	2.50740	0.00016

Passive power(pJ) for A2 rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_a21o_1	(A1 * B1)	0.01860	0.00010	0.32940	-0.00010	2.50740	-0.00016
	(!A1 * B1)	0.01860	-0.00015	0.32940	-0.00016	2.50740	-0.00016

Passive power(pJ) for A2 falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_a21o_1	(A1 * B1)	0.01860	0.00018	0.32940	0.00017	2.50740	0.00017
	(!A1 * B1)	0.01860	0.00015	0.32940	0.00016	2.50740	0.00016

Passive power(pJ) for B1 rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_a21o_1	0.01860	0.00024	0.32940	0.00027	2.50740	0.00028

Passive power(pJ) for B1 falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_a21o_1	0.01860	0.00095	0.32940	0.00095	2.50740	0.00096

Passive power(pJ) for B1 rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_a21o_1	(A1 * A2)	0.01860	0.00024	0.32940	0.00027	2.50740	0.00028

Passive power(pJ) for B1 falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_a21o_1	(A1 * A2)	0.01860	0.00095	0.32940	0.00095	2.50740	0.00096

BTLx



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT		OUTPUT
A	TE_B	Z
0	0	0
1	0	1
-	1	HiZ

Footprint

Cell Name	Area
sg13g2_ebufn_8	45.36000
sg13g2_ebufn_4	25.40160
sg13g2_ebufn_2	18.14400

Pin Capacitance Information

Cell Name	Pin Cap(pf)		Max Cap(pf)
	A	TE_B	Z
sg13g2_ebufn_8	0.00554	0.01498	2.40000
sg13g2_ebufn_4	0.00288	0.00912	1.20000
sg13g2_ebufn_2	0.00245	0.00558	0.60000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_ebufn_8	2462.42000	3998.33000	7045.57000
sg13g2_ebufn_4	1611.87000	2240.96000	3625.89000
sg13g2_ebufn_2	1171.82000	1486.28000	1947.78000

Delay Information

Delay(ns) to Z rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_ebufn_8	A->Z (RR)	0.01860	0.01958	0.06534	0.32940	0.53698	0.57400	2.50740	2.41858	2.22069
	TE_B->Z (RR)	0.01860	0.01958	0.07063	0.32940	0.53698	0.16398	2.50740	2.41858	0.34997
	TE_B->Z (FR)	0.01860	0.01958	0.03490	0.32940	0.53698	0.52436	2.50740	2.41858	2.60019
sg13g2_ebufn_4	A->Z (RR)	0.01860	0.01045	0.06723	0.32940	0.26865	0.57380	2.50740	1.20945	2.21672
	TE_B->Z (RR)	0.01860	0.01045	0.05420	0.32940	0.26865	0.12457	2.50740	1.20945	0.25433
	TE_B->Z (FR)	0.01860	0.01045	0.03512	0.32940	0.26865	0.52192	2.50740	1.20945	2.59054
sg13g2_ebufn_2	A->Z (RR)	0.01860	0.00583	0.05806	0.32940	0.13443	0.54001	2.50740	0.60483	2.14378
	TE_B->Z (RR)	0.01860	0.00583	0.04655	0.32940	0.13443	0.10410	2.50740	0.60483	0.20586
	TE_B->Z (FR)	0.01860	0.00583	0.03553	0.32940	0.13443	0.52142	2.50740	0.60483	2.58935

Delay(ns) to Z falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_ebufn_8	A->Z (FF)	0.01860	0.02958	0.08499	0.32940	0.54698	0.48610	2.50740	2.42858	1.75969
	TE_B->Z (RF)	0.01860	0.02958	0.03982	0.32940	0.54698	-0.18771	2.50740	2.42858	-1.87426
	TE_B->Z (FF)	0.01860	0.02958	0.08510	0.32940	0.54698	0.48807	2.50740	2.42858	1.75090
sg13g2_ebufn_4	A->Z (FF)	0.01860	0.01555	0.08737	0.32940	0.27375	0.48870	2.50740	1.21455	1.76423
	TE_B->Z (RF)	0.01860	0.01555	0.03079	0.32940	0.27375	-0.18695	2.50740	1.21455	-1.87327
	TE_B->Z (FF)	0.01860	0.01555	0.06416	0.32940	0.27375	0.43695	2.50740	1.21455	1.62428
sg13g2_ebufn_2	A->Z (FF)	0.01860	0.00846	0.06589	0.32940	0.13706	0.44211	2.50740	0.60746	1.65775
	TE_B->Z (RF)	0.01860	0.00846	0.02139	0.32940	0.13706	-0.20424	2.50740	0.60746	-1.89041
	TE_B->Z (FF)	0.01860	0.00846	0.05474	0.32940	0.13706	0.40246	2.50740	0.60746	1.54255

Power Information

Internal switching power(pJ) to Z rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_ebufn_8	A	0.01860	0.01958	0.01735	0.32940	0.53698	0.02664	2.50740	2.41858	0.02579
	TE_B	0.01860	0.01958	0.01275	0.32940	0.53698	0.01053	2.50740	2.41858	0.00788
sg13g2_ebufn_4	A	0.01860	0.01045	0.00890	0.32940	0.26865	0.01317	2.50740	1.20945	0.01081
	TE_B	0.01860	0.01045	0.00635	0.32940	0.26865	0.00518	2.50740	1.20945	0.00228
sg13g2_ebufn_2	A	0.01860	0.00583	0.00483	0.32940	0.13443	0.00656	2.50740	0.60483	0.00539
	TE_B	0.01860	0.00583	0.00315	0.32940	0.13443	0.00269	2.50740	0.60483	0.00125

Internal switching power(pJ) to Z falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_ebufn_8	A	0.01860	0.02958	0.04274	0.32940	0.54698	0.04231	2.50740	2.42858	0.02917
	TE_B	0.01860	0.02958	0.00977	0.32940	0.54698	0.00730	2.50740	2.42858	0.00313
sg13g2_ebufn_4	A	0.01860	0.01555	0.02145	0.32940	0.27375	0.02115	2.50740	1.21455	0.01597
	TE_B	0.01860	0.01555	0.00489	0.32940	0.27375	0.00389	2.50740	1.21455	0.00122
sg13g2_ebufn_2	A	0.01860	0.00846	0.01041	0.32940	0.13706	0.01040	2.50740	0.60746	0.00846
	TE_B	0.01860	0.00846	0.00244	0.32940	0.13706	0.00209	2.50740	0.60746	0.00223

Passive power(pJ) for A rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_ebufn_8	0.01860	0.03507	0.32940	0.03721	2.50740	0.07595
sg13g2_ebufn_4	0.01860	0.01792	0.32940	0.01896	2.50740	0.03818
sg13g2_ebufn_2	0.01860	0.00957	0.32940	0.01074	2.50740	0.02782

Passive power(pJ) for A falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_ebufn_8	0.01860	0.01135	0.32940	0.01408	2.50740	0.05222
sg13g2_ebufn_4	0.01860	0.00600	0.32940	0.00732	2.50740	0.02625
sg13g2_ebufn_2	0.01860	0.00387	0.32940	0.00520	2.50740	0.02206

Passive power(pJ) for TE_B rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_ebufn_8	0.01860	-0.00553	0.32940	-0.00634	2.50740	0.01054
sg13g2_ebufn_4	0.01860	-0.00138	0.32940	-0.00124	2.50740	0.01741
sg13g2_ebufn_2	0.01860	0.00007	0.32940	0.00071	2.50740	0.01755

Passive power(pJ) for TE_B falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_ebufn_8	0.01860	0.05731	0.32940	0.05909	2.50740	0.07664
sg13g2_ebufn_4	0.01860	0.02971	0.32940	0.03144	2.50740	0.05038
sg13g2_ebufn_2	0.01860	0.01569	0.32940	0.01716	2.50740	0.03408

BU_x



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT	OUTPUT
A	X
0	0
1	1

Footprint

Cell Name	Area
sg13g2_buf_16	45.36000
sg13g2_buf_8	23.58720
sg13g2_buf_4	14.51520
sg13g2_buf_2	9.07200
sg13g2_buf_1	9.07200

Pin Capacitance Information

Cell Name	Pin Cap(pf)	Max Cap(pf)
	A	X
sg13g2_buf_16	0.01660	4.80000
sg13g2_buf_8	0.00829	2.40000
sg13g2_buf_4	0.00352	1.20000
sg13g2_buf_2	0.00244	0.60000
sg13g2_buf_1	0.00210	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_buf_16	7855.68000	10631.10000	13406.50000
sg13g2_buf_8	3927.86000	5315.64000	6703.42000
sg13g2_buf_4	1952.91000	2605.01000	3257.12000
sg13g2_buf_2	1090.12000	1391.01000	1691.89000
sg13g2_buf_1	775.59600	837.73500	899.87400

Delay Information

Delay(ns) to X rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_buf_16	A->X (RR)	0.01860	0.00100	0.05596	0.32940	1.03680	0.34813	2.50740	4.80000	1.25313
sg13g2_buf_8	A->X (RR)	0.01860	0.00100	0.05523	0.32940	0.51840	0.34647	2.50740	2.40000	1.25048
sg13g2_buf_4	A->X (RR)	0.01860	0.00100	0.07090	0.32940	0.25920	0.38091	2.50740	1.20000	1.38095
sg13g2_buf_2	A->X (RR)	0.01860	0.00100	0.05500	0.32940	0.12960	0.34125	2.50740	0.60000	1.24365
sg13g2_buf_1	A->X (RR)	0.01860	0.00100	0.04891	0.32940	0.06480	0.31676	2.50740	0.30000	1.18820

Delay(ns) to X falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_buf_16	A->X (FF)	0.01860	0.00100	0.06195	0.32940	1.03680	0.33847	2.50740	4.80000	1.16585
sg13g2_buf_8	A->X (FF)	0.01860	0.00100	0.06110	0.32940	0.51840	0.33759	2.50740	2.40000	1.16633
sg13g2_buf_4	A->X (FF)	0.01860	0.00100	0.05989	0.32940	0.25920	0.33216	2.50740	1.20000	1.10525
sg13g2_buf_2	A->X (FF)	0.01860	0.00100	0.05876	0.32940	0.12960	0.32619	2.50740	0.60000	1.12863
sg13g2_buf_1	A->X (FF)	0.01860	0.00100	0.05187	0.32940	0.06480	0.29887	2.50740	0.30000	1.07073

Power Information

Internal switching power(pJ) to X rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_buf_16	A	0.01860	0.00100	0.09421	0.32940	1.03680	0.10488	2.50740	4.80000	0.21472
sg13g2_buf_8	A	0.01860	0.00100	0.04565	0.32940	0.51840	0.05067	2.50740	2.40000	0.10496
sg13g2_buf_4	A	0.01860	0.00100	0.02240	0.32940	0.25920	0.02376	2.50740	1.20000	0.05265
sg13g2_buf_2	A	0.01860	0.00100	0.01186	0.32940	0.12960	0.01319	2.50740	0.60000	0.02902
sg13g2_buf_1	A	0.01860	0.00100	0.00691	0.32940	0.06480	0.00786	2.50740	0.30000	0.02136

Internal switching power(pJ) to X falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_buf_16	A	0.01860	0.00100	0.09037	0.32940	1.03680	0.10009	2.50740	4.80000	0.20440
sg13g2_buf_8	A	0.01860	0.00100	0.04465	0.32940	0.51840	0.04942	2.50740	2.40000	0.10282
sg13g2_buf_4	A	0.01860	0.00100	0.02232	0.32940	0.25920	0.02442	2.50740	1.20000	0.04366
sg13g2_buf_2	A	0.01860	0.00100	0.01161	0.32940	0.12960	0.01326	2.50740	0.60000	0.02831
sg13g2_buf_1	A	0.01860	0.00100	0.00704	0.32940	0.06480	0.00820	2.50740	0.30000	0.02151

DECAP_x



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp
125.00*

Footprint

Cell Name	Area
sg13g2_decap_4	7.25760
sg13g2_decap_8	12.70080

Pin Capacitance Information Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_decap_4	425.41100	425.41100	425.41100
sg13g2_decap_8	850.81400	850.81400	850.81400

DFFRRx



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp
125.00*

Truth Table

INPUT			OUTPUT	
D	RESET_B	CLK	Q	Q_N
0	1	R	0	1
1	1	R	1	0
x	0	x	0	1
x	1	x	IQ	IQN

Footprint

Cell Name	Area
sg13g2_dfrbp_2	54.43200
sg13g2_dfrbp_1	47.17440

Pin Capacitance Information

Cell Name	Pin Cap(pf)			Max Cap(pf)	
	D	RESET_B	CLK	Q	Q_N
sg13g2_dfrbp_2	0.00133	0.00480	0.00270	0.60000	0.60000
sg13g2_dfrbp_1	0.00139	0.00533	0.00253	0.30000	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_dfrbp_2	4377.30000	5083.70000	5903.84000
sg13g2_dfrbp_1	3291.09000	3958.98000	4709.15000

Delay Information

Delay(ns) to Q rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dfrbp_2	CLK->Q (RR)	0.01860	0.00100	0.23079	0.32940	0.12960	0.49691	2.50740	0.60000	1.38407
sg13g2_dfrbp_1	CLK->Q (RR)	0.01860	0.00100	0.18107	0.32940	0.06480	0.44971	2.50740	0.30000	1.30963

Delay(ns) to Q falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dfrbp_2	CLK->Q (RF)	0.01860	0.00100	0.19969	0.32940	0.12960	0.44921	2.50740	0.60000	1.20990
	RESET_B->Q (FF)	0.01860	0.00100	0.27183	0.32940	0.12960	0.56093	2.50740	0.60000	1.50508
sg13g2_dfrbp_1	CLK->Q (RF)	0.01860	0.00100	0.17178	0.32940	0.06480	0.41868	2.50740	0.30000	1.15779
	RESET_B->Q (FF)	0.01860	0.00100	0.23560	0.32940	0.06480	0.52057	2.50740	0.30000	1.44935

Delay(ns) to Q_N rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dfrbp_2	CLK->Q_N (RR)	0.01860	0.00100	0.13272	0.32940	0.12960	0.43743	2.50740	0.60000	1.29002
	RESET_B->Q_N (FR)	0.01860	0.00100	0.20614	0.32940	0.12960	0.54736	2.50740	0.60000	1.58366
sg13g2_dfrbp_1	CLK->Q_N (RR)	0.01860	0.00100	0.13120	0.32940	0.06480	0.42285	2.50740	0.30000	1.25525
	RESET_B->Q_N (FR)	0.01860	0.00100	0.19543	0.32940	0.06480	0.52371	2.50740	0.30000	1.54741

Delay(ns) to Q_N falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dfrbp_2	CLK->Q_N (RF)	0.01860	0.00100	0.14828	0.32940	0.12960	0.46027	2.50740	0.60000	1.25612
sg13g2_dfrbp_1	CLK->Q_N (RF)	0.01860	0.00100	0.13522	0.32940	0.06480	0.42211	2.50740	0.30000	1.19483

Constraint Information

Constraints(ns) for D rising :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_dfrbp_2	hold	CLK (R)	0.01860	0.01860	-0.04646	1.26300	1.26300	-0.17809	2.50740	2.50740	-0.23317
	setup	CLK (R)	0.01860	0.01860	0.11737	1.26300	1.26300	0.24825	2.50740	2.50740	0.30696
sg13g2_dfrbp_1	hold	CLK (R)	0.01860	0.01860	-0.04890	1.26300	1.26300	-0.19158	2.50740	2.50740	-0.25678
	setup	CLK (R)	0.01860	0.01860	0.11248	1.26300	1.26300	0.25365	2.50740	2.50740	0.32762

Constraints(ns) for D falling :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_dfrbp_2	hold	CLK (R)	0.01860	0.01860	-0.02445	1.26300	1.26300	-0.15651	2.50740	2.50740	-0.25088
	setup	CLK (R)	0.01860	0.01860	0.11492	1.26300	1.26300	0.26174	2.50740	2.50740	0.36599
sg13g2_dfrbp_1	hold	CLK (R)	0.01860	0.01860	-0.02445	1.26300	1.26300	-0.15651	2.50740	2.50740	-0.25088
	setup	CLK (R)	0.01860	0.01860	0.10759	1.26300	1.26300	0.26444	2.50740	2.50740	0.38075

Constraints(ns) for RESET_B rising :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_dfrbp_2	recovery	CLK (R)	0.01860	0.01860	0.12470	1.26300	1.26300	0.27523	2.50740	2.50740	0.39551
	removal	CLK (R)	0.01860	0.01860	-0.10025	1.26300	1.26300	-0.25634	2.50740	2.50740	-0.37780
sg13g2_dfrbp_1	recovery	CLK (R)	0.01860	0.01860	0.11981	1.26300	1.26300	0.28333	2.50740	2.50740	0.42207
	removal	CLK (R)	0.01860	0.01860	-0.09292	1.26300	1.26300	-0.25904	2.50740	2.50740	-0.39255

Min Pulse Width (ns) for RESET_B:

Cell Name	High	Low
sg13g2_dfrbp_2	-	3.3435
sg13g2_dfrbp_1	-	3.3435

Min Pulse Width (ns) for CLK:

Cell Name	High	Low
sg13g2_dfrbp_2	3.3435	3.3435
sg13g2_dfrbp_1	3.3435	3.3435

Power Information

Internal switching power(pJ) to Q rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dfrbp_2	CLK	0.01860	0.00100	0.04657	0.32940	0.12960	0.16436	2.50740	0.60000	0.61967
sg13g2_dfrbp_1	CLK	0.01860	0.00100	0.03574	0.32940	0.06480	0.09526	2.50740	0.30000	0.33185

Internal switching power(pJ) to Q falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dfrbp_2	CLK	0.01860	0.00100	0.04605	0.32940	0.12960	0.16505	2.50740	0.60000	0.61572
	RESET_B	0.01860	0.00100	0.03559	0.32940	0.12960	0.15346	2.50740	0.60000	0.59375
sg13g2_dfrbp_1	CLK	0.01860	0.00100	0.03467	0.32940	0.06480	0.09451	2.50740	0.30000	0.32811
	RESET_B	0.01860	0.00100	0.02417	0.32940	0.06480	0.08289	2.50740	0.30000	0.30666

Internal switching power(pJ) to Q_N rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dfrbp_2	CLK	0.01860	0.00100	0.04608	0.32940	0.12960	0.16571	2.50740	0.60000	0.62018
	RESET_B	0.01860	0.00100	0.03562	0.32940	0.12960	0.15415	2.50740	0.60000	0.59398
sg13g2_dfrbp_1	CLK	0.01860	0.00100	0.03467	0.32940	0.06480	0.09484	2.50740	0.30000	0.33092
	RESET_B	0.01860	0.00100	0.02415	0.32940	0.06480	0.08342	2.50740	0.30000	0.30846

Internal switching power(pJ) to Q_N falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dfrbp_2	CLK	0.01860	0.00100	0.04659	0.32940	0.12960	0.16381	2.50740	0.60000	0.61328
sg13g2_dfrbp_1	CLK	0.01860	0.00100	0.03572	0.32940	0.06480	0.09496	2.50740	0.30000	0.32977

Passive power(pJ) for D rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dfrbp_2	0.01860	0.00195	0.32940	0.00245	2.50740	0.00982
sg13g2_dfrbp_1	0.01860	0.00202	0.32940	0.00250	2.50740	0.00985

Passive power(pJ) for D falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dfrbp_2	0.01860	0.00178	0.32940	0.00226	2.50740	0.00969
sg13g2_dfrbp_1	0.01860	0.00191	0.32940	0.00239	2.50740	0.00978

Passive power(pJ) for D rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dfrbp_2	CLK	0.01860	0.00195	0.32940	0.00245	2.50740	0.00982
	(!CLK * RESET_B)	0.01860	0.01388	0.32940	0.01437	2.50740	0.02282
	(!CLK * !RESET_B)	0.01860	-0.00030	0.32940	-0.00032	2.50740	-0.00032
sg13g2_dfrbp_1	CLK	0.01860	0.00202	0.32940	0.00250	2.50740	0.00985
	(!CLK * RESET_B)	0.01860	0.01199	0.32940	0.01255	2.50740	0.02107
	(!CLK * !RESET_B)	0.01860	-0.00017	0.32940	-0.00018	2.50740	-0.00018

Passive power(pJ) for D falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dfrbp_2	CLK	0.01860	0.00178	0.32940	0.00226	2.50740	0.00969
	(!CLK * RESET_B)	0.01860	0.01138	0.32940	0.01181	2.50740	0.02070
	(!CLK * !RESET_B)	0.01860	0.00030	0.32940	0.00032	2.50740	0.00032
sg13g2_dfrbp_1	CLK	0.01860	0.00191	0.32940	0.00239	2.50740	0.00978
	(!CLK * RESET_B)	0.01860	0.01030	0.32940	0.01076	2.50740	0.01954
	(!CLK * !RESET_B)	0.01860	0.00032	0.32940	0.00035	2.50740	0.00035

Passive power(pJ) for RESET_B rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dfrbp_2	0.01860	0.00409	0.32940	0.00426	2.50740	0.01098
sg13g2_dfrbp_1	0.01860	0.00462	0.32940	0.00477	2.50740	0.01145

Passive power(pJ) for RESET_B falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dfrbp_2	0.01860	0.01151	0.32940	0.01153	2.50740	0.02206
sg13g2_dfrbp_1	0.01860	0.00997	0.32940	0.00992	2.50740	0.02056

Passive power(pJ) for RESET_B rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dfrbp_2	(CLK * D * !Q * Q_N)	0.01860	0.00409	0.32940	0.00426	2.50740	0.01098
	(CLK * !D * !Q * Q_N)	0.01860	0.00115	0.32940	0.00111	2.50740	0.00111
	(!CLK * D * !Q * Q_N)	0.01860	0.01644	0.32940	0.01661	2.50740	0.02672
	(!CLK * !D * !Q * Q_N)	0.01860	0.00113	0.32940	0.00109	2.50740	0.00109
sg13g2_dfrbp_1	(CLK * D * !Q * Q_N)	0.01860	0.00462	0.32940	0.00477	2.50740	0.01145
	(CLK * !D * !Q * Q_N)	0.01860	0.00167	0.32940	0.00163	2.50740	0.00163
	(!CLK * D * !Q * Q_N)	0.01860	0.01504	0.32940	0.01519	2.50740	0.02544
	(!CLK * !D * !Q * Q_N)	0.01860	0.00170	0.32940	0.00165	2.50740	0.00165

Passive power(pJ) for RESET_B falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dfrbp_2	(CLK * D * !Q * Q_N)	0.01860	0.04654	0.32940	0.04704	2.50740	0.06792
	(CLK * !D * !Q * Q_N)	0.01860	-0.00060	0.32940	-0.00080	2.50740	-0.00088
	(!CLK * D * !Q * Q_N)	0.01860	0.01151	0.32940	0.01153	2.50740	0.02206
	(!CLK * !D * !Q * Q_N)	0.01860	-0.00091	0.32940	-0.00107	2.50740	-0.00109
sg13g2_dfrbp_1	(CLK * D * !Q * Q_N)	0.01860	0.03322	0.32940	0.03368	2.50740	0.05414
	(CLK * !D * !Q * Q_N)	0.01860	-0.00109	0.32940	-0.00130	2.50740	-0.00138
	(!CLK * D * !Q * Q_N)	0.01860	0.00997	0.32940	0.00992	2.50740	0.02056
	(!CLK * !D * !Q * Q_N)	0.01860	-0.00120	0.32940	-0.00138	2.50740	-0.00145

Passive power(pJ) for CLK rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dfrbp_2	0.01860	0.01281	0.32940	0.01397	2.50740	0.03466
sg13g2_dfrbp_1	0.01860	0.01242	0.32940	0.01346	2.50740	0.03274

Passive power(pJ) for CLK falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dfrbp_2	0.01860	0.02443	0.32940	0.02553	2.50740	0.04672
sg13g2_dfrbp_1	0.01860	0.02174	0.32940	0.02281	2.50740	0.04282

Passive power(pJ) for CLK rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dfrbp_2	(D * RESET_B * Q * !Q_N)	0.01860	0.01281	0.32940	0.01397	2.50740	0.03466
	(D * !RESET_B * !Q * Q_N)	0.01860	0.01338	0.32940	0.01460	2.50740	0.03514
	(!D * RESET_B * !Q * Q_N)	0.01860	0.01262	0.32940	0.01380	2.50740	0.03442
	(!D * !RESET_B * !Q * Q_N)	0.01860	0.01321	0.32940	0.01442	2.50740	0.03496
sg13g2_dfrbp_1	(D * RESET_B * Q * !Q_N)	0.01860	0.01288	0.32940	0.01389	2.50740	0.03323
	(D * !RESET_B * !Q * Q_N)	0.01860	0.01242	0.32940	0.01346	2.50740	0.03274
	(!D * RESET_B * !Q * Q_N)	0.01860	0.01224	0.32940	0.01332	2.50740	0.03257
	(!D * !RESET_B * !Q * Q_N)	0.01860	0.01222	0.32940	0.01325	2.50740	0.03252

Passive power(pJ) for CLK falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dfrbp_2	(D * RESET_B * Q * !Q_N)	0.01860	0.02607	0.32940	0.02717	2.50740	0.04837
	(D * RESET_B * !Q * Q_N)	0.01860	0.02443	0.32940	0.02553	2.50740	0.04672
	(D * !RESET_B * !Q * Q_N)	0.01860	0.01265	0.32940	0.01385	2.50740	0.03434
	(!D * RESET_B * Q * !Q_N)	0.01860	0.00432	0.32940	0.04689	2.50740	0.06726
	(!D * RESET_B * !Q * Q_N)	0.01860	0.01257	0.32940	0.01378	2.50740	0.03428
	(!D * !RESET_B * !Q * Q_N)	0.01860	0.01259	0.32940	0.01379	2.50740	0.03428
sg13g2_dfrbp_1	(D * RESET_B * Q * !Q_N)	0.01860	0.02383	0.32940	0.02490	2.50740	0.04491
	(D * RESET_B * !Q * Q_N)	0.01860	0.02174	0.32940	0.02281	2.50740	0.04282
	(D * !RESET_B * !Q * Q_N)	0.01860	0.01195	0.32940	0.01316	2.50740	0.03233
	(!D * RESET_B * Q * !Q_N)	0.01860	0.00398	0.32940	0.03578	2.50740	0.05485
	(!D * RESET_B * !Q * Q_N)	0.01860	0.01186	0.32940	0.01307	2.50740	0.03226
	(!D * !RESET_B * !Q * Q_N)	0.01860	0.01188	0.32940	0.01308	2.50740	0.03226

DLHQ



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT		OUTPUT
D	GATE	Q
x	0	IQ
0	1	0
1	1	1

Footprint

Cell Name	Area
sg13g2_dlhq_1	30.84480

Pin Capacitance Information

Cell Name	Pin Cap(pf)		Max Cap(pf)
	D	GATE	Q
sg13g2_dlhq_1	0.00208	0.00214	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_dlhq_1	2192.01000	2673.06000	3355.59000

Delay Information

Delay(ns) to Q rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlhq_1	D->Q (RR)	0.01860	0.00100	0.17111	0.32940	0.06480	0.43479	2.50740	0.30000	1.27469
	GATE->Q (RR)	0.01860	0.00100	0.14475	0.32940	0.06480	0.40968	2.50740	0.30000	1.21430

Delay(ns) to Q falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlhq_1	D->Q (FF)	0.01860	0.00100	0.15027	0.32940	0.06480	0.39326	2.50740	0.30000	1.13393
	GATE->Q (RF)	0.01860	0.00100	0.15342	0.32940	0.06480	0.39535	2.50740	0.30000	1.08918

Constraint Information

Constraints(ns) for D rising :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_dlhq_1	hold	GATE (F)	0.01860	0.01860	-0.09292	1.26300	1.26300	-0.18079	2.50740	2.50740	-0.20070
	setup	GATE (F)	0.01860	0.01860	0.10270	1.26300	1.26300	0.23476	2.50740	2.50740	0.29515

Constraints(ns) for D falling :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_dlhq_1	hold	GATE (F)	0.01860	0.01860	-0.03912	1.26300	1.26300	0.00270	2.50740	2.50740	0.04132
	setup	GATE (F)	0.01860	0.01860	0.04890	1.26300	1.26300	0.00540	2.50740	2.50740	-0.03247

Min Pulse Width (ns) for GATE:

Cell Name	High	Low
sg13g2_dlhq_1	3.3435	-

Power Information

Internal switching power(pJ) to Q rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlhq_1	D	0.01860	0.00100	0.01779	0.32940	0.06480	0.01803	2.50740	0.30000	0.01843
	GATE	0.01860	0.00100	0.01427	0.32940	0.06480	0.01444	2.50740	0.30000	0.01554

Internal switching power(pJ) to Q falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlhq_1	D	0.01860	0.00100	0.01816	0.32940	0.06480	0.01857	2.50740	0.30000	0.01882
	GATE	0.01860	0.00100	0.01552	0.32940	0.06480	0.01615	2.50740	0.30000	0.01620

Passive power(pJ) for D rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhq_1	0.01860	0.00405	0.32940	0.00496	2.50740	0.01899

Passive power(pJ) for D falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhq_1	0.01860	0.00446	0.32940	0.00538	2.50740	0.01917

Passive power(pJ) for D rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhq_1	(!GATE * Q)	0.01860	0.00488	0.32940	0.00569	2.50740	0.01968
	(!GATE * !Q)	0.01860	0.00405	0.32940	0.00496	2.50740	0.01899

Passive power(pJ) for D falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhq_1	(!GATE * Q)	0.01860	0.00403	0.32940	0.00505	2.50740	0.01889
	(!GATE * !Q)	0.01860	0.00446	0.32940	0.00538	2.50740	0.01917

Passive power(pJ) for GATE rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhq_1	0.01860	0.01025	0.32940	0.01137	2.50740	0.02915

Passive power(pJ) for GATE falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhq_1	0.01860	0.00395	0.32940	0.01931	2.50740	0.03724

Passive power(pJ) for GATE rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhq_1	(!D * !Q)	0.01860	0.01025	0.32940	0.01137	2.50740	0.02915

Passive power(pJ) for GATE falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhq_1	(!D * !Q)	0.01860	0.00395	0.32940	0.01931	2.50740	0.03724

DLHRQ



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT			OUTPUT
D	RESET_B	GATE	Q
x	0	x	0
x	1	0	IQ
0	1	1	0
1	1	1	1

Footprint

Cell Name	Area
sg13g2_dlhrq_1	27.21600

Pin Capacitance Information

Cell Name	Pin Cap(pf)			Max Cap(pf)
	D	RESET_B	GATE	Q
sg13g2_dlhrq_1	0.00194	0.00262	0.00205	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_dlhrq_1	2461.77000	2905.86000	3378.47000

Delay Information

Delay(ns) to Q rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlhrq_1	D->Q (RR)	0.01860	0.00100	0.17719	0.32940	0.06480	0.44489	2.50740	0.30000	1.28133
	GATE->Q (RR)	0.01860	0.00100	0.15769	0.32940	0.06480	0.42757	2.50740	0.30000	1.22894

Delay(ns) to Q falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlhrq_1	D->Q (FF)	0.01860	0.00100	0.15704	0.32940	0.06480	0.40056	2.50740	0.30000	1.14271
	GATE->Q (RF)	0.01860	0.00100	0.16068	0.32940	0.06480	0.40451	2.50740	0.30000	1.10179
	RESET_B->Q (FF)	0.01860	0.00100	0.06259	0.32940	0.06480	0.32716	2.50740	0.30000	1.14638

Constraint Information

Constraints(ns) for D rising :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_dlhrq_1	hold	GATE (F)	0.01860	0.01860	-0.07825	1.26300	1.26300	-0.16190	2.50740	2.50740	-0.17709
	setup	GATE (F)	0.01860	0.01860	0.09781	1.26300	1.26300	0.21587	2.50740	2.50740	0.26269

Constraints(ns) for D falling :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_dlhrq_1	hold	GATE (F)	0.01860	0.01860	-0.04157	1.26300	1.26300	0.00270	2.50740	2.50740	0.04132
	setup	GATE (F)	0.01860	0.01860	0.05624	1.26300	1.26300	0.00810	2.50740	2.50740	-0.03247

Constraints(ns) for RESET_B rising :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_dlhrq_1	recovery	GATE (F)	0.01860	0.01860	-0.00978	1.26300	1.26300	-0.10794	2.50740	2.50740	-0.17119
	removal	GATE (F)	0.01860	0.01860	0.02445	1.26300	1.26300	0.13762	2.50740	2.50740	0.20366

Min Pulse Width (ns) for RESET_B:

Cell Name	High	Low
sg13g2_dlhrq_1	-	3.3435

Min Pulse Width (ns) for GATE:

Cell Name	High	Low
sg13g2_dlhrq_1	3.3435	-

Power Information

Internal switching power(pJ) to Q rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlhrq_1	D	0.01860	0.00100	0.00312	0.32940	0.06480	0.00176	2.50740	0.30000	0.00144
	GATE	0.01860	0.00100	0.01465	0.32940	0.06480	0.01483	2.50740	0.30000	0.01631

Internal switching power(pJ) to Q falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlhrq_1	D	0.01860	0.00100	0.00713	0.32940	0.06480	-0.00176	2.50740	0.30000	-0.00144
	GATE	0.01860	0.00100	0.01438	0.32940	0.06480	0.01502	2.50740	0.30000	0.01486
	RESET_B	0.01860	0.00100	0.00836	0.32940	0.06480	0.00969	2.50740	0.30000	0.02582

Passive power(pJ) for D rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhrq_1	0.01860	0.01861	0.32940	0.02114	2.50740	0.03551

Passive power(pJ) for D falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhrq_1	0.01860	0.01371	0.32940	0.03052	2.50740	0.04463

Passive power(pJ) for D rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhrq_1	(!GATE * RESET_B * Q)	0.01860	0.00125	0.32940	0.00212	2.50740	0.01613
	!RESET_B	0.01860	0.01861	0.32940	0.02114	2.50740	0.03551

Passive power(pJ) for D falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhrq_1	(!GATE * RESET_B * Q)	0.01860	0.00536	0.32940	0.00639	2.50740	0.02021
	!RESET_B	0.01860	0.01371	0.32940	0.03052	2.50740	0.04463

Passive power(pJ) for RESET_B rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhrq_1	0.01860	-0.00012	0.32940	-0.00017	2.50740	-0.00016

Passive power(pJ) for RESET_B falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhrq_1	0.01860	0.00045	0.32940	0.00034	2.50740	0.00029

Passive power(pJ) for RESET_B rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhrq_1	(D * !GATE * !Q)	0.01860	-0.00012	0.32940	-0.00017	2.50740	-0.00016
	(!D * !GATE * !Q)	0.01860	-0.00012	0.32940	-0.00017	2.50740	-0.00016

Passive power(pJ) for RESET_B falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhrq_1	(D * !GATE * !Q)	0.01860	0.00045	0.32940	0.00034	2.50740	0.00029
	(!D * !GATE * !Q)	0.01860	0.00045	0.32940	0.00034	2.50740	0.00029

Passive power(pJ) for GATE rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhrq_1	0.01860	0.01036	0.32940	0.01143	2.50740	0.02918

Passive power(pJ) for GATE falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhrq_1	0.01860	0.00391	0.32940	0.01908	2.50740	0.03702

Passive power(pJ) for GATE rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhrq_1	(D * !RESET_B * !Q)	0.01860	0.01337	0.32940	0.01442	2.50740	0.03329
	(!D * !RESET_B * !Q)	0.01860	0.01036	0.32940	0.01143	2.50740	0.02918

Passive power(pJ) for GATE falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhrq_1	(D * !RESET_B * !Q)	0.01860	0.01386	0.32940	0.01516	2.50740	0.03442
	(!D * RESET_B * !Q)	0.01860	0.00391	0.32940	0.01908	2.50740	0.03702
	(!D * !RESET_B * !Q)	0.01860	0.00398	0.32940	0.01915	2.50740	0.03709

DLHR



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT			OUTPUT	
D	RESET_B	GATE	Q	Q_N
x	0	x	0	1
x	1	0	IQ	IQN
0	1	1	0	1
1	1	1	1	0

Footprint

Cell Name	Area
sg13g2_dlhr_1	32.65920

Pin Capacitance Information

Cell Name	Pin Cap(pf)			Max Cap(pf)	
	D	RESET_B	GATE	Q	Q_N
sg13g2_dlhr_1	0.00196	0.00277	0.00212	0.30000	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_dlhr_1	3241.36000	3729.64000	4179.15000

Delay Information

Delay(ns) to Q rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlhr_1	D->Q (RR)	0.01860	0.00100	0.19188	0.32940	0.06480	0.46567	2.50740	0.30000	1.30151
	GATE->Q (RR)	0.01860	0.00100	0.17333	0.32940	0.06480	0.45038	2.50740	0.30000	1.25399

Delay(ns) to Q falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlhr_1	D->Q (FF)	0.01860	0.00100	0.16325	0.32940	0.06480	0.40974	2.50740	0.30000	1.14697
	GATE->Q (RF)	0.01860	0.00100	0.16687	0.32940	0.06480	0.41442	2.50740	0.30000	1.10719
	RESET_B->Q (FF)	0.01860	0.00100	0.06791	0.32940	0.06480	0.34375	2.50740	0.30000	1.17371

Delay(ns) to Q_N rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlhr_1	D->Q_N (FR)	0.01860	0.00100	0.20070	0.32940	0.06480	0.45919	2.50740	0.30000	1.29096
	GATE->Q_N (RR)	0.01860	0.00100	0.20452	0.32940	0.06480	0.46386	2.50740	0.30000	1.25039
	RESET_B->Q_N (FR)	0.01860	0.00100	0.10535	0.32940	0.06480	0.38753	2.50740	0.30000	1.26271

Delay(ns) to Q_N falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlhr_1	D->Q_N (RF)	0.01860	0.00100	0.23271	0.32940	0.06480	0.46690	2.50740	0.30000	1.20845
	GATE->Q_N (RF)	0.01860	0.00100	0.21439	0.32940	0.06480	0.45139	2.50740	0.30000	1.16090

Constraint Information

Constraints(ns) for D rising :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_dlhr_1	hold	GATE (F)	0.01860	0.01860	-0.08314	1.26300	1.26300	-0.16460	2.50740	2.50740	-0.18004
	setup	GATE (F)	0.01860	0.01860	0.10514	1.26300	1.26300	0.21587	2.50740	2.50740	0.26564

Constraints(ns) for D falling :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_dlhr_1	hold	GATE (F)	0.01860	0.01860	-0.04401	1.26300	1.26300	0.00270	2.50740	2.50740	0.04132
	setup	GATE (F)	0.01860	0.01860	0.06113	1.26300	1.26300	0.00810	2.50740	2.50740	-0.02952

Constraints(ns) for RESET_B rising :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_dlhr_1	recovery	GATE (F)	0.01860	0.01860	0.00000	1.26300	1.26300	-0.06746	2.50740	2.50740	-0.10921
	removal	GATE (F)	0.01860	0.01860	0.01956	1.26300	1.26300	0.09984	2.50740	2.50740	0.14463

Min Pulse Width (ns) for RESET_B:

Cell Name	High	Low
sg13g2_dlhr_1	-	3.3435

Min Pulse Width (ns) for GATE:

Cell Name	High	Low
sg13g2_dlhr_1	3.3435	-

Power Information

Internal switching power(pJ) to Q rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlhr_1	D	0.01860	0.00100	0.00657	0.32940	0.06480	0.00611	2.50740	0.30000	0.00778
	GATE	0.01860	0.00100	0.01215	0.32940	0.06480	0.01245	2.50740	0.30000	0.01361

Internal switching power(pJ) to Q falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlhr_1	D	0.01860	0.00100	0.00839	0.32940	0.06480	0.00075	2.50740	0.30000	0.00022
	GATE	0.01860	0.00100	0.01198	0.32940	0.06480	0.01234	2.50740	0.30000	0.01244
	RESET_B	0.01860	0.00100	0.00879	0.32940	0.06480	0.00946	2.50740	0.30000	0.01837

Internal switching power(pJ) to Q_N rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlhr_1	D	0.01860	0.00100	0.00841	0.32940	0.06480	0.00096	2.50740	0.30000	0.00151
	GATE	0.01860	0.00100	0.01695	0.32940	0.06480	0.01805	2.50740	0.30000	0.02777
	RESET_B	0.01860	0.00100	0.00881	0.32940	0.06480	0.00964	2.50740	0.30000	0.01900

Internal switching power(pJ) to Q_N falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlhr_1	D	0.01860	0.00100	0.00657	0.32940	0.06480	0.00594	2.50740	0.30000	0.00606
	GATE	0.01860	0.00100	0.01214	0.32940	0.06480	0.01230	2.50740	0.30000	0.01281

Passive power(pJ) for D rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhr_1	0.01860	0.01813	0.32940	0.02066	2.50740	0.03502

Passive power(pJ) for D falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhr_1	0.01860	0.01335	0.32940	0.03000	2.50740	0.04425

Passive power(pJ) for D rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhr_1	(!GATE * RESET_B * Q)	0.01860	0.00399	0.32940	0.00491	2.50740	0.01893
	!RESET_B	0.01860	0.01813	0.32940	0.02066	2.50740	0.03502

Passive power(pJ) for D falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhr_1	(!GATE * RESET_B * Q)	0.01860	0.00785	0.32940	0.00891	2.50740	0.02280
	!RESET_B	0.01860	0.01335	0.32940	0.03000	2.50740	0.04425

Passive power(pJ) for RESET_B rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhr_1	0.01860	-0.00027	0.32940	-0.00032	2.50740	-0.00032

Passive power(pJ) for RESET_B falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhr_1	0.01860	0.00058	0.32940	0.00048	2.50740	0.00043

Passive power(pJ) for RESET_B rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhr_1	(D * !GATE * !Q)	0.01860	-0.00027	0.32940	-0.00032	2.50740	-0.00032
	(!D * !GATE * !Q)	0.01860	-0.00027	0.32940	-0.00032	2.50740	-0.00032

Passive power(pJ) for RESET_B falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhr_1	(D * !GATE * !Q)	0.01860	0.00058	0.32940	0.00048	2.50740	0.00043
	(!D * !GATE * !Q)	0.01860	0.00058	0.32940	0.00048	2.50740	0.00043

Passive power(pJ) for GATE rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhr_1	0.01860	0.00991	0.32940	0.01102	2.50740	0.02877

Passive power(pJ) for GATE falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhr_1	0.01860	0.00398	0.32940	0.01873	2.50740	0.03675

Passive power(pJ) for GATE rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhr_1	(D * !RESET_B * !Q)	0.01860	0.01293	0.32940	0.01397	2.50740	0.03289
	(!D * !RESET_B * !Q)	0.01860	0.00991	0.32940	0.01102	2.50740	0.02877

Passive power(pJ) for GATE falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dlhr_1	(D * !RESET_B * !Q)	0.01860	0.01424	0.32940	0.01557	2.50740	0.03484
	(!D * RESET_B * !Q)	0.01860	0.00398	0.32940	0.01873	2.50740	0.03675
	(!D * !RESET_B * !Q)	0.01860	0.00404	0.32940	0.01880	2.50740	0.03681

DLLRQ



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT			OUTPUT
D	RESET_B	GATE_N	Q
x	0	x	0
0	1	0	0
x	1	1	IQ
1	1	0	1

Footprint

Cell Name	Area
sg13g2_dllrq_1	29.03040

Pin Capacitance Information

Cell Name	Pin Cap(pf)			Max Cap(pf)
	D	RESET_B	GATE_N	Q
sg13g2_dllrq_1	0.00192	0.00264	0.00205	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_dllrq_1	2319.78000	2868.89000	3378.60000

Delay Information

Delay(ns) to Q rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dllrq_1	D->Q (RR)	0.01860	0.00100	0.17695	0.32940	0.06480	0.44378	2.50740	0.30000	1.27969
	GATE_N->Q (FR)	0.01860	0.00100	0.19534	0.32940	0.06480	0.47615	2.50740	0.30000	1.36861
	RESET_B->Q (RR)	0.01860	0.00100	0.07852	0.32940	0.06480	0.34558	2.50740	0.30000	1.23160

Delay(ns) to Q falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dllrq_1	D->Q (FF)	0.01860	0.00100	0.15641	0.32940	0.06480	0.39786	2.50740	0.30000	1.13517
	GATE_N->Q (FF)	0.01860	0.00100	0.14755	0.32940	0.06480	0.40831	2.50740	0.30000	1.23234
	RESET_B->Q (FF)	0.01860	0.00100	0.06338	0.32940	0.06480	0.32721	2.50740	0.30000	1.14311

Constraint Information

Constraints(ns) for D rising :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_dllrq_1	hold	GATE_N (R)	0.01860	0.01860	-0.07091	1.26300	1.26300	-0.08905	2.50740	2.50740	-0.12101
	setup	GATE_N (R)	0.01860	0.01860	0.08069	1.26300	1.26300	0.09984	2.50740	2.50740	0.13282

Constraints(ns) for D falling :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_dllrq_1	hold	GATE_N (R)	0.01860	0.01860	-0.08069	1.26300	1.26300	-0.22666	2.50740	2.50740	-0.30401
	setup	GATE_N (R)	0.01860	0.01860	0.09047	1.26300	1.26300	0.27523	2.50740	2.50740	0.39255

Constraints(ns) for RESET_B rising :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_dllrq_1	recovery	GATE_N (R)	0.01860	0.01860	-0.02690	1.26300	1.26300	-0.07016	2.50740	2.50740	-0.06493
	removal	GATE_N (R)	0.01860	0.01860	0.04401	1.26300	1.26300	0.08905	2.50740	2.50740	0.08264

Min Pulse Width (ns) for RESET_B:

Cell Name	High	Low
sg13g2_dllrq_1	-	3.3435

Min Pulse Width (ns) for GATE_N:

Cell Name	High	Low
sg13g2_dllrq_1	-	3.3435

Power Information

Internal switching power(pJ) to Q rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dllrq_1	D	0.01860	0.00100	0.00782	0.32940	0.06480	0.00832	2.50740	0.30000	0.00932
	GATE_N	0.01860	0.00100	0.02181	0.32940	0.06480	0.00837	2.50740	0.30000	0.00907
	RESET_B	0.01860	0.00100	0.01015	0.32940	0.06480	0.01078	2.50740	0.30000	0.02645

Internal switching power(pJ) to Q falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dllrq_1	D	0.01860	0.00100	0.01781	0.32940	0.06480	-0.00001	2.50740	0.30000	-0.00041
	GATE_N	0.01860	0.00100	0.02019	0.32940	0.06480	0.00678	2.50740	0.30000	0.00829
	RESET_B	0.01860	0.00100	0.00718	0.32940	0.06480	0.00853	2.50740	0.30000	0.02432

Passive power(pJ) for D rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dllrq_1	0.01860	0.01388	0.32940	0.01449	2.50740	0.02846

Passive power(pJ) for D falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dllrq_1	0.01860	0.00311	0.32940	0.02221	2.50740	0.03647

Passive power(pJ) for D rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dllrq_1	(GATE_N * RESET_B * Q)	0.01860	0.00121	0.32940	0.00214	2.50740	0.01615
	!RESET_B	0.01860	0.01388	0.32940	0.01449	2.50740	0.02846

Passive power(pJ) for D falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dllrq_1	(GATE_N * RESET_B * Q)	0.01860	0.00659	0.32940	0.00763	2.50740	0.02151
	!RESET_B	0.01860	0.00311	0.32940	0.02221	2.50740	0.03647

Passive power(pJ) for RESET_B rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dllrq_1	0.01860	0.00130	0.32940	0.00125	2.50740	0.00126

Passive power(pJ) for RESET_B falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dllrq_1	0.01860	0.00187	0.32940	0.00176	2.50740	0.00171

Passive power(pJ) for RESET_B rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dllrq_1	(D * GATE_N * !Q)	0.01860	-0.00012	0.32940	-0.00017	2.50740	-0.00016
	(!D * GATE_N * !Q)	0.01860	0.00130	0.32940	0.00125	2.50740	0.00126

Passive power(pJ) for RESET_B falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dllrq_1	(D * GATE_N * !Q)	0.01860	0.00045	0.32940	0.00034	2.50740	0.00029
	(!D * GATE_N * !Q)	0.01860	0.00187	0.32940	0.00176	2.50740	0.00171

Passive power(pJ) for GATE_N rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dllrq_1	0.01860	0.00998	0.32940	0.01109	2.50740	0.02883

Passive power(pJ) for GATE_N falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dllrq_1	0.01860	0.00389	0.32940	0.01887	2.50740	0.03681

Passive power(pJ) for GATE_N rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dllrq_1	(D * !RESET_B * !Q)	0.01860	0.01538	0.32940	0.01641	2.50740	0.03385
	(!D * !RESET_B * !Q)	0.01860	0.00998	0.32940	0.01109	2.50740	0.02883

Passive power(pJ) for GATE_N falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dllrq_1	(D * !RESET_B * !Q)	0.01860	0.01416	0.32940	0.01533	2.50740	0.03314
	(!D * RESET_B * !Q)	0.01860	0.00389	0.32940	0.01887	2.50740	0.03681
	(!D * !RESET_B * !Q)	0.01860	0.00396	0.32940	0.01894	2.50740	0.03688

DLLR



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT			OUTPUT	
D	RESET_B	GATE_N	Q	Q_N
x	0	x	0	1
0	1	0	0	1
x	1	1	IQ	IQN
1	1	0	1	0

Footprint

Cell Name	Area
sg13g2_dllr_1	34.47360

Pin Capacitance Information

Cell Name	Pin Cap(pf)			Max Cap(pf)	
	D	RESET_B	GATE_N	Q	Q_N
sg13g2_dllr_1	0.00197	0.00277	0.00212	0.30000	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_dllr_1	3098.94000	3804.90000	4197.66000

Delay Information

Delay(ns) to Q rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dllr_1	D->Q (RR)	0.01860	0.00100	0.19430	0.32940	0.06480	0.46796	2.50740	0.30000	1.30469
	GATE_N->Q (FR)	0.01860	0.00100	0.21304	0.32940	0.06480	0.50176	2.50740	0.30000	1.39549

Delay(ns) to Q falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dllr_1	D->Q (FF)	0.01860	0.00100	0.16524	0.32940	0.06480	0.41158	2.50740	0.30000	1.14897
	GATE_N->Q (FF)	0.01860	0.00100	0.15727	0.32940	0.06480	0.42407	2.50740	0.30000	1.25190
	RESET_B->Q (FF)	0.01860	0.00100	0.06786	0.32940	0.06480	0.34834	2.50740	0.30000	1.15557

Delay(ns) to Q_N rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dllr_1	D->Q_N (FR)	0.01860	0.00100	0.20259	0.32940	0.06480	0.46076	2.50740	0.30000	1.29179
	GATE_N->Q_N (FR)	0.01860	0.00100	0.19476	0.32940	0.06480	0.47390	2.50740	0.30000	1.39351
	RESET_B->Q_N (FR)	0.01860	0.00100	0.10606	0.32940	0.06480	0.38925	2.50740	0.30000	1.27103

Delay(ns) to Q_N falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dllr_1	D->Q_N (RF)	0.01860	0.00100	0.23494	0.32940	0.06480	0.46923	2.50740	0.30000	1.21109
	GATE_N->Q_N (FF)	0.01860	0.00100	0.25395	0.32940	0.06480	0.50287	2.50740	0.30000	1.30344

Constraint Information

Constraints(ns) for D rising :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_dllr_1	hold	GATE_N (R)	0.01860	0.01860	-0.08069	1.26300	1.26300	-0.09444	2.50740	2.50740	-0.12692
	setup	GATE_N (R)	0.01860	0.01860	0.09292	1.26300	1.26300	0.10794	2.50740	2.50740	0.13872

Constraints(ns) for D falling :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_dllr_1	hold	GATE_N (R)	0.01860	0.01860	-0.08314	1.26300	1.26300	-0.22936	2.50740	2.50740	-0.30696
	setup	GATE_N (R)	0.01860	0.01860	0.09781	1.26300	1.26300	0.28333	2.50740	2.50740	0.40141

Constraints(ns) for RESET_B rising :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_dllr_1	recovery	GATE_N (R)	0.01860	0.01860	-0.01467	1.26300	1.26300	-0.03238	2.50740	2.50740	-0.00295
	removal	GATE_N (R)	0.01860	0.01860	0.03912	1.26300	1.26300	0.05397	2.50740	2.50740	0.02361

Min Pulse Width (ns) for RESET_B:

Cell Name	High	Low
sg13g2_dllr_1	-	3.3435

Min Pulse Width (ns) for GATE_N:

Cell Name	High	Low
sg13g2_dllr_1	-	3.3435

Power Information

Internal switching power(pJ) to Q rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dllr_1	D	0.01860	0.00100	0.01269	0.32940	0.06480	0.07081	2.50740	0.30000	0.28682
	GATE_N	0.01860	0.00100	0.02704	0.32940	0.06480	0.08592	2.50740	0.30000	0.30330

Internal switching power(pJ) to Q falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dllr_1	D	0.01860	0.00100	0.01770	0.32940	0.06480	0.05901	2.50740	0.30000	0.27261
	GATE_N	0.01860	0.00100	0.02479	0.32940	0.06480	0.08327	2.50740	0.30000	0.29967
	RESET_B	0.01860	0.00100	0.02676	0.32940	0.06480	0.08565	2.50740	0.30000	0.31442

Internal switching power(pJ) to Q_N rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dllr_1	D	0.01860	0.00100	0.01775	0.32940	0.06480	0.05939	2.50740	0.30000	0.27565
	GATE_N	0.01860	0.00100	0.03458	0.32940	0.06480	0.09478	2.50740	0.30000	0.33072
	RESET_B	0.01860	0.00100	0.02821	0.32940	0.06480	0.08733	2.50740	0.30000	0.31724

Internal switching power(pJ) to Q_N falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dllr_1	D	0.01860	0.00100	0.01269	0.32940	0.06480	0.07046	2.50740	0.30000	0.28483
	GATE_N	0.01860	0.00100	0.02703	0.32940	0.06480	0.08555	2.50740	0.30000	0.30023

Passive power(pJ) for D rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dllr_1	0.01860	0.01999	0.32940	0.02159	2.50740	0.03601

Passive power(pJ) for D falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dllr_1	0.01860	0.01343	0.32940	0.03160	2.50740	0.04584

Passive power(pJ) for D rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dllr_1	(GATE_N * RESET_B * Q)	0.01860	0.00400	0.32940	0.00490	2.50740	0.01895
	!RESET_B	0.01860	0.01999	0.32940	0.02159	2.50740	0.03601

Passive power(pJ) for D falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dllr_1	(GATE_N * RESET_B * Q)	0.01860	0.00373	0.32940	0.00478	2.50740	0.01867
	!RESET_B	0.01860	0.01343	0.32940	0.03160	2.50740	0.04584

Passive power(pJ) for RESET_B rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dllr_1	0.01860	-0.00026	0.32940	-0.00032	2.50740	-0.00032

Passive power(pJ) for RESET_B falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dllr_1	0.01860	0.00201	0.32940	0.00190	2.50740	0.00186

Passive power(pJ) for RESET_B rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dllr_1	(D * GATE_N * !Q)	0.01860	0.00325	0.32940	0.00319	2.50740	0.00320
	(!D * GATE_N * !Q)	0.01860	-0.00026	0.32940	-0.00032	2.50740	-0.00032

Passive power(pJ) for RESET_B falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dllr_1	(D * GATE_N * !Q)	0.01860	0.00059	0.32940	0.00048	2.50740	0.00044
	(!D * GATE_N * !Q)	0.01860	0.00201	0.32940	0.00190	2.50740	0.00186

Passive power(pJ) for GATE_N rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dllr_1	0.01860	0.00276	0.32940	0.01912	2.50740	0.03682

Passive power(pJ) for GATE_N falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dllr_1	0.01860	0.00976	0.32940	0.01101	2.50740	0.02893

Passive power(pJ) for GATE_N rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dllr_1	(D * !RESET_B * !Q)	0.01860	0.01558	0.32940	0.01653	2.50740	0.03401
	(!D * RESET_B * !Q)	0.01860	0.00276	0.32940	0.01912	2.50740	0.03682
	(!D * !RESET_B * !Q)	0.01860	0.00425	0.32940	0.02059	2.50740	0.03831

Passive power(pJ) for GATE_N falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_dllr_1	(D * !RESET_B * !Q)	0.01860	0.01467	0.32940	0.01586	2.50740	0.03360
	(!D * !RESET_B * !Q)	0.01860	0.00976	0.32940	0.01101	2.50740	0.02893

DLY1



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT	OUTPUT
A	X
0	0
1	1

Footprint

Cell Name	Area
sg13g2_dlygate4sd1_1	16.32960

Pin Capacitance Information

Cell Name	Pin Cap(pf)	Max Cap(pf)
	A	X
sg13g2_dlygate4sd1_1	0.00128	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_dlygate4sd1_1	1250.82000	1439.19000	1627.55000

Delay Information

Delay(ns) to X rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlygate4sd1_1	A->X (RR)	0.01860	0.00100	0.11314	0.32940	0.06480	0.37613	2.50740	0.30000	1.15118

Delay(ns) to X falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlygate4sd1_1	A->X (FF)	0.01860	0.00100	0.12980	0.32940	0.06480	0.39594	2.50740	0.30000	1.25223

Power Information

Internal switching power(pJ) to X rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlygate4sd1_1	A	0.01860	0.00100	0.01514	0.32940	0.06480	0.01578	2.50740	0.30000	0.02525

Internal switching power(pJ) to X falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlygate4sd1_1	A	0.01860	0.00100	0.01439	0.32940	0.06480	0.01517	2.50740	0.30000	0.02464

DLY2



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT	OUTPUT
A	X
0	0
1	1

Footprint

Cell Name	Area
sg13g2_dlygate4sd2_1	16.32960

Pin Capacitance Information

Cell Name	Pin Cap(pf)	Max Cap(pf)
	A	X
sg13g2_dlygate4sd2_1	0.00129	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_dlygate4sd2_1	1270.94000	1459.31000	1647.68000

Delay Information

Delay(ns) to X rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlygate4sd2_1	A->X (RR)	0.01860	0.00100	0.16864	0.32940	0.06480	0.44321	2.50740	0.30000	1.26233

Delay(ns) to X falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlygate4sd2_1	A->X (FF)	0.01860	0.00100	0.18944	0.32940	0.06480	0.47624	2.50740	0.30000	1.37260

Power Information

Internal switching power(pJ) to X rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlygate4sd2_1	A	0.01860	0.00100	0.01801	0.32940	0.06480	0.01836	2.50740	0.30000	0.02656

Internal switching power(pJ) to X falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlygate4sd2_1	A	0.01860	0.00100	0.01742	0.32940	0.06480	0.01795	2.50740	0.30000	0.02602

DLY4



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT	OUTPUT
A	X
0	0
1	1

Footprint

Cell Name	Area
sg13g2_dlygate4sd3_1	16.32960

Pin Capacitance Information

Cell Name	Pin Cap(pf)	Max Cap(pf)
	A	X
sg13g2_dlygate4sd3_1	0.00126	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_dlygate4sd3_1	2554.51000	2742.91000	2931.30000

Delay Information

Delay(ns) to X rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlygate4sd3_1	A->X (RR)	0.01860	0.00100	0.37634	0.32940	0.06480	0.68669	2.50740	0.30000	1.59345

Delay(ns) to X falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlygate4sd3_1	A->X (FF)	0.01860	0.00100	0.39394	0.32940	0.06480	0.71945	2.50740	0.30000	1.70654

Power Information

Internal switching power(pJ) to X rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlygate4sd3_1	A	0.01860	0.00100	0.02623	0.32940	0.06480	0.02619	2.50740	0.30000	0.03295

Internal switching power(pJ) to X falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_dlygate4sd3_1	A	0.01860	0.00100	0.02598	0.32940	0.06480	0.02581	2.50740	0.30000	0.03253

EINVIN_x



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp
125.00*

Truth Table

INPUT		OUTPUT
A	TE_B	Z
0	0	1
1	0	0
-	1	HiZ

Footprint

Cell Name	Area
sg13g2_einvn_4	23.58720
sg13g2_einvn_2	16.32960

Pin Capacitance Information

Cell Name	Pin Cap(pf)		Max Cap(pf)
	A	TE_B	Z
sg13g2_einvn_4	0.00753	0.00856	1.20000
sg13g2_einvn_2	0.00378	0.00448	0.60000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_einvn_4	1199.74000	2309.86000	3419.99000
sg13g2_einvn_2	594.23400	1149.30000	1704.37000

Delay Information

Delay(ns) to Z rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_einvn_4	A->Z (FR)	0.01860	0.01041	0.02483	0.32940	0.26861	0.53312	2.50740	1.20941	2.80262
	TE_B->Z (RR)	0.01860	0.01041	0.05212	0.32940	0.26861	0.12289	2.50740	1.20941	0.25216
	TE_B->Z (FR)	0.01860	0.01041	0.03167	0.32940	0.26861	0.51705	2.50740	1.20941	2.58008
sg13g2_einvn_2	A->Z (FR)	0.01860	0.00583	0.02638	0.32940	0.13443	0.53290	2.50740	0.60483	2.80034
	TE_B->Z (RR)	0.01860	0.00583	0.05099	0.32940	0.13443	0.11966	2.50740	0.60483	0.25391
	TE_B->Z (FR)	0.01860	0.00583	0.03330	0.32940	0.13443	0.51728	2.50740	0.60483	2.57969

Delay(ns) to Z falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_einvn_4	A->Z (RF)	0.01860	0.01549	0.02274	0.32940	0.27369	0.43138	2.50740	1.21449	2.34581
sg13g2_einvn_2	A->Z (RF)	0.01860	0.00845	0.02406	0.32940	0.13705	0.43148	2.50740	0.60745	2.34565

Power Information

Internal switching power(pJ) to Z rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_einvn_4	A	0.01860	0.01041	0.01213	0.32940	0.26861	0.01271	2.50740	1.20941	0.02461
	TE_B	0.01860	0.01041	0.02660	0.32940	0.26861	0.01737	2.50740	1.20941	0.01323
sg13g2_einvn_2	A	0.01860	0.00583	0.00612	0.32940	0.13443	0.00636	2.50740	0.60483	0.01211
	TE_B	0.01860	0.00583	0.01323	0.32940	0.13443	0.00858	2.50740	0.60483	0.00646

Internal switching power(pJ) to Z falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_einvn_4	A	0.01860	0.01549	0.01146	0.32940	0.27369	0.01361	2.50740	1.21449	0.02250
sg13g2_einvn_2	A	0.01860	0.00845	0.00586	0.32940	0.13705	0.00681	2.50740	0.60745	0.01135

Passive power(pJ) for A rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_einvn_4	0.01860	0.00000	0.32940	0.00000	2.50740	0.00000
sg13g2_einvn_2	0.01860	0.00000	0.32940	0.00000	2.50740	0.00000

Passive power(pJ) for A falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_einvn_4	0.01860	0.00000	0.32940	0.00000	2.50740	0.00000
sg13g2_einvn_2	0.01860	0.00000	0.32940	0.00000	2.50740	0.00000

Passive power(pJ) for TE_B rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_einvn_4	0.01860	-0.00938	0.32940	-0.01431	2.50740	0.00453
sg13g2_einvn_2	0.01860	-0.00482	0.32940	-0.00640	2.50740	0.00347

Passive power(pJ) for TE_B falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_einvn_4	0.01860	0.00938	0.32940	0.01921	2.50740	0.03896
sg13g2_einvn_2	0.01860	0.00482	0.32940	0.00975	2.50740	0.01998

FILLx



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Footprint

Cell Name	Area
sg13g2_fill_1	1.81440
sg13g2_fill_2	3.62880
sg13g2_fill_4	7.25760
sg13g2_fill_8	14.51520

Pin Capacitance Information Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_fill_1	0.00000	0.00000	0.00000
sg13g2_fill_2	0.00000	0.00000	0.00000
sg13g2_fill_4	0.00000	0.00000	0.00000
sg13g2_fill_8	0.00000	0.00000	0.00000

GCLK



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT		OUTPUT
GATE	CLK	GCLK
x	0	0
x	1	GCLK

Footprint

Cell Name	Area
sg13g2_lgcp_1	27.21600

Pin Capacitance Information

Cell Name	Pin Cap(pf)		Max Cap(pf)
	GATE	CLK	GCLK
sg13g2_lgcp_1	0.00220	0.00490	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_lgcp_1	2605.69000	2864.00000	3045.11000

Delay Information

Delay(ns) to GCLK rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_lgcp_1	CLK->GCLK (RR)	0.01860	0.00100	0.07160	0.32940	0.06480	0.33753	2.50740	0.30000	1.21348

Delay(ns) to GCLK falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_lgcp_1	CLK->GCLK (FF)	0.01860	0.00100	0.06019	0.32940	0.06480	0.32046	2.50740	0.30000	1.12884

Constraint Information

Constraints(ns) for GATE rising :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_lgcp_1	hold	CLK (R)	0.01860	0.01860	-0.03610	1.26300	1.26300	-0.16460	2.50740	2.50740	-0.27027
	setup	CLK (R)	0.01860	0.01860	0.06038	1.26300	1.26300	0.22936	2.50740	2.50740	0.37370

Constraints(ns) for GATE falling :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_lgcp_1	hold	CLK (R)	0.01860	0.01860	-0.01697	1.26300	1.26300	-0.02429	2.50740	2.50740	-0.03982
	setup	CLK (R)	0.01860	0.01860	0.04621	1.26300	1.26300	0.06746	2.50740	2.50740	0.09656

Min Pulse Width (ns) for CLK:

Cell Name	High	Low
sg13g2_lgcp_1	3.3435	3.3435

Power Information

Internal switching power(pJ) to GCLK rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_lgcp_1	CLK	0.01860	0.00100	0.01273	0.32940	0.06480	0.01334	2.50740	0.30000	0.02626

Internal switching power(pJ) to GCLK falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_lgcp_1	CLK	0.01860	0.00100	0.00986	0.32940	0.06480	0.01113	2.50740	0.30000	0.02450

Passive power(pJ) for GATE rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_lgcp_1	0.01860	0.02071	0.32940	0.02330	2.50740	0.03748

Passive power(pJ) for GATE falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_lgcp_1	0.01860	0.01109	0.32940	0.03404	2.50740	0.04830

Passive power(pJ) for GATE rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_lgcp_1	!CLK	0.01860	0.02071	0.32940	0.02330	2.50740	0.03748

Passive power(pJ) for GATE falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_lgcp_1	!CLK	0.01860	0.01109	0.32940	0.03404	2.50740	0.04830

Passive power(pJ) for CLK rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_lgcp_1	0.01860	0.00600	0.32940	0.00708	2.50740	0.02484

Passive power(pJ) for CLK falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_lgcp_1	0.01860	0.00693	0.32940	0.00805	2.50740	0.02612

INx



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT	OUTPUT
A	Y
0	1
1	0

Footprint

Cell Name	Area
sg13g2_inv_16	34.47360
sg13g2_inv_8	18.14400
sg13g2_inv_4	10.88640
sg13g2_inv_2	7.25760
sg13g2_inv_1	5.44320

Pin Capacitance Information

Cell Name	Pin Cap(pf)	Max Cap(pf)
	A	Y
sg13g2_inv_16	0.04455	4.80000
sg13g2_inv_8	0.02168	2.40000
sg13g2_inv_4	0.01085	1.20000
sg13g2_inv_2	0.00542	0.60000
sg13g2_inv_1	0.00272	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_inv_16	3291.04000	7731.67000	12172.30000
sg13g2_inv_8	1645.53000	3865.84000	6086.14000
sg13g2_inv_4	822.76100	1932.92000	3043.07000
sg13g2_inv_2	411.38100	966.46100	1521.54000
sg13g2_inv_1	205.87300	483.32500	760.77700

Delay Information

Delay(ns) to Y rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_inv_16	A->Y (FR)	0.01860	0.00100	0.01662	0.32940	1.03680	0.35176	2.50740	4.80000	2.00414
sg13g2_inv_8	A->Y (FR)	0.01860	0.00100	0.01648	0.32940	0.51840	0.35125	2.50740	2.40000	2.00258
sg13g2_inv_4	A->Y (FR)	0.01860	0.00100	0.01691	0.32940	0.25920	0.35112	2.50740	1.20000	2.00206
sg13g2_inv_2	A->Y (FR)	0.01860	0.00100	0.01808	0.32940	0.12960	0.35046	2.50740	0.60000	1.99934
sg13g2_inv_1	A->Y (FR)	0.01860	0.00100	0.02069	0.32940	0.06480	0.35119	2.50740	0.30000	1.99977

Delay(ns) to Y falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_inv_16	A->Y (RF)	0.01860	0.00100	0.01595	0.32940	1.03680	0.32593	2.50740	4.80000	1.86274
sg13g2_inv_8	A->Y (RF)	0.01860	0.00100	0.01585	0.32940	0.51840	0.32629	2.50740	2.40000	1.86255
sg13g2_inv_4	A->Y (RF)	0.01860	0.00100	0.01624	0.32940	0.25920	0.32610	2.50740	1.20000	1.86207
sg13g2_inv_2	A->Y (RF)	0.01860	0.00100	0.01725	0.32940	0.12960	0.32455	2.50740	0.60000	1.85469
sg13g2_inv_1	A->Y (RF)	0.01860	0.00100	0.01977	0.32940	0.06480	0.32521	2.50740	0.30000	1.85545

Power Information

Internal switching power(pJ) to Y rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_inv_16	A	0.01860	0.00100	0.02728	0.32940	1.03680	0.03426	2.50740	4.80000	0.10347
sg13g2_inv_8	A	0.01860	0.00100	0.01303	0.32940	0.51840	0.01637	2.50740	2.40000	0.05014
sg13g2_inv_4	A	0.01860	0.00100	0.00655	0.32940	0.25920	0.00831	2.50740	1.20000	0.02513
sg13g2_inv_2	A	0.01860	0.00100	0.00335	0.32940	0.12960	0.00407	2.50740	0.60000	0.01290
sg13g2_inv_1	A	0.01860	0.00100	0.00200	0.32940	0.06480	0.00228	2.50740	0.30000	0.00664

Internal switching power(pJ) to Y falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_inv_16	A	0.01860	0.00100	0.02354	0.32940	1.03680	0.03154	2.50740	4.80000	0.09801
sg13g2_inv_8	A	0.01860	0.00100	0.01124	0.32940	0.51840	0.01574	2.50740	2.40000	0.04602
sg13g2_inv_4	A	0.01860	0.00100	0.00569	0.32940	0.25920	0.00775	2.50740	1.20000	0.02311
sg13g2_inv_2	A	0.01860	0.00100	0.00293	0.32940	0.12960	0.00378	2.50740	0.60000	0.01138
sg13g2_inv_1	A	0.01860	0.00100	0.00193	0.32940	0.06480	0.00226	2.50740	0.30000	0.00609

ITL



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT		OUTPUT
A	TE_B	Z
0	0	1
1	0	0
-	1	HiZ

Footprint

Cell Name	Area
sg13g2_einvn_8	39.84120

Pin Capacitance Information

Cell Name	Pin Cap(pf)		Max Cap(pf)
	A	TE_B	Z
sg13g2_einvn_8	0.01498	0.01459	2.40000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_einvn_8	2193.62000	4413.89000	6634.15000

Delay Information

Delay(ns) to Z rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_einvn_8	A->Z (FR)	0.01860	0.01976	0.02416	0.32940	0.53716	0.53530	2.50740	2.41876	2.80912
	TE_B->Z (RR)	0.01860	0.01976	0.06829	0.32940	0.53716	0.16252	2.50740	2.41876	0.34787
	TE_B->Z (FR)	0.01860	0.01976	0.03237	0.32940	0.53716	0.51980	2.50740	2.41876	2.58816

Delay(ns) to Z falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_einvn_8	A->Z (RF)	0.01860	0.02987	0.02294	0.32940	0.54727	0.43271	2.50740	2.42887	2.35217

Power Information

Internal switching power(pJ) to Z rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_einvn_8	A	0.01860	0.01976	0.02388	0.32940	0.53716	0.02623	2.50740	2.41876	0.05232
	TE_B	0.01860	0.01976	0.05521	0.32940	0.53716	0.03610	2.50740	2.41876	0.02971

Internal switching power(pJ) to Z falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_einvn_8	A	0.01860	0.02987	0.02224	0.32940	0.54727	0.02702	2.50740	2.42887	0.04431

Passive power(pJ) for A rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_einvn_8	0.01860	0.00000	0.32940	0.00000	2.50740	0.00000

Passive power(pJ) for A falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_einvn_8	0.01860	0.00000	0.32940	0.00000	2.50740	0.00000

Passive power(pJ) for TE_B rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_einvn_8	0.01860	-0.01454	0.32940	-0.03302	2.50740	-0.02039

Passive power(pJ) for TE_B falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_einvn_8	0.01860	0.01454	0.32940	0.03302	2.50740	0.05177

KEEPSTATE



sg13g2_stdcell_slow_1p35V_125C Cell
Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage
1.35, Temp 125.00

Truth Table

INPUT	OUTPUT
SH	SH
x	-

Footprint

Cell Name	Area
sg13g2_sighold	9.07200

Pin Capacitance Information

Cell Name	Pin Cap(pf)	Max Cap(pf)
	SH	SH
sg13g2_sighold	0.00000	-

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_sighold	290.15900	312.05800	333.95700

Passive Power Information

Passive power(pJ) for SH rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_sighold	0.01860	0.00000	0.32940	0.00000	2.50740	0.00000

Passive power(pJ) for SH falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_sighold	0.01860	0.00000	0.32940	0.00000	2.50740	0.00000

MUX2



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT			OUTPUT
A0	A1	S	X
0	0	x	0
0	1	0	0
x	1	1	1
1	x	0	1
1	0	1	0

Footprint

Cell Name	Area
sg13g2_mux2_1	18.14400

Pin Capacitance Information

Cell Name	Pin Cap(pf)			Max Cap(pf)
	A0	A1	S	X
sg13g2_mux2_1	0.00185	0.00186	0.00481	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_mux2_1	1203.82000	1680.13000	2354.83000

Delay Information

Delay(ns) to X rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_mux2_1	A0->X (RR)	0.01860	0.00100	0.07667	0.32940	0.06480	0.35573	2.50740	0.30000	1.22822
	A1->X (RR)	0.01860	0.00100	0.05053	0.32940	0.06480	0.35697	2.50740	0.30000	1.24263
	S->X (-R)	0.01860	0.00100	0.08000	0.32940	0.06480	0.35466	2.50740	0.30000	1.23356

Delay(ns) to X falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_mux2_1	A0->X (FF)	0.01860	0.00100	0.05469	0.32940	0.06480	0.37490	2.50740	0.30000	1.26303
	A1->X (FF)	0.01860	0.00100	0.09481	0.32940	0.06480	0.38030	2.50740	0.30000	1.27527
	S->X (-F)	0.01860	0.00100	0.10630	0.32940	0.06480	0.36724	2.50740	0.30000	1.21397

Delay(ns) to X rising (conditional):

Cell Name	Timing Arc(Dir)	When	Delay(ns)								
			Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_mux2_1	S->X (RR)	(!A0 * A1)	0.01860	0.00100	0.08000	0.32940	0.06480	0.35466	2.50740	0.30000	1.23356
	S->X (FR)	(A0 * !A1)	0.01860	0.00100	0.11558	0.32940	0.06480	0.38557	2.50740	0.30000	1.22172

Delay(ns) to X falling (conditional):

Cell Name	Timing Arc(Dir)	When	Delay(ns)								
			Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_mux2_1	S->X (FF)	(!A0 * A1)	0.01860	0.00100	0.10630	0.32940	0.06480	0.36724	2.50740	0.30000	1.21397
	S->X (RF)	(A0 * !A1)	0.01860	0.00100	0.13784	0.32940	0.06480	0.39268	2.50740	0.30000	1.14737

Power Information

Internal switching power(pJ) to X rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_mux2_1	A0	0.01860	0.00100	0.01102	0.32940	0.06480	0.01149	2.50740	0.30000	0.02635
	A1	0.01860	0.00100	0.01064	0.32940	0.06480	0.01635	2.50740	0.30000	0.03174
	S	0.01860	0.00100	0.01074	0.32940	0.06480	0.01169	2.50740	0.30000	0.02553

Internal switching power(pJ) to X falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_mux2_1	A0	0.01860	0.00100	0.01046	0.32940	0.06480	0.01702	2.50740	0.30000	0.03278
	A1	0.01860	0.00100	0.01228	0.32940	0.06480	0.01323	2.50740	0.30000	0.02825
	S	0.01860	0.00100	0.01127	0.32940	0.06480	0.01210	2.50740	0.30000	0.02671

Internal switching power(pJ) to X rising (conditional):

Cell Name	Input	When	Power(pJ)								
			Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_mux2_1	S	(A0 * !A1)	0.01860	0.00100	0.01205	0.32940	0.06480	0.01229	2.50740	0.30000	0.01249
	S	(!A0 * A1)	0.01860	0.00100	0.01074	0.32940	0.06480	0.01169	2.50740	0.30000	0.02553

Internal switching power(pJ) to X falling (conditional):

Cell Name	Input	When	Power(pJ)								
			Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_mux2_1	S	(A0 * !A1)	0.01860	0.00100	0.01080	0.32940	0.06480	0.01107	2.50740	0.30000	0.01096
	S	(!A0 * A1)	0.01860	0.00100	0.01127	0.32940	0.06480	0.01210	2.50740	0.30000	0.02671

Passive power(pJ) for S rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_mux2_1	0.01860	0.00599	0.32940	0.00667	2.50740	0.02062

Passive power(pJ) for S falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_mux2_1	0.01860	0.00484	0.32940	0.00565	2.50740	0.01941

MUX4



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT						OUTPUT
A0	A1	A2	A3	S0	S1	X
0	0	0	0	x	x	0
0	x	0	1	0	x	0
x	0	x	1	1	0	0
x	x	x	1	1	1	1
0	0	1	x	x	0	0
0	x	1	x	0	1	1
0	x	1	0	1	1	0
0	1	0	x	0	x	0
0	1	x	x	1	0	1
0	1	x	0	1	1	0
0	1	1	x	0	0	0
1	0	0	x	0	0	1
1	x	0	0	x	1	0
1	0	x	0	1	x	0
1	x	0	1	0	1	0
1	x	1	x	0	x	1
1	1	0	x	x	0	1
1	1	1	x	1	0	1
1	1	1	0	1	1	0

Footprint

Cell Name	Area
sg13g2_mux4_1	38.10240

Pin Capacitance Information

Cell Name	Pin Cap(pf)						Max Cap(pf)
	A0	A1	A2	A3	S0	S1	X
sg13g2_mux4_1	0.00259	0.00259	0.00259	0.00260	0.00747	0.00462	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_mux4_1	1583.44000	3711.48000	5416.70000

Delay Information

Delay(ns) to X rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_mux4_1	A0->X (RR)	0.01860	0.00100	0.13770	0.32940	0.06480	0.43478	2.50740	0.30000	1.41278
	A1->X (RR)	0.01860	0.00100	0.13115	0.32940	0.06480	0.43292	2.50740	0.30000	1.40708
	A2->X (RR)	0.01860	0.00100	0.14053	0.32940	0.06480	0.44404	2.50740	0.30000	1.43261
	A3->X (RR)	0.01860	0.00100	0.13725	0.32940	0.06480	0.44227	2.50740	0.30000	1.43013
	S0->X (-R)	0.01860	0.00100	0.11405	0.32940	0.06480	0.43022	2.50740	0.30000	1.40678
	S1->X (-R)	0.01860	0.00100	-0.00291	0.32940	0.06480	0.34104	2.50740	0.30000	1.22594

Delay(ns) to X falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_mux4_1	A0->X (FF)	0.01860	0.00100	0.15615	0.32940	0.06480	0.44389	2.50740	0.30000	1.30422
	A1->X (FF)	0.01860	0.00100	0.15716	0.32940	0.06480	0.44360	2.50740	0.30000	1.30864
	A2->X (FF)	0.01860	0.00100	0.16674	0.32940	0.06480	0.45775	2.50740	0.30000	1.33414
	A3->X (FF)	0.01860	0.00100	0.16811	0.32940	0.06480	0.45732	2.50740	0.30000	1.33305
	S0->X (-F)	0.01860	0.00100	0.14021	0.32940	0.06480	0.44855	2.50740	0.30000	1.35624
	S1->X (-F)	0.01860	0.00100	0.03539	0.32940	0.06480	0.35458	2.50740	0.30000	1.19662

Delay(ns) to X rising (conditional):

Cell Name	Timing Arc(Dir)	When	Delay(ns)								
			Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_mux4_1	S0->X (RR)	(!A2 * A3 * S1)	0.01860	0.00100	0.11405	0.32940	0.06480	0.43022	2.50740	0.30000	1.40678
	S0->X (RR)	(!A0 * A1 * !S1)	0.01860	0.00100	0.10710	0.32940	0.06480	0.41589	2.50740	0.30000	1.37518
	S0->X (FR)	(A2 * !A3 * S1)	0.01860	0.00100	0.16923	0.32940	0.06480	0.46485	2.50740	0.30000	1.35267
	S0->X (FR)	(A0 * !A1 * !S1)	0.01860	0.00100	0.16373	0.32940	0.06480	0.45662	2.50740	0.30000	1.33962
	S1->X (RR)	(!A1 * A3 * S0)	0.01860	0.00100	-0.00632	0.32940	0.06480	0.33474	2.50740	0.30000	1.22541
	S1->X (RR)	(!A0 * A2 * !S0)	0.01860	0.00100	-0.00291	0.32940	0.06480	0.34104	2.50740	0.30000	1.22594
	S1->X (FR)	(A1 * !A3 * S0)	0.01860	0.00100	-0.00639	0.32940	0.06480	0.36308	2.50740	0.30000	1.20670
	S1->X (FR)	(A0 * !A2 * !S0)	0.01860	0.00100	-0.00377	0.32940	0.06480	0.36571	2.50740	0.30000	1.20661

Delay(ns) to X falling (conditional):

Cell Name	Timing Arc(Dir)	When	Delay(ns)								
			Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_mux4_1	S0->X (FF)	(!A2 * A3 * S1)	0.01860	0.00100	0.14021	0.32940	0.06480	0.44855	2.50740	0.30000	1.35624
	S0->X (FF)	(!A0 * A1 * !S1)	0.01860	0.00100	0.12689	0.32940	0.06480	0.42842	2.50740	0.30000	1.31935
	S0->X (RF)	(A2 * !A3 * S1)	0.01860	0.00100	0.18492	0.32940	0.06480	0.47350	2.50740	0.30000	1.27790
	S0->X (RF)	(A0 * !A1 * !S1)	0.01860	0.00100	0.17492	0.32940	0.06480	0.46016	2.50740	0.30000	1.25911
	S1->X (FF)	(!A1 * A3 * S0)	0.01860	0.00100	0.03539	0.32940	0.06480	0.35458	2.50740	0.30000	1.19662
	S1->X (FF)	(!A0 * A2 * !S0)	0.01860	0.00100	-0.00738	0.32940	0.06480	0.34440	2.50740	0.30000	1.19521
	S1->X (RF)	(A1 * !A3 * S0)	0.01860	0.00100	0.00116	0.32940	0.06480	0.37015	2.50740	0.30000	1.13221
	S1->X (RF)	(A0 * !A2 * !S0)	0.01860	0.00100	-0.00742	0.32940	0.06480	0.36482	2.50740	0.30000	1.13147

Power Information

Internal switching power(pJ) to X rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_mux4_1	A0	0.01860	0.00100	0.02140	0.32940	0.06480	0.02147	2.50740	0.30000	0.03356
	A1	0.01860	0.00100	0.01455	0.32940	0.06480	0.01455	2.50740	0.30000	0.02685
	A2	0.01860	0.00100	0.01554	0.32940	0.06480	0.01552	2.50740	0.30000	0.02703
	A3	0.01860	0.00100	0.01947	0.32940	0.06480	0.01947	2.50740	0.30000	0.03154
	S0	0.01860	0.00100	0.01071	0.32940	0.06480	0.01184	2.50740	0.30000	0.02739
	S1	0.01860	0.00100	0.01172	0.32940	0.06480	0.03245	2.50740	0.30000	0.04758

Internal switching power(pJ) to X falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_mux4_1	A0	0.01860	0.00100	0.02112	0.32940	0.06480	0.02110	2.50740	0.30000	0.03213
	A1	0.01860	0.00100	0.02000	0.32940	0.06480	0.01996	2.50740	0.30000	0.03133
	A2	0.01860	0.00100	0.02226	0.32940	0.06480	0.02217	2.50740	0.30000	0.03295
	A3	0.01860	0.00100	0.01646	0.32940	0.06480	0.01634	2.50740	0.30000	0.02850
	S0	0.01860	0.00100	0.01827	0.32940	0.06480	0.02338	2.50740	0.30000	0.01187
	S1	0.01860	0.00100	0.01286	0.32940	0.06480	0.03396	2.50740	0.30000	0.04987

Internal switching power(pJ) to X rising (conditional):

Cell Name	Input	When	Power(pJ)								
			Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_mux4_1	S0	(A2 * !A3 * S1)	0.01860	0.00100	0.02186	0.32940	0.06480	0.01286	2.50740	0.30000	0.00059
	S0	(A0 * !A1 * !S1)	0.01860	0.00100	0.02179	0.32940	0.06480	0.01292	2.50740	0.30000	0.00075
	S0	(!A2 * A3 * S1)	0.01860	0.00100	0.01063	0.32940	0.06480	0.01205	2.50740	0.30000	0.02632
	S0	(!A0 * A1 * !S1)	0.01860	0.00100	0.01071	0.32940	0.06480	0.01184	2.50740	0.30000	0.02739
	S1	(A1 * !A3 * S0)	0.01860	0.00100	0.01201	0.32940	0.06480	0.04226	2.50740	0.30000	0.05355
	S1	(A0 * !A2 * !S0)	0.01860	0.00100	0.01384	0.32940	0.06480	0.03866	2.50740	0.30000	0.04897
	S1	(!A1 * A3 * S0)	0.01860	0.00100	0.01172	0.32940	0.06480	0.03245	2.50740	0.30000	0.04758
	S1	(!A0 * A2 * !S0)	0.01860	0.00100	0.01320	0.32940	0.06480	0.02980	2.50740	0.30000	0.04362

Internal switching power(pJ) to X falling (conditional):

Cell Name	Input	When	Power(pJ)								
			Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_mux4_1	S0	(A2 * !A3 * S1)	0.01860	0.00100	0.01827	0.32940	0.06480	0.02338	2.50740	0.30000	0.01187
	S0	(A0 * !A1 * !S1)	0.01860	0.00100	0.01771	0.32940	0.06480	0.02435	2.50740	0.30000	0.01231
	S0	(!A2 * A3 * S1)	0.01860	0.00100	0.01222	0.32940	0.06480	0.00569	2.50740	0.30000	0.01859
	S0	(!A0 * A1 * !S1)	0.01860	0.00100	0.01215	0.32940	0.06480	0.00556	2.50740	0.30000	0.02016
	S1	(A1 * !A3 * S0)	0.01860	0.00100	0.01573	0.32940	0.06480	0.03127	2.50740	0.30000	0.04048
	S1	(A0 * !A2 * !S0)	0.01860	0.00100	0.01401	0.32940	0.06480	0.04238	2.50740	0.30000	0.05321
	S1	(!A1 * A3 * S0)	0.01860	0.00100	0.01417	0.32940	0.06480	0.02502	2.50740	0.30000	0.03870
	S1	(!A0 * A2 * !S0)	0.01860	0.00100	0.01286	0.32940	0.06480	0.03396	2.50740	0.30000	0.04987

Passive power(pJ) for S0 rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_mux4_1	0.01860	0.00937	0.32940	0.01126	2.50740	0.04232

Passive power(pJ) for S0 falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_mux4_1	0.01860	0.00759	0.32940	0.01740	2.50740	0.04834

Passive power(pJ) for S0 rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_mux4_1	(A2 * A3 * S1)	0.01860	0.00876	0.32940	0.01087	2.50740	0.04235
	(A0 * A1 * !S1)	0.01860	0.00937	0.32940	0.01126	2.50740	0.04232
	(!A2 * !A3 * S1)	0.01860	0.00900	0.32940	0.01118	2.50740	0.04268
	(!A0 * !A1 * !S1)	0.01860	0.01023	0.32940	0.01222	2.50740	0.04329

Passive power(pJ) for S0 falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_mux4_1	(A2 * A3 * S1)	0.01860	0.00690	0.32940	0.01492	2.50740	0.04624
	(A0 * A1 * !S1)	0.01860	0.00759	0.32940	0.01740	2.50740	0.04834
	(!A2 * !A3 * S1)	0.01860	0.01437	0.32940	0.01499	2.50740	0.03189
	(!A0 * !A1 * !S1)	0.01860	0.01233	0.32940	0.02361	2.50740	0.04037

Passive power(pJ) for S1 rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_mux4_1	0.01860	0.00462	0.32940	0.00611	2.50740	0.02338

Passive power(pJ) for S1 falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_mux4_1	0.01860	0.00463	0.32940	0.00607	2.50740	0.02318

Passive power(pJ) for S1 rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_mux4_1	(A1 * A3 * S0)	0.01860	0.00343	0.32940	0.00475	2.50740	0.02205
	(A0 * A2 * !S0)	0.01860	0.00343	0.32940	0.00474	2.50740	0.02205
	(!A1 * !A3 * S0)	0.01860	0.00462	0.32940	0.00611	2.50740	0.02338
	(!A0 * !A2 * !S0)	0.01860	0.00468	0.32940	0.00617	2.50740	0.02343

Passive power(pJ) for S1 falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_mux4_1	(A1 * A3 * S0)	0.01860	0.00344	0.32940	0.00508	2.50740	0.02219
	(A0 * A2 * !S0)	0.01860	0.00343	0.32940	0.00508	2.50740	0.02219
	(!A1 * !A3 * S0)	0.01860	0.00463	0.32940	0.00607	2.50740	0.02318
	(!A0 * !A2 * !S0)	0.01860	0.00468	0.32940	0.00613	2.50740	0.02324

NAND2B1



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp
125.00*

Truth Table

INPUT		OUTPUT
A_N	B	Y
x	0	1
0	1	0
1	1	1

Footprint

Cell Name	Area
sg13g2_nand2b_1	9.07200

Pin Capacitance Information

Cell Name	Pin Cap(pf)		Max Cap(pf)
	A_N	B	Y
sg13g2_nand2b_1	0.00218	0.00293	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_nand2b_1	330.12500	860.13100	1660.52000

Delay Information

Delay(ns) to Y rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_nand2b_1	A_N->Y (RR)	0.01860	0.00100	0.05139	0.32940	0.06480	0.31899	2.50740	0.30000	1.19209
	B->Y (FR)	0.01860	0.00100	0.02567	0.32940	0.06480	0.35744	2.50740	0.30000	2.00775

Delay(ns) to Y falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_nand2b_1	A_N->Y (FF)	0.01860	0.00100	0.06171	0.32940	0.06480	0.41592	2.50740	0.30000	1.58989
	B->Y (RF)	0.01860	0.00100	0.03744	0.32940	0.06480	0.42398	2.50740	0.30000	2.20242

Power Information

Internal switching power(pJ) to Y rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_nand2b_1	A_N	0.01860	0.00100	0.00269	0.32940	0.06480	0.00269	2.50740	0.30000	0.00198
	B	0.01860	0.00100	0.00250	0.32940	0.06480	0.00251	2.50740	0.30000	0.00632

Internal switching power(pJ) to Y falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_nand2b_1	A_N	0.01860	0.00100	0.00526	0.32940	0.06480	0.00535	2.50740	0.30000	0.00521
	B	0.01860	0.00100	0.00521	0.32940	0.06480	0.00521	2.50740	0.30000	0.00700

Passive power(pJ) for A_N rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_nand2b_1	0.01860	0.00473	0.32940	0.00574	2.50740	0.01993

Passive power(pJ) for A_N falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_nand2b_1	0.01860	0.00261	0.32940	0.00363	2.50740	0.01751

Passive power(pJ) for A_N rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_nand2b_1	!B	0.01860	0.00473	0.32940	0.00574	2.50740	0.01993

Passive power(pJ) for A_N falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_nand2b_1	!B	0.01860	0.00261	0.32940	0.00363	2.50740	0.01751

NAND2



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT		OUTPUT
A	B	Y
0	x	1
1	0	1
1	1	0

Footprint

Cell Name	Area
sg13g2_nand2_1	7.25760

Pin Capacitance Information

Cell Name	Pin Cap(pf)		Max Cap(pf)
	A	B	Y
sg13g2_nand2_1	0.00268	0.00278	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_nand2_1	191.13100	647.87200	1521.53000

Delay Information

Delay(ns) to Y rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_nand2_1	A->Y (FR)	0.01860	0.00100	0.02259	0.32940	0.06480	0.35303	2.50740	0.30000	2.00014
	B->Y (FR)	0.01860	0.00100	0.02611	0.32940	0.06480	0.35656	2.50740	0.30000	2.00534

Delay(ns) to Y falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_nand2_1	A->Y (RF)	0.01860	0.00100	0.02907	0.32940	0.06480	0.43095	2.50740	0.30000	2.33431
	B->Y (RF)	0.01860	0.00100	0.03380	0.32940	0.06480	0.42048	2.50740	0.30000	2.20200

Power Information

Internal switching power(pJ) to Y rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_nand2_1	A	0.01860	0.00100	0.00221	0.32940	0.06480	0.00245	2.50740	0.30000	0.00620
	B	0.01860	0.00100	0.00238	0.32940	0.06480	0.00235	2.50740	0.30000	0.00615

Internal switching power(pJ) to Y falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_nand2_1	A	0.01860	0.00100	0.00283	0.32940	0.06480	0.00304	2.50740	0.30000	0.00587
	B	0.01860	0.00100	0.00495	0.32940	0.06480	0.00493	2.50740	0.30000	0.00712

NAND3B1



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp
125.00*

Truth Table

INPUT			OUTPUT
A_N	B	C	Y
x	0	x	1
x	1	0	1
0	1	1	0
1	1	1	1

Footprint

Cell Name	Area
sg13g2_nand3b_1	12.70080

Pin Capacitance Information

Cell Name	Pin Cap(pf)			Max Cap(pf)
	A_N	B	C	Y
sg13g2_nand3b_1	0.00209	0.00278	0.00281	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_nand3b_1	221.57000	766.53900	2421.29000

Delay Information

Delay(ns) to Y rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_nand3b_1	A_N->Y (RR)	0.01860	0.00100	0.05418	0.32940	0.06480	0.31973	2.50740	0.30000	1.18909
	B->Y (FR)	0.01860	0.00100	0.02899	0.32940	0.06480	0.36075	2.50740	0.30000	2.00899
	C->Y (FR)	0.01860	0.00100	0.03169	0.32940	0.06480	0.36443	2.50740	0.30000	2.01315

Delay(ns) to Y falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_nand3b_1	A_N->Y (FF)	0.01860	0.00100	0.07472	0.32940	0.06480	0.53976	2.50740	0.30000	2.12694
	B->Y (RF)	0.01860	0.00100	0.05598	0.32940	0.06480	0.55061	2.50740	0.30000	2.72660
	C->Y (RF)	0.01860	0.00100	0.06190	0.32940	0.06480	0.54168	2.50740	0.30000	2.57671

Power Information

Internal switching power(pJ) to Y rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_nand3b_1	A_N	0.01860	0.00100	0.00391	0.32940	0.06480	0.00384	2.50740	0.30000	0.00314
	B	0.01860	0.00100	0.00293	0.32940	0.06480	0.00294	2.50740	0.30000	0.00615
	C	0.01860	0.00100	0.00337	0.32940	0.06480	0.00322	2.50740	0.30000	0.00637

Internal switching power(pJ) to Y falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_nand3b_1	A_N	0.01860	0.00100	0.01255	0.32940	0.06480	0.01259	2.50740	0.30000	0.01183
	B	0.01860	0.00100	0.00673	0.32940	0.06480	0.00661	2.50740	0.30000	0.00856
	C	0.01860	0.00100	0.00886	0.32940	0.06480	0.00875	2.50740	0.30000	0.01082

Passive power(pJ) for A_N rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_nand3b_1	0.01860	0.00361	0.32940	0.00463	2.50740	0.01881

Passive power(pJ) for A_N falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_nand3b_1	0.01860	-0.00310	0.32940	-0.00207	2.50740	0.01184

Passive power(pJ) for A_N rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_nand3b_1	(B * !C) + (!B)	0.01860	0.00361	0.32940	0.00463	2.50740	0.01881

Passive power(pJ) for A_N falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_nand3b_1	(B * !C) + (!B)	0.01860	-0.00310	0.32940	-0.00207	2.50740	0.01184

NOR2



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT		OUTPUT
A	B	Y
0	0	1
x	1	0
1	x	0

Footprint

Cell Name	Area
sg13g2_nor2_1	7.25760

Pin Capacitance Information

Cell Name	Pin Cap(pf)		Max Cap(pf)
	A	B	Y
sg13g2_nor2_1	0.00280	0.00268	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_nor2_1	407.92500	645.11200	982.68100

Delay Information

Delay(ns) to Y rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_nor2_1	A->Y (FR)	0.01860	0.00100	0.04174	0.32940	0.06480	0.52042	2.50740	0.30000	2.60100
	B->Y (FR)	0.01860	0.00100	0.03517	0.32940	0.06480	0.53738	2.50740	0.30000	2.79824

Delay(ns) to Y falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_nor2_1	A->Y (RF)	0.01860	0.00100	0.02465	0.32940	0.06480	0.33015	2.50740	0.30000	1.85917
	B->Y (RF)	0.01860	0.00100	0.02153	0.32940	0.06480	0.32633	2.50740	0.30000	1.85508

Power Information

Internal switching power(pJ) to Y rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_nor2_1	A	0.01860	0.00100	0.00544	0.32940	0.06480	0.00534	2.50740	0.30000	0.00811
	B	0.01860	0.00100	0.00277	0.32940	0.06480	0.00295	2.50740	0.30000	0.00583

Internal switching power(pJ) to Y falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_nor2_1	A	0.01860	0.00100	0.00246	0.32940	0.06480	0.00229	2.50740	0.30000	0.00555
	B	0.01860	0.00100	0.00223	0.32940	0.06480	0.00238	2.50740	0.30000	0.00552

NOR3



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT			OUTPUT
A	B	C	Y
0	0	0	1
0	x	1	0
x	1	x	0
1	x	x	0

Footprint

Cell Name	Area
sg13g2_nor3_1	9.07200

Pin Capacitance Information

Cell Name	Pin Cap(pf)			Max Cap(pf)
	A	B	C	Y
sg13g2_nor3_1	0.00278	0.00273	0.00264	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_nor3_1	381.28600	743.99100	1273.85000

Delay Information

Delay(ns) to Y rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_nor3_1	A->Y (FR)	0.01860	0.00100	0.07523	0.32940	0.06480	0.72567	2.50740	0.30000	3.30904
	B->Y (FR)	0.01860	0.00100	0.07036	0.32940	0.06480	0.73853	2.50740	0.30000	3.50973
	C->Y (FR)	0.01860	0.00100	0.05421	0.32940	0.06480	0.73770	2.50740	0.30000	3.62977

Delay(ns) to Y falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_nor3_1	A->Y (RF)	0.01860	0.00100	0.02791	0.32940	0.06480	0.33716	2.50740	0.30000	1.87005
	B->Y (RF)	0.01860	0.00100	0.02755	0.32940	0.06480	0.33385	2.50740	0.30000	1.86740
	C->Y (RF)	0.01860	0.00100	0.02384	0.32940	0.06480	0.32970	2.50740	0.30000	1.86017

Power Information

Internal switching power(pJ) to Y rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_nor3_1	A	0.01860	0.00100	0.00908	0.32940	0.06480	0.00891	2.50740	0.30000	0.01096
	B	0.01860	0.00100	0.00684	0.32940	0.06480	0.00667	2.50740	0.30000	0.00893
	C	0.01860	0.00100	0.00420	0.32940	0.06480	0.00431	2.50740	0.30000	0.00693

Internal switching power(pJ) to Y falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_nor3_1	A	0.01860	0.00100	0.00323	0.32940	0.06480	0.00301	2.50740	0.30000	0.00589
	B	0.01860	0.00100	0.00291	0.32940	0.06480	0.00275	2.50740	0.30000	0.00542
	C	0.01860	0.00100	0.00240	0.32940	0.06480	0.00263	2.50740	0.30000	0.00521

NOR4



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT				OUTPUT
A	B	C	D	Y
0	0	0	0	1
0	0	x	1	0
0	x	1	x	0
x	1	x	x	0
1	x	x	x	0

Footprint

Cell Name	Area
sg13g2_nor4_1	12.70080

Pin Capacitance Information

Cell Name	Pin Cap(pf)				Max Cap(pf)
	A	B	C	D	Y
sg13g2_nor4_1	0.00276	0.00270	0.00234	0.00241	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_nor4_1	389.18300	724.66100	1561.86000

Delay Information

Delay(ns) to Y rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_nor4_1	A->Y (FR)	0.01860	0.00100	0.11611	0.32940	0.06480	0.95262	2.50740	0.30000	4.12588
	B->Y (FR)	0.01860	0.00100	0.11170	0.32940	0.06480	0.95758	2.50740	0.30000	4.27098
	C->Y (FR)	0.01860	0.00100	0.09776	0.32940	0.06480	0.95564	2.50740	0.30000	4.41248
	D->Y (FR)	0.01860	0.00100	0.06994	0.32940	0.06480	0.94004	2.50740	0.30000	4.48754

Delay(ns) to Y falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_nor4_1	A->Y (RF)	0.01860	0.00100	0.02912	0.32940	0.06480	0.34228	2.50740	0.30000	1.87362
	B->Y (RF)	0.01860	0.00100	0.03012	0.32940	0.06480	0.33955	2.50740	0.30000	1.87536
	C->Y (RF)	0.01860	0.00100	0.02908	0.32940	0.06480	0.33569	2.50740	0.30000	1.86855
	D->Y (RF)	0.01860	0.00100	0.02500	0.32940	0.06480	0.33152	2.50740	0.30000	1.85706

Power Information

Internal switching power(pJ) to Y rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_nor4_1	A	0.01860	0.00100	0.01180	0.32940	0.06480	0.01159	2.50740	0.30000	0.01338
	B	0.01860	0.00100	0.00965	0.32940	0.06480	0.00942	2.50740	0.30000	0.01115
	C	0.01860	0.00100	0.00782	0.32940	0.06480	0.00760	2.50740	0.30000	0.00918
	D	0.01860	0.00100	0.00443	0.32940	0.06480	0.00447	2.50740	0.30000	0.00667

Internal switching power(pJ) to Y falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_nor4_1	A	0.01860	0.00100	0.00417	0.32940	0.06480	0.00408	2.50740	0.30000	0.00594
	B	0.01860	0.00100	0.00392	0.32940	0.06480	0.00373	2.50740	0.30000	0.00617
	C	0.01860	0.00100	0.00257	0.32940	0.06480	0.00247	2.50740	0.30000	0.00487
	D	0.01860	0.00100	0.00057	0.32940	0.06480	0.00093	2.50740	0.30000	0.00274

Passive power(pJ) for A rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_nor4_1	0.01860	-0.00006	0.32940	-0.00024	2.50740	-0.00025

Passive power(pJ) for A falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_nor4_1	0.01860	0.00023	0.32940	0.00024	2.50740	0.00025

Passive power(pJ) for A rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_nor4_1	(!B * C) + (!B * !C * D)	0.01860	-0.00006	0.32940	-0.00024	2.50740	-0.00025

Passive power(pJ) for A falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_nor4_1	(!B * C) + (!B * !C * D)	0.01860	0.00023	0.32940	0.00024	2.50740	0.00025

Passive power(pJ) for B rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_nor4_1	0.01860	-0.00005	0.32940	-0.00020	2.50740	-0.00020

Passive power(pJ) for B falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_nor4_1	0.01860	0.00018	0.32940	0.00020	2.50740	0.00020

Passive power(pJ) for B rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_nor4_1	(!A * C) + (!A * !C * D)	0.01860	-0.00005	0.32940	-0.00020	2.50740	-0.00020

Passive power(pJ) for B falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_nor4_1	(!A * C) + (!A * !C * D)	0.01860	0.00018	0.32940	0.00020	2.50740	0.00020

Passive power(pJ) for C rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_nor4_1	0.01860	0.00065	0.32940	0.00066	2.50740	0.00067

Passive power(pJ) for C falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_nor4_1	0.01860	-0.00022	0.32940	-0.00023	2.50740	-0.00022

Passive power(pJ) for C rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_nor4_1	$(A * !D) + (!A * B * !D)$	0.01860	0.00065	0.32940	0.00066	2.50740	0.00067

Passive power(pJ) for C falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_nor4_1	$(A * !D) + (!A * B * !D)$	0.01860	-0.00022	0.32940	-0.00023	2.50740	-0.00022

Passive power(pJ) for D rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_nor4_1	0.01860	0.00193	0.32940	0.00194	2.50740	0.00194

Passive power(pJ) for D falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_nor4_1	0.01860	0.00053	0.32940	0.00055	2.50740	0.00057

Passive power(pJ) for D rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_nor4_1	$(A * !C) + (!A * B * !C)$	0.01860	0.00193	0.32940	0.00194	2.50740	0.00194

Passive power(pJ) for D falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_nor4_1	(A * !C) + (!A * B * !C)	0.01860	0.00053	0.32940	0.00055	2.50740	0.00057

NP_ANT



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp
125.00*

Truth Table

INPUT
A
x

Footprint

Cell Name	Area
sg13g2_antennanp	5.44320

Pin Capacitance Information

Cell Name	Pin Cap(pf)
	A
sg13g2_antennanp	0.00111

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_antennanp	5.54763	5.54763	5.54763

Passive Power Information

Passive power(pJ) for A rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_antennanp	0.01860	-0.00041	0.32940	-0.00042	2.50740	-0.00042

Passive power(pJ) for A falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_antennanp	0.01860	0.00041	0.32940	0.00042	2.50740	0.00042

OR2



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT		OUTPUT
A	B	X
0	0	0
x	1	1
1	x	1

Footprint

Cell Name	Area
sg13g2_or2_1	10.88640

Pin Capacitance Information

Cell Name	Pin Cap(pf)		Max Cap(pf)
	A	B	X
sg13g2_or2_1	0.00212	0.00208	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_or2_1	509.17300	819.34300	1038.51000

Delay Information

Delay(ns) to X rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_or2_1	A->X (RR)	0.01860	0.00100	0.05582	0.32940	0.06480	0.33366	2.50740	0.30000	1.20905
	B->X (RR)	0.01860	0.00100	0.05128	0.32940	0.06480	0.31935	2.50740	0.30000	1.16232

Delay(ns) to X falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_or2_1	A->X (FF)	0.01860	0.00100	0.08941	0.32940	0.06480	0.34220	2.50740	0.30000	1.15722
	B->X (FF)	0.01860	0.00100	0.08319	0.32940	0.06480	0.35040	2.50740	0.30000	1.19769

Power Information

Internal switching power(pJ) to X rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_or2_1	A	0.01860	0.00100	0.00724	0.32940	0.06480	0.00799	2.50740	0.30000	0.01992
	B	0.01860	0.00100	0.00721	0.32940	0.06480	0.00789	2.50740	0.30000	0.02036

Internal switching power(pJ) to X falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_or2_1	A	0.01860	0.00100	0.00947	0.32940	0.06480	0.00989	2.50740	0.30000	0.02135
	B	0.01860	0.00100	0.00752	0.32940	0.06480	0.00838	2.50740	0.30000	0.02038

OR3



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT			OUTPUT
A	B	C	X
0	0	0	0
0	x	1	1
x	1	x	1
1	x	x	1

Footprint

Cell Name	Area
sg13g2_or3_1	12.70080

Pin Capacitance Information

Cell Name	Pin Cap(pf)			Max Cap(pf)
	A	B	C	X
sg13g2_or3_1	0.00233	0.00229	0.00222	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_or3_1	530.86200	880.61900	1337.96000

Delay Information

Delay(ns) to X rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_or3_1	A->X (RR)	0.01860	0.00100	0.06524	0.32940	0.06480	0.35861	2.50740	0.30000	1.28519
	B->X (RR)	0.01860	0.00100	0.06194	0.32940	0.06480	0.34731	2.50740	0.30000	1.23696
	C->X (RR)	0.01860	0.00100	0.05589	0.32940	0.06480	0.33139	2.50740	0.30000	1.18753

Delay(ns) to X falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_or3_1	A->X (FF)	0.01860	0.00100	0.12856	0.32940	0.06480	0.38489	2.50740	0.30000	1.18085
	B->X (FF)	0.01860	0.00100	0.12312	0.32940	0.06480	0.39115	2.50740	0.30000	1.23794
	C->X (FF)	0.01860	0.00100	0.10833	0.32940	0.06480	0.38817	2.50740	0.30000	1.25208

Power Information

Internal switching power(pJ) to X rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_or3_1	A	0.01860	0.00100	0.00799	0.32940	0.06480	0.00845	2.50740	0.30000	0.02131
	B	0.01860	0.00100	0.00758	0.32940	0.06480	0.00816	2.50740	0.30000	0.02027
	C	0.01860	0.00100	0.00732	0.32940	0.06480	0.00793	2.50740	0.30000	0.01974

Internal switching power(pJ) to X falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_or3_1	A	0.01860	0.00100	0.01337	0.32940	0.06480	0.01334	2.50740	0.30000	0.02486
	B	0.01860	0.00100	0.01137	0.32940	0.06480	0.01156	2.50740	0.30000	0.02373
	C	0.01860	0.00100	0.00910	0.32940	0.06480	0.00972	2.50740	0.30000	0.02274

OR4



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT				OUTPUT
A	B	C	D	X
0	0	0	0	0
0	0	x	1	1
0	x	1	x	1
x	1	x	x	1
1	x	x	x	1

Footprint

Cell Name	Area
sg13g2_or4_1	14.51520

Pin Capacitance Information

Cell Name	Pin Cap(pf)				Max Cap(pf)
	A	B	C	D	X
sg13g2_or4_1	0.00232	0.00226	0.00194	0.00201	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_or4_1	532.49900	866.55400	1594.51000

Delay Information

Delay(ns) to X rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_or4_1	A->X (RR)	0.01860	0.00100	0.06811	0.32940	0.06480	0.37050	2.50740	0.30000	1.31063
	B->X (RR)	0.01860	0.00100	0.06723	0.32940	0.06480	0.36192	2.50740	0.30000	1.26602
	C->X (RR)	0.01860	0.00100	0.06332	0.32940	0.06480	0.34952	2.50740	0.30000	1.22219
	D->X (RR)	0.01860	0.00100	0.05689	0.32940	0.06480	0.33466	2.50740	0.30000	1.17372

Delay(ns) to X falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_or4_1	A->X (FF)	0.01860	0.00100	0.17765	0.32940	0.06480	0.44719	2.50740	0.30000	1.23859
	B->X (FF)	0.01860	0.00100	0.17255	0.32940	0.06480	0.44836	2.50740	0.30000	1.30077
	C->X (FF)	0.01860	0.00100	0.15851	0.32940	0.06480	0.44313	2.50740	0.30000	1.34485
	D->X (FF)	0.01860	0.00100	0.13352	0.32940	0.06480	0.43102	2.50740	0.30000	1.34261

Power Information

Internal switching power(pJ) to X rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_or4_1	A	0.01860	0.00100	0.00889	0.32940	0.06480	0.00937	2.50740	0.30000	0.02205
	B	0.01860	0.00100	0.00842	0.32940	0.06480	0.00874	2.50740	0.30000	0.01944
	C	0.01860	0.00100	0.00730	0.32940	0.06480	0.00764	2.50740	0.30000	0.01860
	D	0.01860	0.00100	0.00573	0.32940	0.06480	0.00638	2.50740	0.30000	0.01713

Internal switching power(pJ) to X falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_or4_1	A	0.01860	0.00100	0.01485	0.32940	0.06480	0.01438	2.50740	0.30000	0.02374
	B	0.01860	0.00100	0.01383	0.32940	0.06480	0.01339	2.50740	0.30000	0.02285
	C	0.01860	0.00100	0.01200	0.32940	0.06480	0.01164	2.50740	0.30000	0.02235
	D	0.01860	0.00100	0.00861	0.32940	0.06480	0.00880	2.50740	0.30000	0.01867

Passive power(pJ) for A rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_or4_1	0.01860	-0.00019	0.32940	-0.00037	2.50740	-0.00041

Passive power(pJ) for A falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_or4_1	0.01860	0.00105	0.32940	0.00108	2.50740	0.00109

Passive power(pJ) for A rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_or4_1	(!B * C) + (!B * !C * D)	0.01860	-0.00019	0.32940	-0.00037	2.50740	-0.00041

Passive power(pJ) for A falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_or4_1	(!B * C) + (!B * !C * D)	0.01860	0.00105	0.32940	0.00108	2.50740	0.00109

Passive power(pJ) for B rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_or4_1	0.01860	-0.00012	0.32940	-0.00013	2.50740	-0.00013

Passive power(pJ) for B falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_or4_1	0.01860	0.00012	0.32940	0.00013	2.50740	0.00013

Passive power(pJ) for B rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_or4_1	(!A * C) + (!A * !C * D)	0.01860	-0.00012	0.32940	-0.00013	2.50740	-0.00013

Passive power(pJ) for B falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_or4_1	(!A * C) + (!A * !C * D)	0.01860	0.00012	0.32940	0.00013	2.50740	0.00013

Passive power(pJ) for C rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_or4_1	0.01860	0.00044	0.32940	0.00046	2.50740	0.00046

Passive power(pJ) for C falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_or4_1	0.01860	-0.00006	0.32940	-0.00006	2.50740	-0.00006

Passive power(pJ) for C rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_or4_1	(A * !D) + (!A * B * !D)	0.01860	0.00044	0.32940	0.00046	2.50740	0.00046

Passive power(pJ) for C falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_or4_1	(A * !D) + (!A * B * !D)	0.01860	-0.00006	0.32940	-0.00006	2.50740	-0.00006

Passive power(pJ) for D rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_or4_1	0.01860	0.00163	0.32940	0.00165	2.50740	0.00165

Passive power(pJ) for D falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_or4_1	0.01860	0.00105	0.32940	0.00106	2.50740	0.00108

Passive power(pJ) for D rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_or4_1	(A * !C) + (!A * B * !C)	0.01860	0.00163	0.32940	0.00165	2.50740	0.00165

Passive power(pJ) for D falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_or4_1	$(A * !C) + (!A * B * !C)$	0.01860	0.00105	0.32940	0.00106	2.50740	0.00108

SDFRRS



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp
125.00*

Truth Table

INPUT						OUTPUT	
D	SCD	SCE	RESET_B	SET_B	CLK	Q	Q_N
0	0	x	1	1	R	0	1
0	1	0	1	1	R	0	1
x	1	1	1	1	R	1	0
1	x	0	1	1	R	1	0
1	0	1	1	1	R	0	1
x	x	x	0	0	x	0	0
x	x	x	0	1	x	0	1
x	x	x	1	0	x	1	0
x	x	x	1	1	x	IQ	IQN

Footprint

Cell Name	Area
sg13g2_sdfbbp_1	63.50400

Pin Capacitance Information

Cell Name	Pin Cap(pf)						Max Cap(pf)	
	D	SCD	SCE	RESET_B	SET_B	CLK	Q	Q_N
sg13g2_sdfbbp_1	0.00164	0.00182	0.00317	0.00155	0.00476	0.00288	0.30000	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_sdfbbp_1	4196.74000	5867.66000	7346.26000

Delay Information

Delay(ns) to Q rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_sdfbbp_1	CLK->Q (RR)	0.01860	0.00100	0.27950	0.32940	0.06480	0.54628	2.50740	0.30000	1.39653
	SET_B->Q (FR)	0.01860	0.00100	0.11758	0.32940	0.06480	0.41005	2.50740	0.30000	1.33260

Delay(ns) to Q falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_sdfbbp_1	CLK->Q (RF)	0.01860	0.00100	0.23224	0.32940	0.06480	0.47920	2.50740	0.30000	1.25435
	RESET_B->Q (FF)	0.01860	0.00100	0.19389	0.32940	0.06480	0.46105	2.50740	0.30000	1.28523

Delay(ns) to Q rising (conditional):

Cell Name	Timing Arc(Dir)	When	Delay(ns)								
			Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_sdfbbp_1	CLK->Q (RR)	SCE	0.01860	0.00100	0.27950	0.32940	0.06480	0.54628	2.50740	0.30000	1.39653

Delay(ns) to Q falling (conditional):

Cell Name	Timing Arc(Dir)	When	Delay(ns)								
			Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_sdfbbp_1	CLK->Q (RF)	SCE	0.01860	0.00100	0.23224	0.32940	0.06480	0.47920	2.50740	0.30000	1.25435

Delay(ns) to Q_N rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_sdfbbp_1	CLK->Q_N (RR)	0.01860	0.00100	0.19082	0.32940	0.06480	0.47960	2.50740	0.30000	1.35239
	RESET_B->Q_N (FR)	0.01860	0.00100	0.15155	0.32940	0.06480	0.46810	2.50740	0.30000	1.39293

Delay(ns) to Q_N falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_sdfbbp_1	CLK->Q_N (RF)	0.01860	0.00100	0.23118	0.32940	0.06480	0.51412	2.50740	0.30000	1.27397
	SET_B->Q_N (FF)	0.01860	0.00100	0.07758	0.32940	0.06480	0.37227	2.50740	0.30000	1.22421

Delay(ns) to Q_N rising (conditional):

Cell Name	Timing Arc(Dir)	When	Delay(ns)								
			Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_sdfbbp_1	CLK->Q_N (RR)	SCE	0.01860	0.00100	0.19082	0.32940	0.06480	0.47960	2.50740	0.30000	1.35239

Delay(ns) to Q_N falling (conditional):

Cell Name	Timing Arc(Dir)	When	Delay(ns)								
			Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_sdfbbp_1	CLK->Q_N (RF)	SCE	0.01860	0.00100	0.23118	0.32940	0.06480	0.51412	2.50740	0.30000	1.27397

Constraint Information

Constraints(ns) for D rising :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_sdfbbp_1	hold	CLK (R)	0.01860	0.01860	-0.08803	1.26300	1.26300	-0.25365	2.50740	2.50740	-0.34533
	setup	CLK (R)	0.01860	0.01860	0.12715	1.26300	1.26300	0.27793	2.50740	2.50740	0.37189

Constraints(ns) for D falling :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_sdfbbp_1	hold	CLK (R)	0.01860	0.01860	-0.09536	1.26300	1.26300	-0.19698	2.50740	2.50740	-0.26269
	setup	CLK (R)	0.01860	0.01860	0.16138	1.26300	1.26300	0.26984	2.50740	2.50740	0.36009

Constraints(ns) for SCD rising :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_sdfbbp_1	hold	CLK (R)	0.01860	0.01860	-0.11492	1.26300	1.26300	-0.30761	2.50740	2.50740	-0.41617
	setup	CLK (R)	0.01860	0.01860	0.15649	1.26300	1.26300	0.32650	2.50740	2.50740	0.43683

Constraints(ns) for SCD falling :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_sdfbbp_1	hold	CLK (R)	0.01860	0.01860	-0.12715	1.26300	1.26300	-0.21047	2.50740	2.50740	-0.27449
	setup	CLK (R)	0.01860	0.01860	0.19562	1.26300	1.26300	0.28333	2.50740	2.50740	0.36894

Constraints(ns) for SCE rising :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_sdfbbp_1	hold	CLK (R)	0.01860	0.01860	-0.09781	1.26300	1.26300	-0.29412	2.50740	2.50740	-0.40436
	setup	CLK (R)	0.01860	0.01860	0.13693	1.26300	1.26300	0.31571	2.50740	2.50740	0.42797

Constraints(ns) for SCE falling :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_sdfbbp_1	hold	CLK (R)	0.01860	0.01860	-0.09781	1.26300	1.26300	-0.14841	2.50740	2.50740	-0.18890
	setup	CLK (R)	0.01860	0.01860	0.16383	1.26300	1.26300	0.22396	2.50740	2.50740	0.28925

Constraints(ns) for RESET_B rising :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_sdfbbp_1	recovery	CLK (R)	0.01860	0.01860	0.07580	1.26300	1.26300	0.13762	2.50740	2.50740	0.17119
	removal	CLK (R)	0.01860	0.01860	-0.04401	1.26300	1.26300	-0.10254	2.50740	2.50740	-0.12987

Min Pulse Width (ns) for RESET_B:

Cell Name	High	Low
sg13g2_sdfbbp_1	-	3.3435

Constraints(ns) for SET_B rising :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_sdfbbp_1	recovery	CLK (R)	0.01860	0.01860	0.03423	1.26300	1.26300	0.21047	2.50740	2.50740	0.55784
	removal	CLK (R)	0.01860	0.01860	0.02934	1.26300	1.26300	0.07555	2.50740	2.50740	0.07084
	hold	RESET_B (R)	0.01860	0.01860	-0.07336	1.26300	1.26300	-0.18889	2.50740	2.50740	-0.25973
	setup	RESET_B (R)	0.01860	0.01860	0.09292	1.26300	1.26300	0.22666	2.50740	2.50740	0.32762

Min Pulse Width (ns) for SET_B:

Cell Name	High	Low
sg13g2_sdfbbp_1	-	3.3435

Min Pulse Width (ns) for CLK:

Cell Name	High	Low
sg13g2_sdfbbp_1	3.3435	3.3435

Power Information

Internal switching power(pJ) to Q rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_sdfbbp_1	CLK	0.01860	0.00100	0.01922	0.32940	0.06480	0.02016	2.50740	0.30000	0.03315
	SET_B	0.01860	0.00100	0.03683	0.32940	0.06480	0.09616	2.50740	0.30000	0.33833

Internal switching power(pJ) to Q falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_sdfbbp_1	CLK	0.01860	0.00100	0.01919	0.32940	0.06480	0.01992	2.50740	0.30000	0.03129
	RESET_B	0.01860	0.00100	0.04196	0.32940	0.06480	0.10060	2.50740	0.30000	0.32598

Internal switching power(pJ) to Q rising (conditional):

Cell Name	Input	When	Power(pJ)								
			Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_sdfbbp_1	CLK	SCE	0.01860	0.00100	0.01922	0.32940	0.06480	0.02016	2.50740	0.30000	0.03315

Internal switching power(pJ) to Q falling (conditional):

Cell Name	Input	When	Power(pJ)								
			Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_sdfbbp_1	CLK	SCE	0.01860	0.00100	0.01919	0.32940	0.06480	0.01992	2.50740	0.30000	0.03129

Internal switching power(pJ) to Q_N rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_sdfbbp_1	CLK	0.01860	0.00100	0.01919	0.32940	0.06480	0.02008	2.50740	0.30000	0.03262
	RESET_B	0.01860	0.00100	0.04196	0.32940	0.06480	0.10091	2.50740	0.30000	0.32928

Internal switching power(pJ) to Q_N falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_sdfbbp_1	CLK	0.01860	0.00100	0.01922	0.32940	0.06480	0.02005	2.50740	0.30000	0.03106
	SET_B	0.01860	0.00100	0.03679	0.32940	0.06480	0.09586	2.50740	0.30000	0.33715

Internal switching power(pJ) to Q_N rising (conditional):

Cell Name	Input	When	Power(pJ)								
			Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_sdfbbp_1	CLK	SCE	0.01860	0.00100	0.01919	0.32940	0.06480	0.02008	2.50740	0.30000	0.03262

Internal switching power(pJ) to Q_N falling (conditional):

Cell Name	Input	When	Power(pJ)								
			Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_sdfbbp_1	CLK	SCE	0.01860	0.00100	0.01922	0.32940	0.06480	0.02005	2.50740	0.30000	0.03106

Passive power(pJ) for D rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_sdfbbp_1	0.01860	-0.00117	0.32940	-0.00092	2.50740	0.00656

Passive power(pJ) for D falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_sdfbbp_1	0.01860	0.00573	0.32940	0.00594	2.50740	0.01334

Passive power(pJ) for D rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_sdfbbp_1	(!CLK * RESET_B * !SCE * SET_B)	0.01860	0.01266	0.32940	0.01286	2.50740	0.02144
	(!CLK * RESET_B * !SCE * !SET_B)	0.01860	-0.00117	0.32940	-0.00092	2.50740	0.00656

Passive power(pJ) for D falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_sdfbbp_1	(!CLK * RESET_B * !SCE * SET_B)	0.01860	0.01361	0.32940	0.01377	2.50740	0.02235
	(!CLK * RESET_B * !SCE * !SET_B)	0.01860	0.00573	0.32940	0.00594	2.50740	0.01334

Passive power(pJ) for SCD rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_sdfbbp_1	0.01860	0.00658	0.32940	0.00667	2.50740	0.01321

Passive power(pJ) for SCD falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_sdfbbp_1	0.01860	-0.00384	0.32940	-0.00384	2.50740	0.00285

Passive power(pJ) for SCD rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_sdfbbp_1	(!CLK * RESET_B * SCE * SET_B)	0.01860	0.01476	0.32940	0.01479	2.50740	0.02222
	(!CLK * RESET_B * SCE * !SET_B)	0.01860	0.00658	0.32940	0.00667	2.50740	0.01321

Passive power(pJ) for SCD falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_sdfbbp_1	(!CLK * RESET_B * SCE * SET_B)	0.01860	0.01825	0.32940	0.01786	2.50740	0.02568
	(!CLK * RESET_B * SCE * !SET_B)	0.01860	-0.00384	0.32940	-0.00384	2.50740	0.00285

Passive power(pJ) for SCE rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_sdfbbp_1	0.01860	0.01680	0.32940	0.01738	2.50740	0.02802

Passive power(pJ) for SCE falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_sdfbbp_1	0.01860	0.01716	0.32940	0.01782	2.50740	0.02799

Passive power(pJ) for SCE rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_sdfbbp_1	(!CLK * D * RESET_B * !SCD * SET_B)	0.01860	0.01680	0.32940	0.01738	2.50740	0.02802
	(!CLK * D * RESET_B * !SCD * !SET_B)	0.01860	0.00922	0.32940	0.00891	2.50740	0.01943
	(!CLK * !D * RESET_B * SCD * SET_B)	0.01860	0.01493	0.32940	0.01600	2.50740	0.03524
	(!CLK * !D * RESET_B * SCD * !SET_B)	0.01860	0.00663	0.32940	0.00769	2.50740	0.02586

Passive power(pJ) for SCE falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_sdfbbp_1	(!CLK * D * RESET_B * !SCD * SET_B)	0.01860	0.01716	0.32940	0.01782	2.50740	0.02799
	(!CLK * D * RESET_B * !SCD * !SET_B)	0.01860	0.01127	0.32940	0.02042	2.50740	0.03065
	(!CLK * !D * RESET_B * SCD * SET_B)	0.01860	0.00068	0.32940	0.02613	2.50740	0.04656
	(!CLK * !D * RESET_B * SCD * !SET_B)	0.01860	-0.00663	0.32940	-0.00572	2.50740	0.01159

Passive power(pJ) for CLK rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_sdfbbp_1	0.01860	0.01313	0.32940	0.01435	2.50740	0.03497

Passive power(pJ) for CLK falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_sdfbbp_1	0.01860	0.01136	0.32940	0.01270	2.50740	0.03322

Passive power(pJ) for CLK rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_sdfbbp_1	(RESET_B * SCD * SCE * SET_B * Q * !Q_N)	0.01860	0.01345	0.32940	0.01468	2.50740	0.03538
	(RESET_B * !SET_B * Q * !Q_N)	0.01860	0.01313	0.32940	0.01435	2.50740	0.03497
	(RESET_B * !SCD * SCE * SET_B * !Q * Q_N)	0.01860	0.01312	0.32940	0.01428	2.50740	0.03516
	(D * RESET_B * !SCE * SET_B * Q * !Q_N)	0.01860	0.01345	0.32940	0.01468	2.50740	0.03538
	(!RESET_B * !Q * Q_N)	0.01860	0.00076	0.32940	0.00194	2.50740	0.02280
	(!D * RESET_B * !SCE * SET_B * !Q * Q_N)	0.01860	0.01310	0.32940	0.01426	2.50740	0.03515

Passive power(pJ) for CLK falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_sdfbbp_1	(RESET_B * SCD * SCE * SET_B * Q * !Q_N)	0.01860	0.01120	0.32940	0.01245	2.50740	0.03311
	(RESET_B * SCD * SCE * SET_B * !Q * Q_N)	0.01860	0.02144	0.32940	0.02262	2.50740	0.04385
	(RESET_B * !SET_B * Q * !Q_N)	0.01860	0.00263	0.32940	0.00406	2.50740	0.02525
	(RESET_B * !SCD * SCE * SET_B * Q * !Q_N)	0.01860	0.02401	0.32940	0.02536	2.50740	0.04669
	(RESET_B * !SCD * SCE * SET_B * !Q * Q_N)	0.01860	0.01185	0.32940	0.01318	2.50740	0.03371
	(D * RESET_B * !SCE * SET_B * Q * !Q_N)	0.01860	0.01120	0.32940	0.01245	2.50740	0.03312
	(!RESET_B * !Q * Q_N)	0.01860	-0.00076	0.32940	-0.00080	2.50740	0.01972
	(!D * RESET_B * !SCE * SET_B * !Q * Q_N)	0.01860	0.01136	0.32940	0.01270	2.50740	0.03322

SGCLK



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT			OUTPUT
GATE	SCE	CLK	GCLK
x	x	0	0
x	x	1	GCLK

Footprint

Cell Name	Area
sg13g2_slgcp_1	30.84480

Pin Capacitance Information

Cell Name	Pin Cap(pf)			Max Cap(pf)
	GATE	SCE	CLK	GCLK
sg13g2_slgcp_1	0.00174	0.00215	0.00466	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_slgcp_1	2647.24000	3177.05000	3734.55000

Delay Information

Delay(ns) to GCLK rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_slgcp_1	CLK->GCLK (RR)	0.01860	0.00100	0.07130	0.32940	0.06480	0.33739	2.50740	0.30000	1.21315

Delay(ns) to GCLK falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_slgcp_1	CLK->GCLK (FF)	0.01860	0.00100	0.06021	0.32940	0.06480	0.32046	2.50740	0.30000	1.12913

Constraint Information

Constraints(ns) for GATE rising :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_slgcp_1	hold	CLK (R)	0.01860	0.01860	-0.03916	1.26300	1.26300	-0.17809	2.50740	2.50740	-0.24830
	setup	CLK (R)	0.01860	0.01860	0.06357	1.26300	1.26300	0.26444	2.50740	2.50740	0.39630

Constraints(ns) for GATE falling :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_slgcp_1	hold	CLK (R)	0.01860	0.01860	-0.06325	1.26300	1.26300	-0.19428	2.50740	2.50740	-0.29591
	setup	CLK (R)	0.01860	0.01860	0.10518	1.26300	1.26300	0.25365	2.50740	2.50740	0.38332

Constraints(ns) for SCE rising :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_slgcp_1	hold	CLK (R)	0.01860	0.01860	-0.04561	1.26300	1.26300	-0.20508	2.50740	2.50740	-0.29642
	setup	CLK (R)	0.01860	0.01860	0.00200	1.26300	1.26300	0.00200	2.50740	2.50740	0.00200

Constraints(ns) for SCE falling :

Cell Name	Timing Check	Ref Pin(trans)	Constraint(ns)								
			Input Slew(ns)	Ref Slew(ns)	Min	Input Slew(ns)	Ref Slew(ns)	Mid	Input Slew(ns)	Ref Slew(ns)	Max
sg13g2_slgcp_1	hold	CLK (R)	0.01860	0.01860	-0.07134	1.26300	1.26300	-0.16190	2.50740	2.50740	-0.24024
	setup	CLK (R)	0.01860	0.01860	0.11060	1.26300	1.26300	0.22936	2.50740	2.50740	0.34050

Min Pulse Width (ns) for CLK:

Cell Name	High	Low
sg13g2_slgcp_1	3.3435	3.3435

Power Information

Internal switching power(pJ) to GCLK rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_slgcp_1	CLK	0.01860	0.00100	0.01331	0.32940	0.06480	0.01395	2.50740	0.30000	0.02670

Internal switching power(pJ) to GCLK falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_slgcp_1	CLK	0.01860	0.00100	0.01154	0.32940	0.06480	0.01270	2.50740	0.30000	0.02614

Passive power(pJ) for GATE rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_slgcp_1	0.01860	0.02129	0.32940	0.02309	2.50740	0.03653

Passive power(pJ) for GATE falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_slgcp_1	0.01860	0.01065	0.32940	0.03513	2.50740	0.04869

Passive power(pJ) for GATE rising (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_slgcp_1	!CLK	0.01860	0.02129	0.32940	0.02309	2.50740	0.03653

Passive power(pJ) for GATE falling (conditional):

Cell Name	When	Power(pJ)					
		Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_slgcp_1	!CLK	0.01860	0.01065	0.32940	0.03513	2.50740	0.04869

Passive power(pJ) for SCE rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_slgcp_1	0.01860	0.00646	0.32940	0.00711	2.50740	0.02064

Passive power(pJ) for SCE falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_slgcp_1	0.01860	0.01285	0.32940	0.03383	2.50740	0.04643

Passive power(pJ) for CLK rising :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_slgcp_1	0.01860	0.00557	0.32940	0.00665	2.50740	0.02449

Passive power(pJ) for CLK falling :

Cell Name	Power(pJ)					
	Slew(ns)	Min	Slew(ns)	Mid	Slew(ns)	Max
sg13g2_slgcp_1	0.01860	0.00420	0.32940	0.00544	2.50740	0.02348

TIE0



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Footprint

Cell Name	Area
sg13g2_tielo	7.25760

Pin Capacitance Information

Cell Name	Max Cap(pf)
	L_LO
sg13g2_tielo	-

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_tielo	57.43060	57.43060	57.43060

TIE1



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Footprint

Cell Name	Area
sg13g2_tiehi	7.25760

Pin Capacitance Information

Cell Name	Max Cap(pf)
	L_HI
sg13g2_tiehi	-

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_tiehi	55.09780	55.09780	55.09780

XNOR2_1



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp
125.00*

Truth Table

INPUT		OUTPUT
A	B	Y
0	0	1
0	1	0
1	0	0
1	1	1

Footprint

Cell Name	Area
sg13g2_xnor2_1	14.51520

Pin Capacitance Information

Cell Name	Pin Cap(pf)		Max Cap(pf)
	A	B	Y
sg13g2_xnor2_1	0.00511	0.00450	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_xnor2_1	436.45000	1366.71000	1931.98000

Delay Information

Delay(ns) to Y rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_xnor2_1	A->Y (RR)	0.01860	0.00100	0.07104	0.32940	0.06480	0.33829	2.50740	0.30000	1.21257
	A->Y (FR)	0.01860	0.00100	0.05425	0.32940	0.06480	0.53543	2.50740	0.30000	2.61345
	B->Y (RR)	0.01860	0.00100	0.06565	0.32940	0.06480	0.33599	2.50740	0.30000	1.21638
	B->Y (FR)	0.01860	0.00100	0.04778	0.32940	0.06480	0.55158	2.50740	0.30000	2.81190

Delay(ns) to Y falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_xnor2_1	A->Y (FF)	0.01860	0.00100	0.06991	0.32940	0.06480	0.43811	2.50740	0.30000	1.65012
	A->Y (RF)	0.01860	0.00100	0.04654	0.32940	0.06480	0.43680	2.50740	0.30000	2.22156
	B->Y (FF)	0.01860	0.00100	0.07075	0.32940	0.06480	0.42553	2.50740	0.30000	1.61942
	B->Y (RF)	0.01860	0.00100	0.03933	0.32940	0.06480	0.42800	2.50740	0.30000	2.20703

Power Information

Internal switching power(pJ) to Y rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_xnor2_1	A	0.01860	0.00100	0.00936	0.32940	0.06480	0.00996	2.50740	0.30000	0.02253
	B	0.01860	0.00100	0.00929	0.32940	0.06480	0.00984	2.50740	0.30000	0.02336

Internal switching power(pJ) to Y falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_xnor2_1	A	0.01860	0.00100	0.00846	0.32940	0.06480	0.00956	2.50740	0.30000	0.02285
	B	0.01860	0.00100	0.00959	0.32940	0.06480	0.00908	2.50740	0.30000	0.02293

XOR2_1



*sg13g2_stdcell_slow_1p35V_125C Cell Library: Process
sg13g2_stdcell_slow_1p35V_125C, Voltage 1.35, Temp 125.00*

Truth Table

INPUT		OUTPUT
A	B	X
0	0	0
0	1	1
1	0	1
1	1	0

Footprint

Cell Name	Area
sg13g2_xor2_1	16.32960

Pin Capacitance Information

Cell Name	Pin Cap(pf)		Max Cap(pf)
	A	B	X
sg13g2_xor2_1	0.00537	0.00460	0.30000

Leakage Information

Cell Name	Leakage(pW)		
	Min.	Avg	Max.
sg13g2_xor2_1	1079.39000	1356.14000	1948.49000

Delay Information

Delay(ns) to X rising :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_xor2_1	A->X (RR)	0.01860	0.00100	0.07229	0.32940	0.06480	0.54351	2.50740	0.30000	2.13940
	A->X (FR)	0.01860	0.00100	0.05980	0.32940	0.06480	0.54368	2.50740	0.30000	2.62410
	B->X (RR)	0.01860	0.00100	0.07568	0.32940	0.06480	0.52885	2.50740	0.30000	2.09418
	B->X (FR)	0.01860	0.00100	0.05123	0.32940	0.06480	0.53409	2.50740	0.30000	2.61154

Delay(ns) to X falling :

Cell Name	Timing Arc(Dir)	Delay(ns)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_xor2_1	A->X (FF)	0.01860	0.00100	0.08593	0.32940	0.06480	0.33163	2.50740	0.30000	1.11221
	A->X (RF)	0.01860	0.00100	0.04467	0.32940	0.06480	0.43382	2.50740	0.30000	2.21230
	B->X (FF)	0.01860	0.00100	0.07909	0.32940	0.06480	0.33669	2.50740	0.30000	1.14434
	B->X (RF)	0.01860	0.00100	0.03891	0.32940	0.06480	0.44304	2.50740	0.30000	2.34668

Power Information

Internal switching power(pJ) to X rising :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_xor2_1	A	0.01860	0.00100	0.00834	0.32940	0.06480	0.00920	2.50740	0.30000	0.02200
	B	0.01860	0.00100	0.00898	0.32940	0.06480	0.00837	2.50740	0.30000	0.02191

Internal switching power(pJ) to X falling :

Cell Name	Input	Power(pJ)								
		Slew(ns)	Load(pf)	Min	Slew(ns)	Load(pf)	Mid	Slew(ns)	Load(pf)	Max
sg13g2_xor2_1	A	0.01860	0.00100	0.01078	0.32940	0.06480	0.01120	2.50740	0.30000	0.02331
	B	0.01860	0.00100	0.00998	0.32940	0.06480	0.01074	2.50740	0.30000	0.02383