#

GPIO	SPIO
→ LED ~	→ I2C ✓
→ LCD ✓	→ UART ~
→ keyboard ✓	→ SSI/SPI ~
→ switch ~	→ CAN ~
⋮	⋮

SW

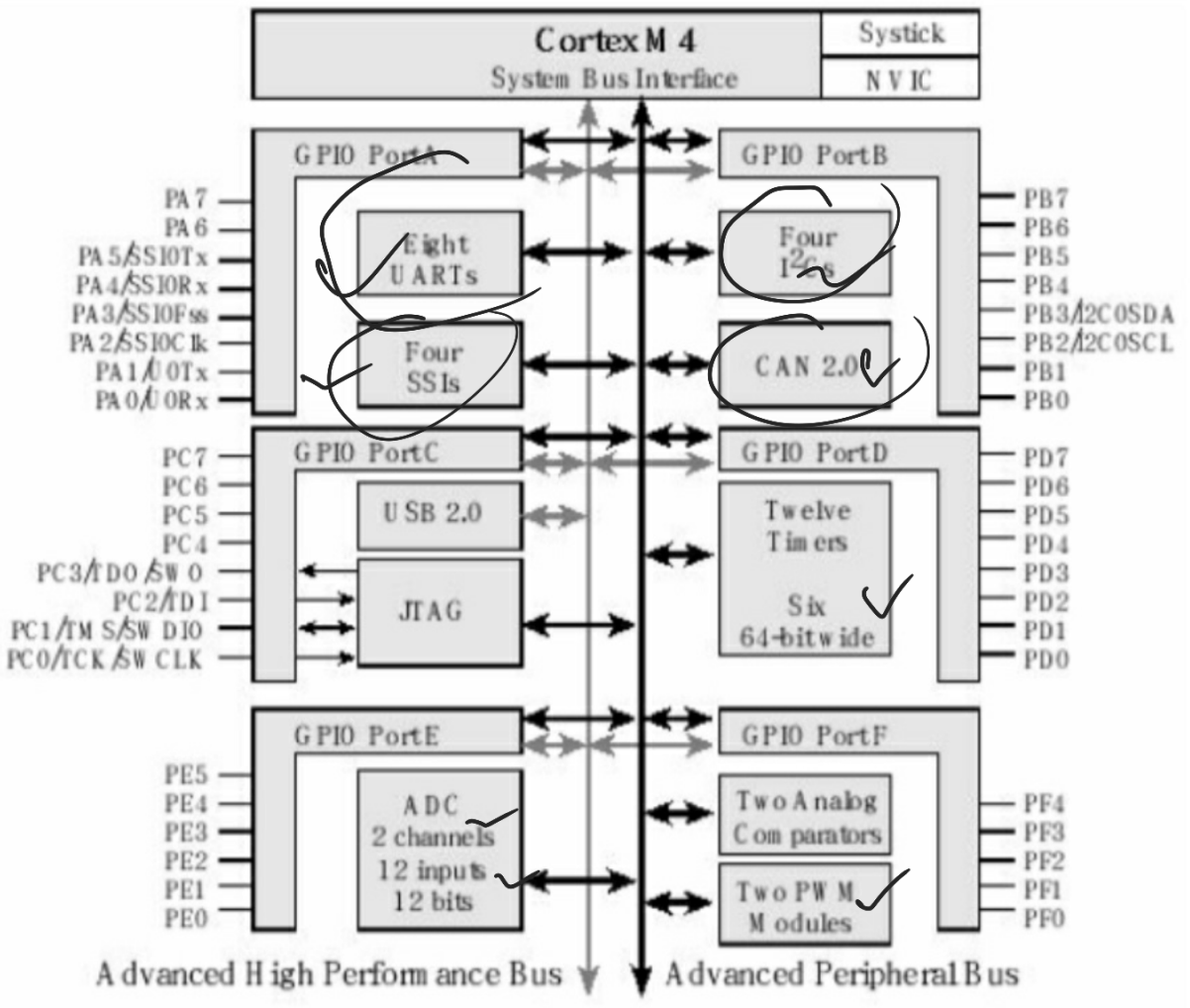
Page - 650 of

- GPIO can be interrupt ✓
  - Edge level ✓
  - → Level sensitive ~

GPIOlock
GPIOCR

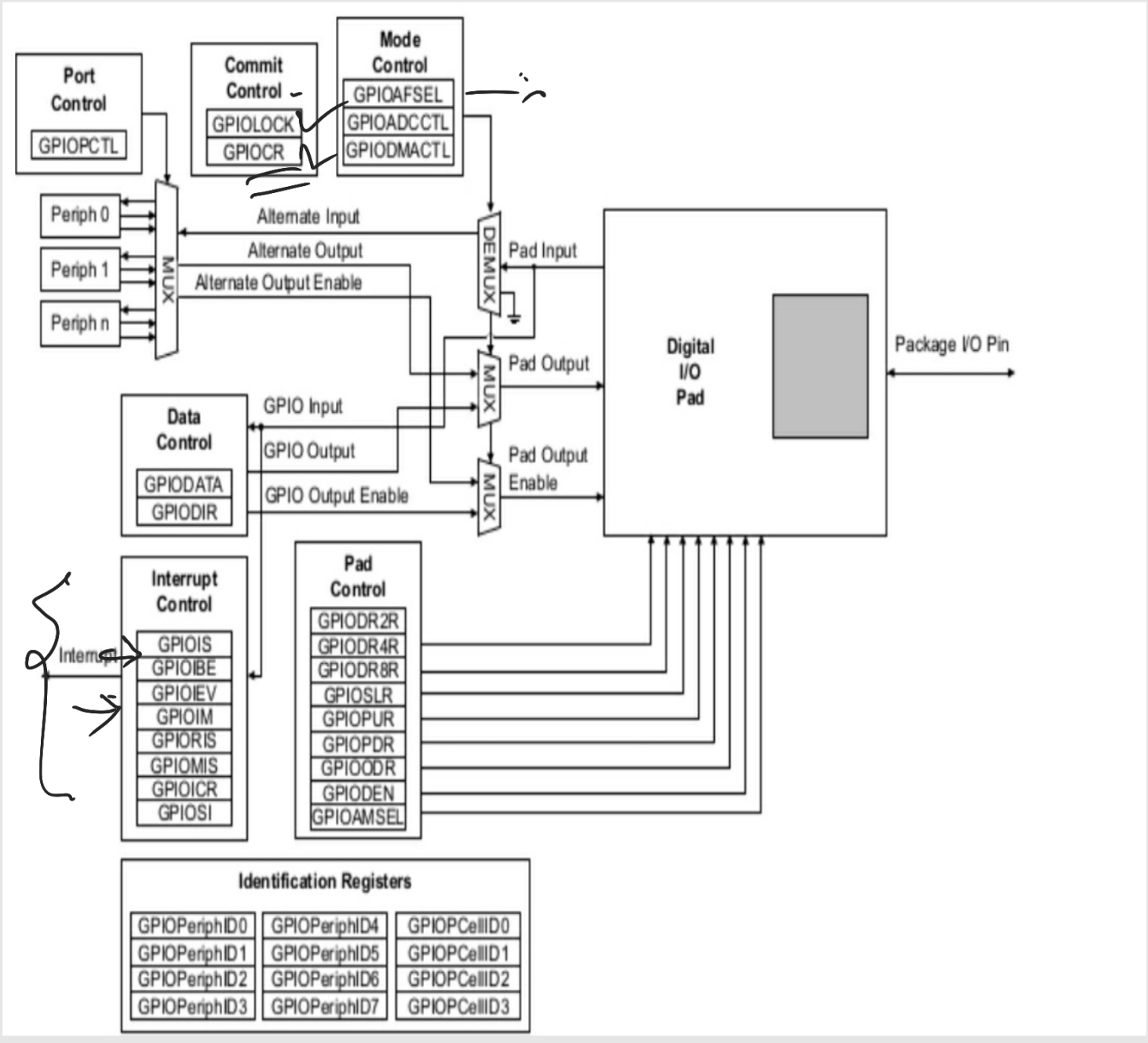
- directly initiate ADC sample seq. or UDMA transfer
- toggle rate = CPU speed or AHPB.
  ½ or APB C
- 5V tolerant
- programmable drive strength ~
- programmable weak - pull up
  pull down
  open drain

	Allocated size	Allocated address
Flash	256 KB	0x0000.0000 To 0x0003.FFFF ~
SRAM	32 KB	0x2000.0000 To 0x2000.7FFF ✓
I/O	All the peripherals	0x4000.0000 to 0x400F.FFFF ✓



Port name	Starting address	Ending address
PortA	0x40004000	0x40004FFF
PortB	0x40005000	0x40005FFF
PortC	0x40006000	0x40006FFF
PortD	0x40007000	0x40007FFF
PortE	0x40024000	0x40024FFF
PortF	0x40025000	0x40025FFF

GPIO Ports	Pins
PORTA	PA0 – PA7
PORTB	PB0- PB7
PORTC	PC0 – PC7
PORTD	PD0 – PD7
PORTE	PE0 – PE5
PORTF	PF0-PF7



① Initialization :-

→ Activate clock for port in Run Mode.
clock gating control register 2
(RCGC2) ⇒ 0x20 ; 10000.

→ see if it is GPIO or GPIO

→ if GPIO
→ unlock it

↳ Disable Analog mode select register (AMSEL) if you want to use digital Pin I/O ex: ADC

→ clear PCTL → for digital function

→ set DIR register ⇒ set o/p

→ clears bit in AFSEL

→ (DEN) ⇒ Enable digital port.

++ PC0 to PC3 - JTAG, debugg connection.

001 → present

002 → [illegible]

003 → [illegible] present - komal

004 → present - suraj

005 → sonali

006 → present

007 → pragati

008 → swapnil

009 → present

010 → saurabh kumbhar

011 → present

012 → Govind

013 →

Achish

14 → swapnil

15 → Achish

16 → shubham

17 →

18 → vaidya

19 → varsha

20 → wagh mam

Shraddha