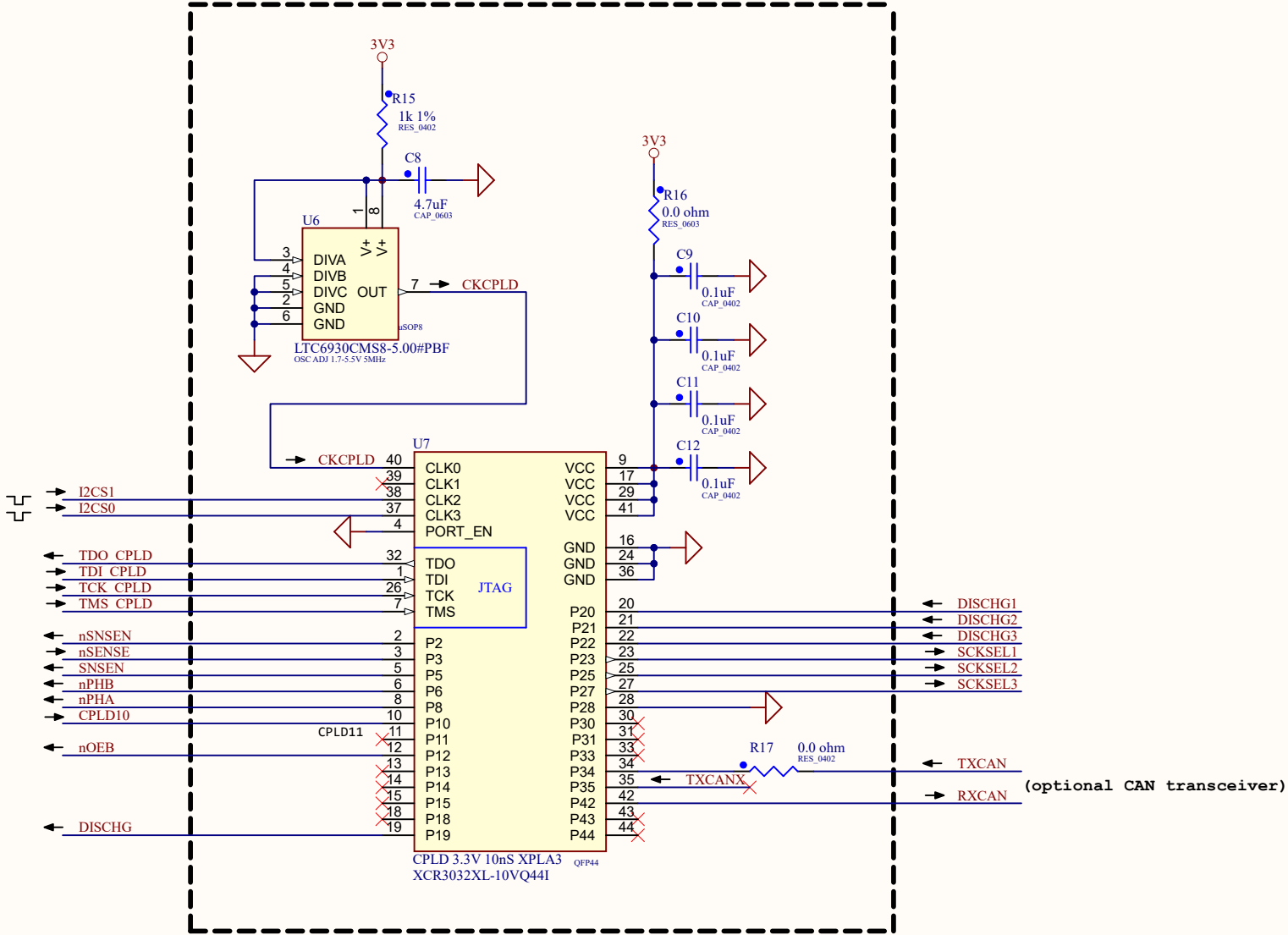


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ECN	DESCRIPTION	REV	DATE

-11 CPLD SYSTEM




CPLD NOTES

- 1) Program U7 with PM20 code.
- 2) Power rail noise on oscillator causes output jitter. LPF Fc of 34 Hz determined empirically from PM1A. $F_c = 1/(2\pi \cdot R \cdot C)$
- 3) Either TXCAN (from CPU) or TXCANX (from optional CAN transceiver) should be used.

CAN BIT DECODING

Logic 1 = RECESSIVE
Logic 0 = DOMINANT

APPROVALS		DATE				
ENG:	R. Yoder	7-19-2018				
DSN:	K. Kowalski	4-12-2022		PROJECT REVISION:*	DOCUMENT REVISION:*	DESIGN ITEM:*
CHK:	T. Crish	4-12-2022		TITLE CPLD		
REFERENCE DOCUMENTS						
BOM: *						
ASSY DWG: 51-2001						
FAB DWG: *						
PCB DWG: 52-2001						
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