
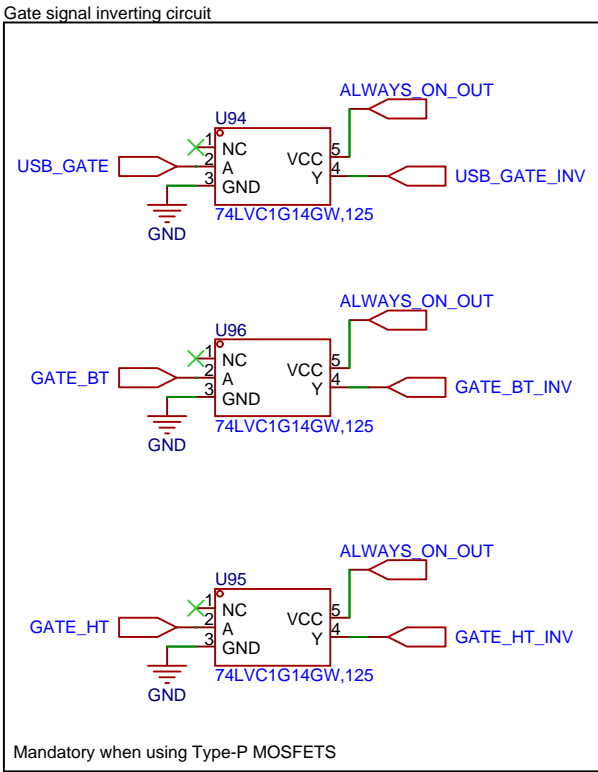
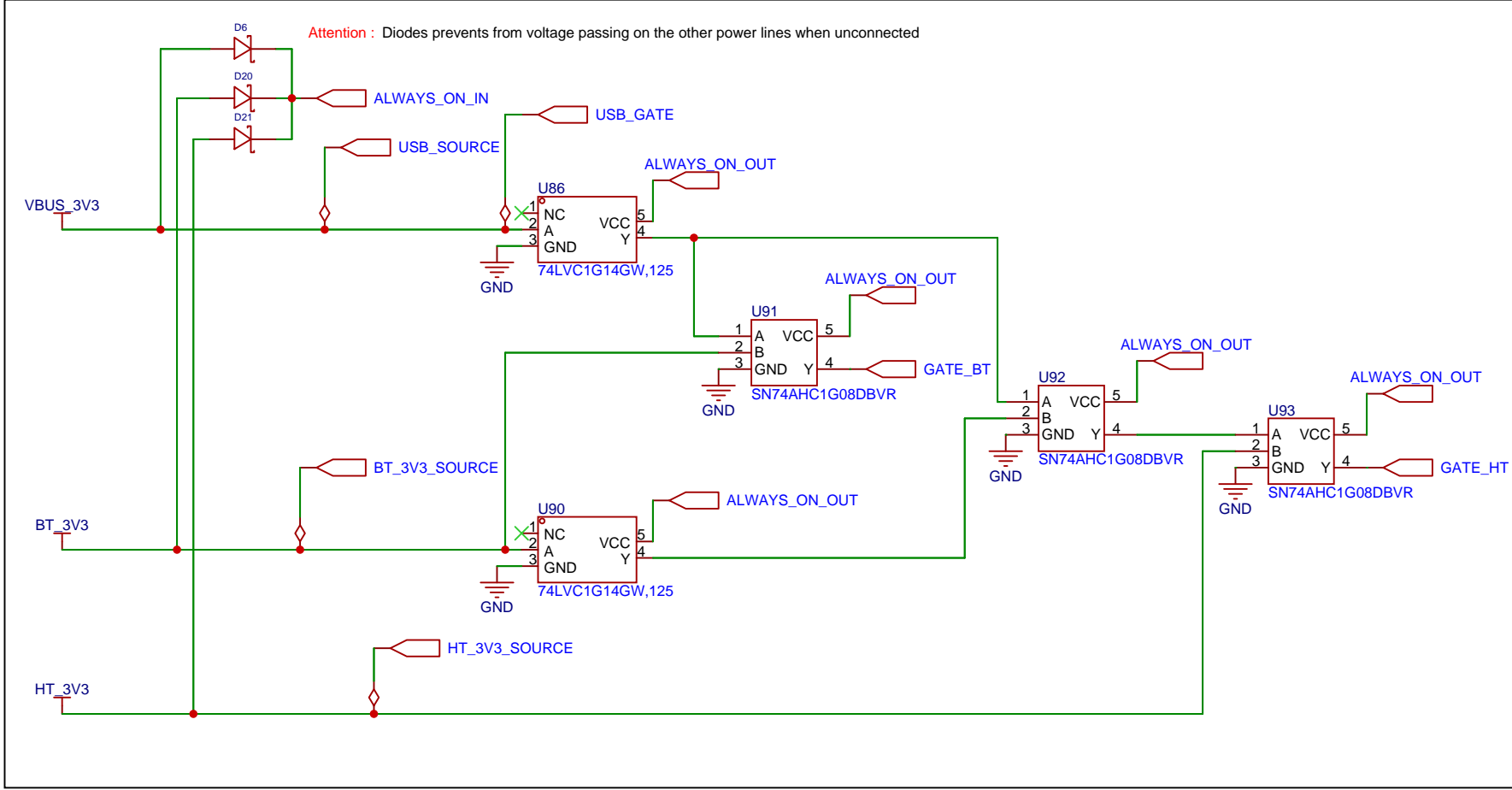


1	2	3	4	5	6
A					A
B					B
C					C
D					D
1	2	3	4	5	6

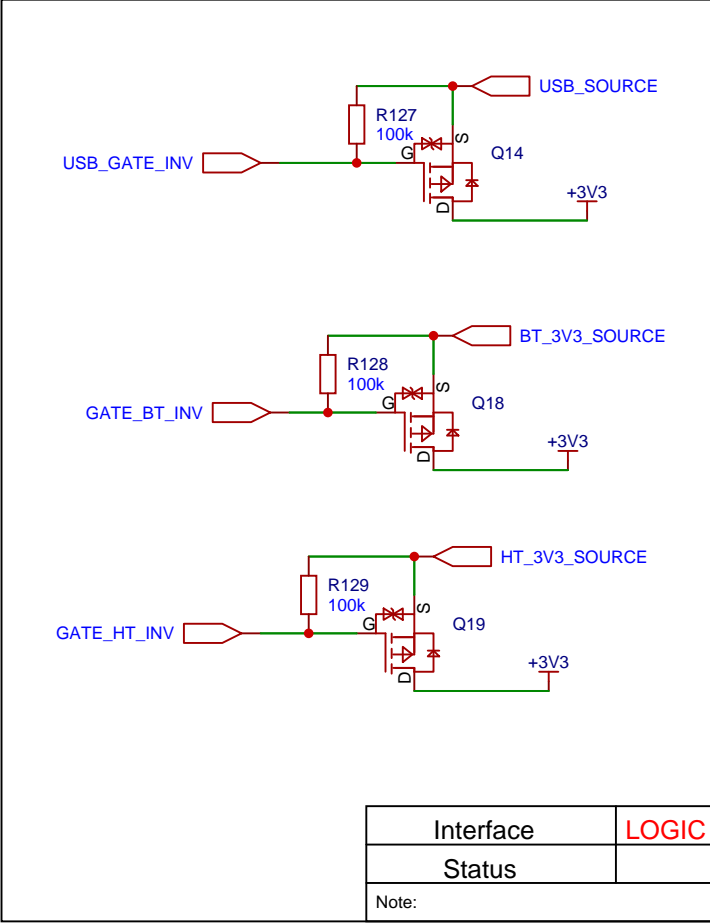
Schematic	Schematics			Update Date	2024-11-04
				Create Date	2024-11-04
Page	Index			Part Number	JLCPCB-002
Drawn	EasyEDA	ProjetElectronique_ESP32Board			
Reviewed	EasyEDA				
		VER	SIZE	PAGE	1 OF 7
		V0.1	A4	EasyEDA.com	

Power Priority Selection Circuit



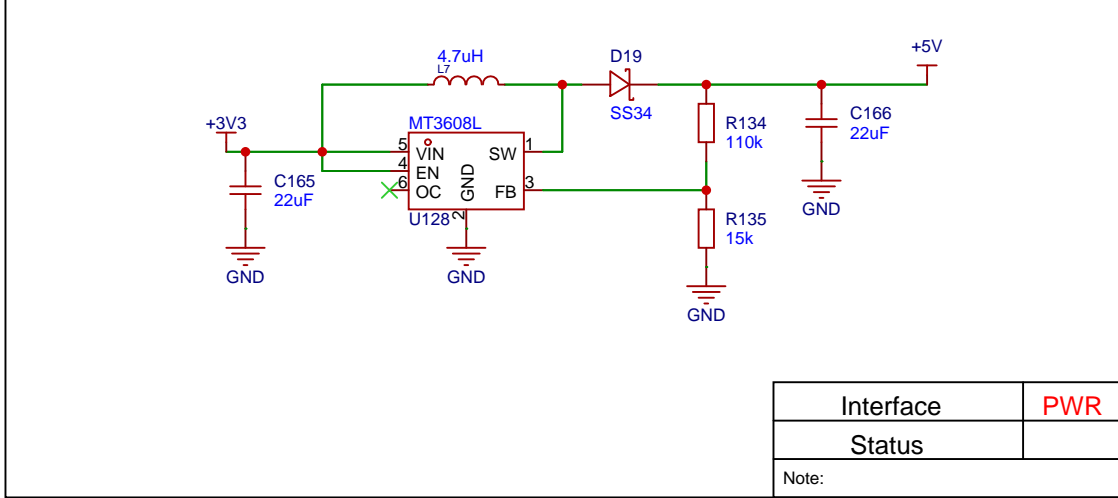
Interface	LOGIC
Status	
Note:	

Power Line Activation Circuit



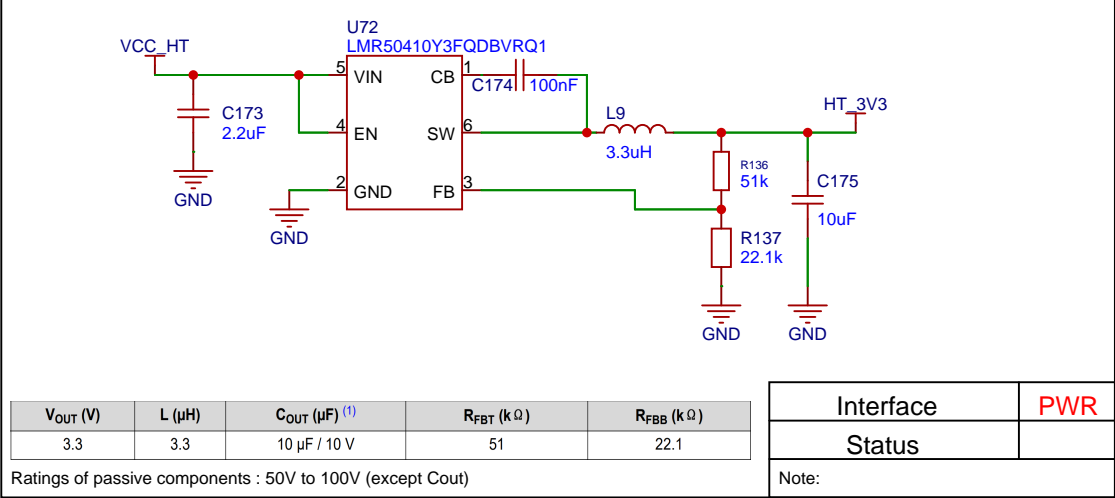
Interface	LOGIC
Status	
Note:	

Step-Up Converter (3.3V to 5V for Mikrobis)



Interface	PWR
Status	
Note:	

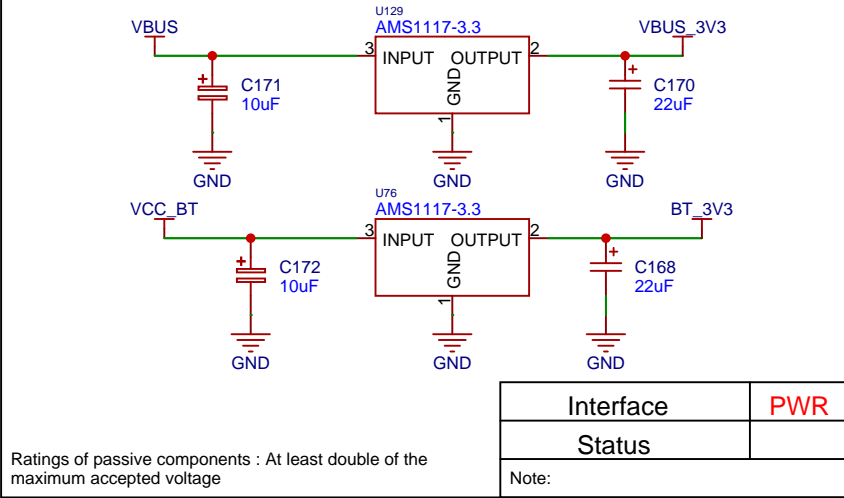
High Voltage DC/DC Converter



V _{OUT} (V)	L (μH)	C _{OUT} (μF) ⁽¹⁾	R _{FBT} (k Ω)	R _{FBB} (k Ω)
3.3	3.3	10 μF / 10 V	51	22.1

Ratings of passive components : 50V to 100V (except Cout)

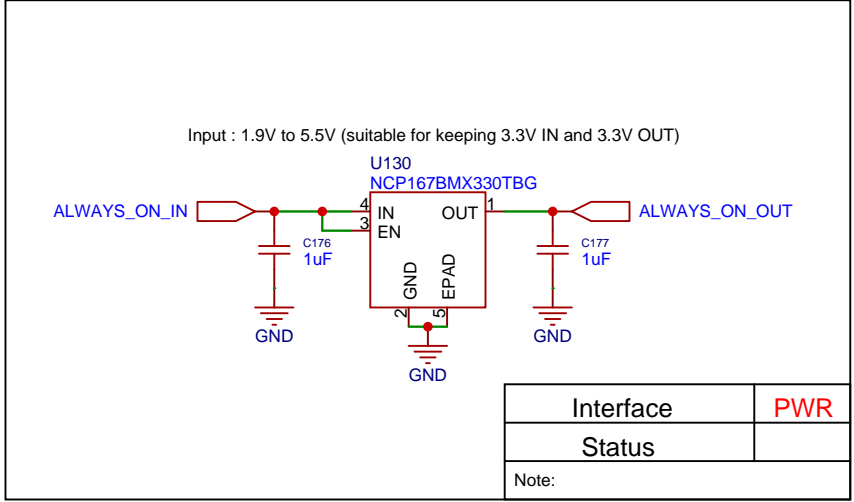
Low Voltage & USB VBUS DC/DC Converter




Interface	PWR
Status	
Note:	

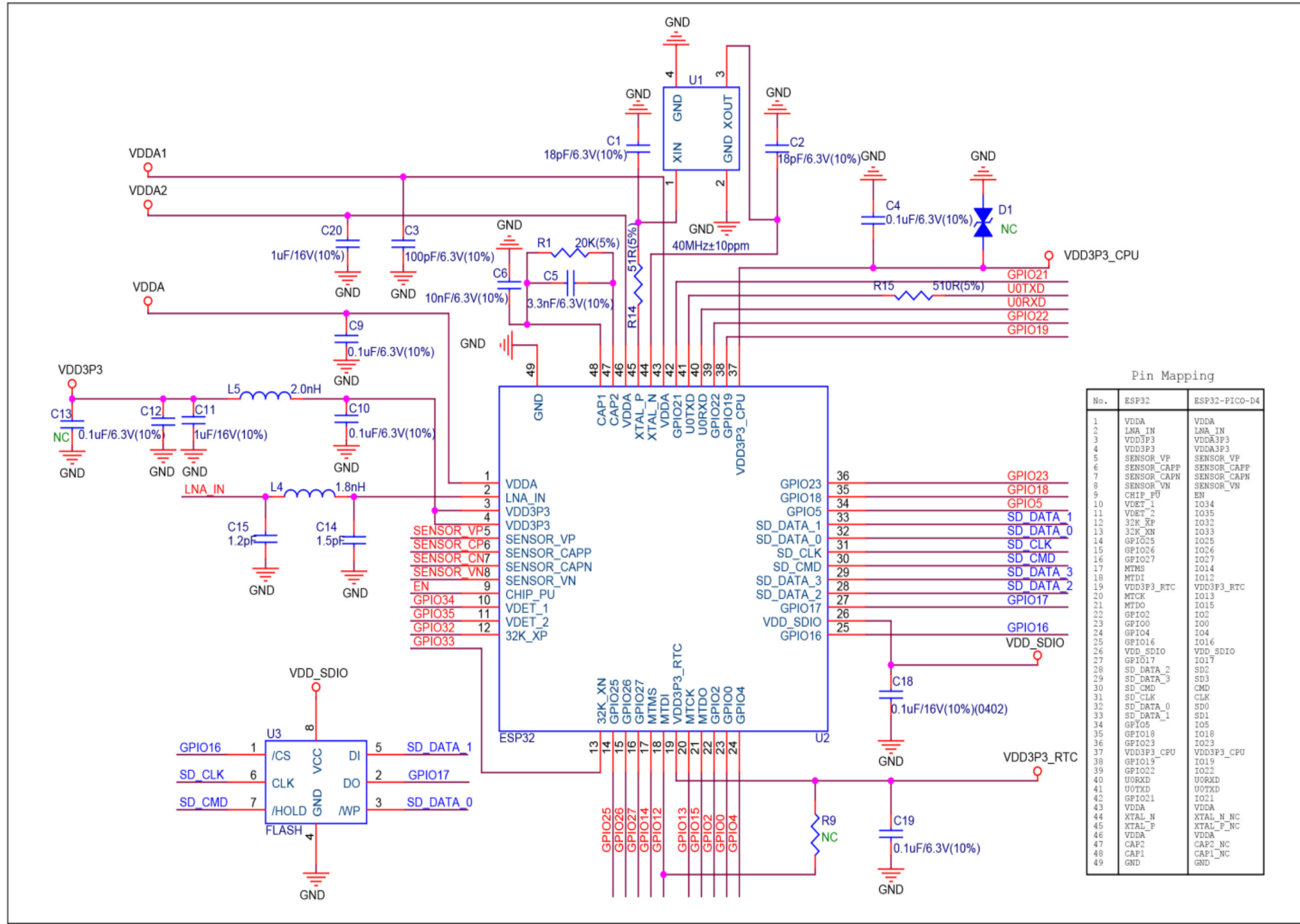
Ratings of passive components : At least double of the maximum accepted voltage

Low Voltage & USB VBUS DC/DC Converter

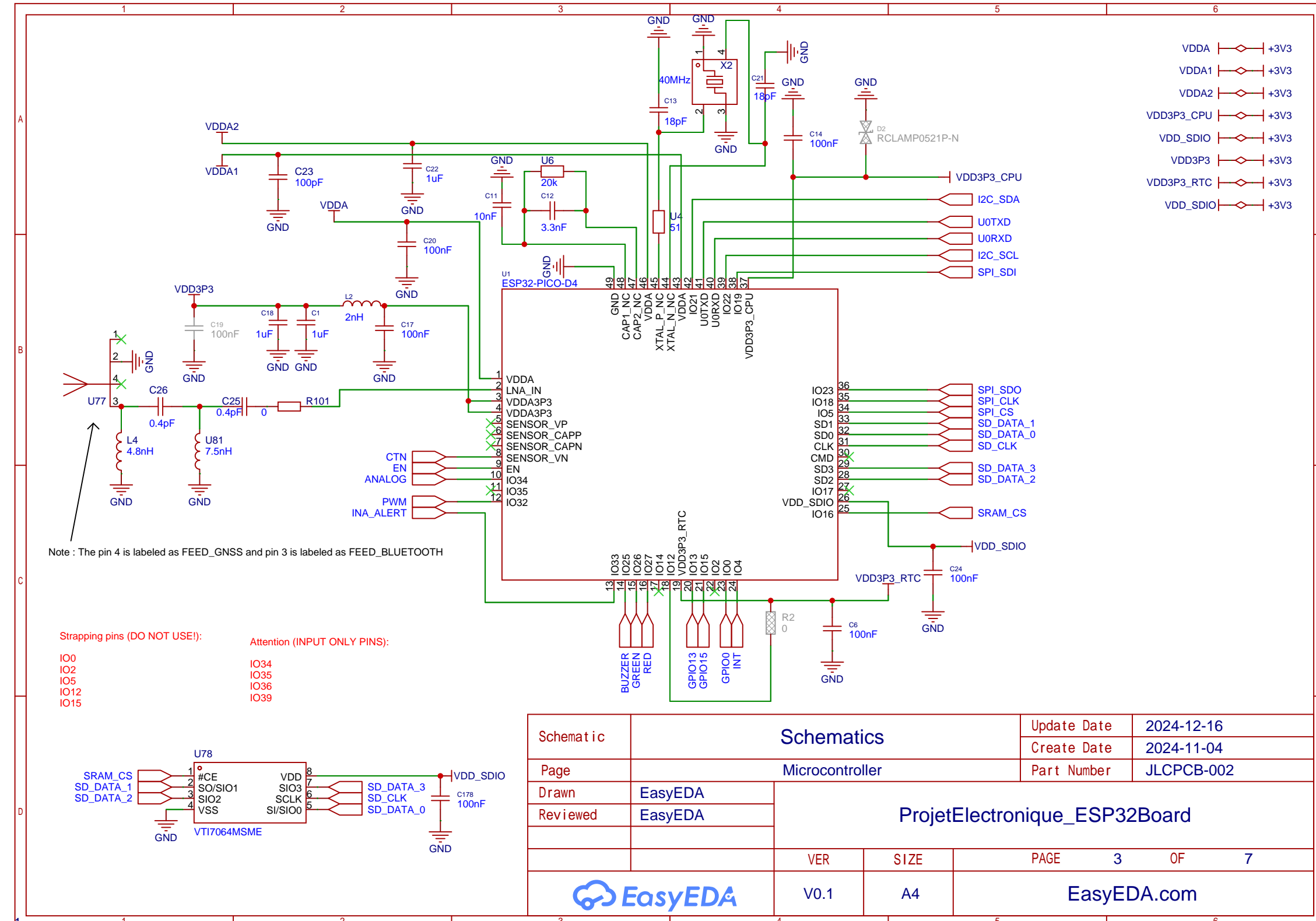


Interface	PWR
Status	
Note:	

Schematic	Schematics			Update Date	2024-11-28		
				Create Date	2024-11-04		
Page	Power			Part Number	JLPCPB-002		
Drawn	EasyEDA	ProjetElectronique_ESP32Board					
Reviewed	EasyEDA						
		VER	SIZE	PAGE	2	OF	7
		V0.1	A4	EasyEDA.com			



Note: For SDI and SDO pins, the Input/Output direction is from the perspective of the device in reference.
- If both sides have MOSI and MISO, connect MOSI to MOSI and MISO to MISO.
- For client devices, connect SDI to MOSI and SDO to MISO. (Our case for this project)
- For host devices, connect SDI to MISO and SDO to MOSI.



Note : The pin 4 is labeled as FEED_GNSS and pin 3 is labeled as FEED_BLUETOOTH

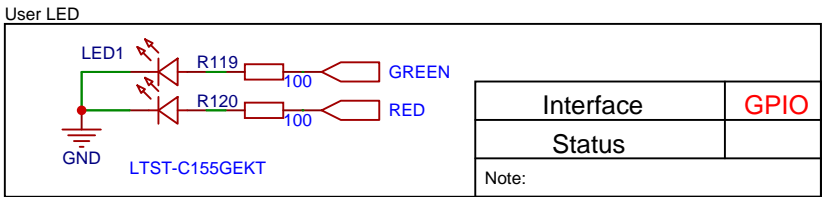
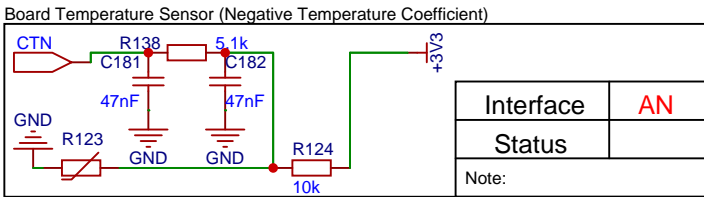
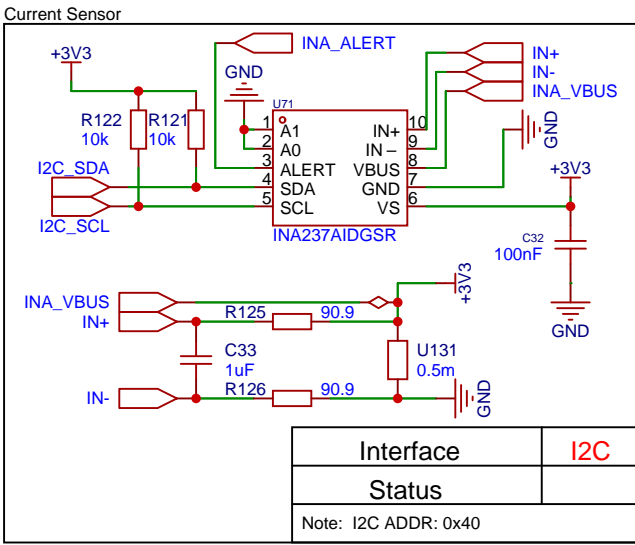
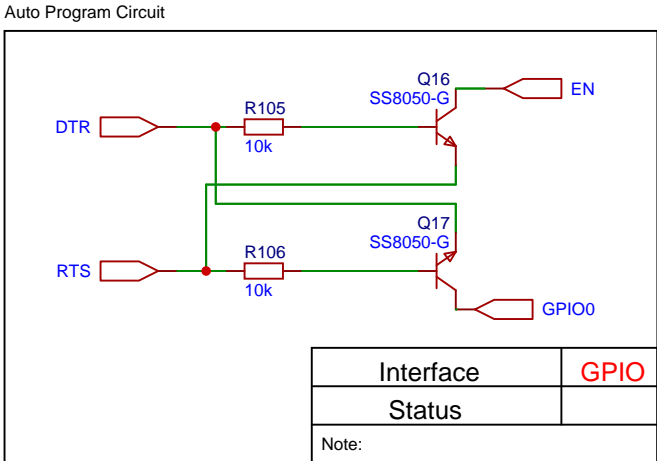
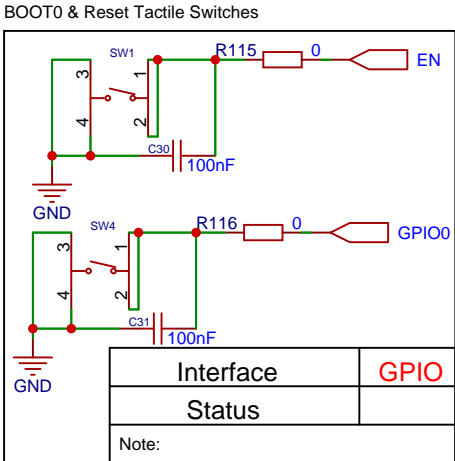
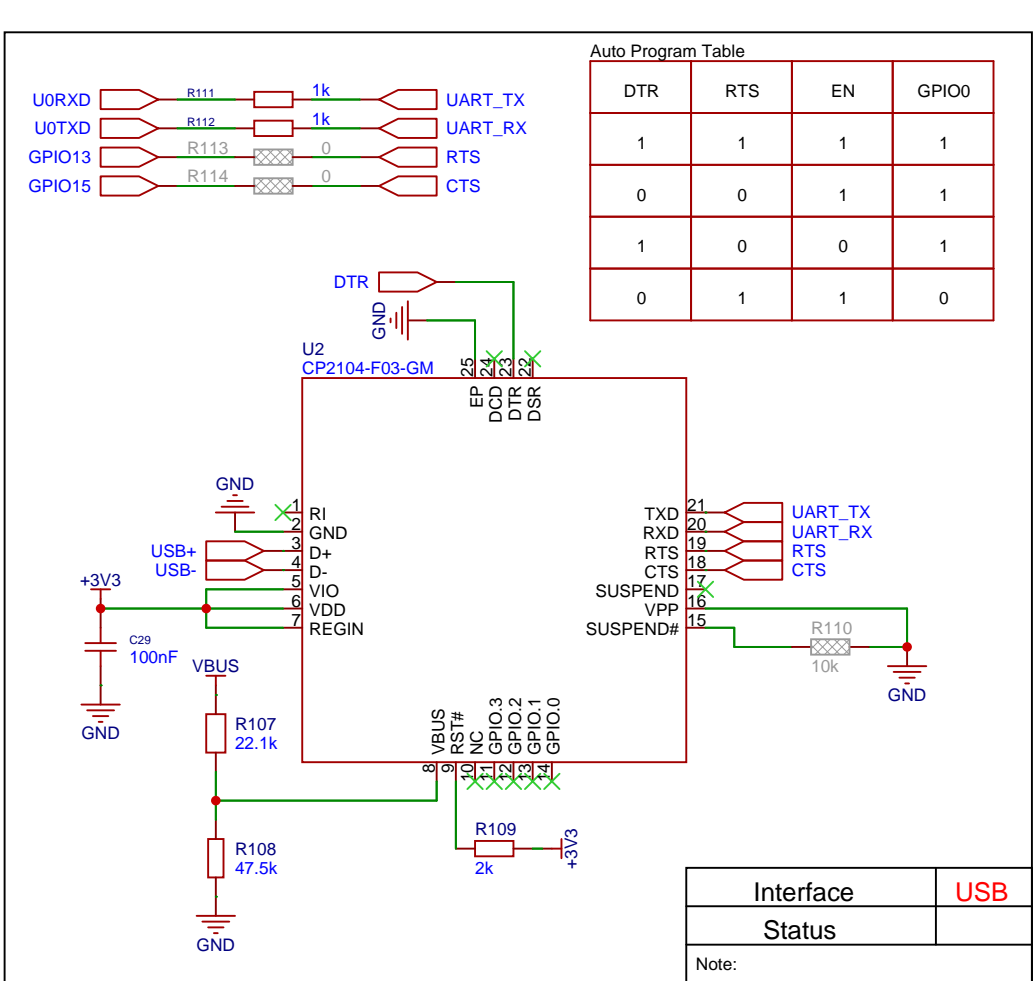
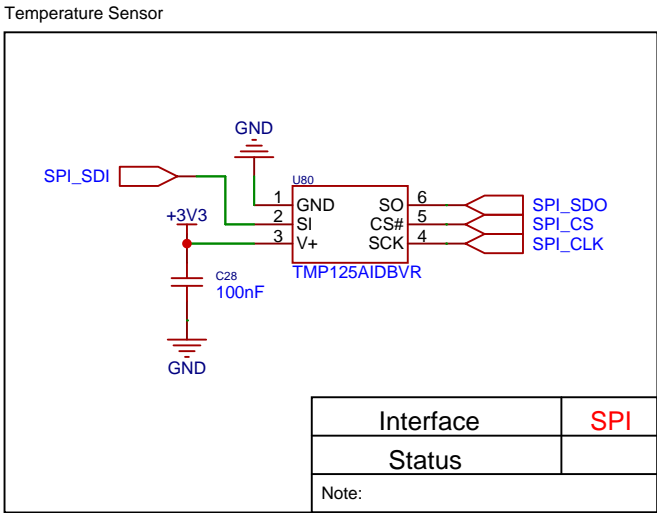
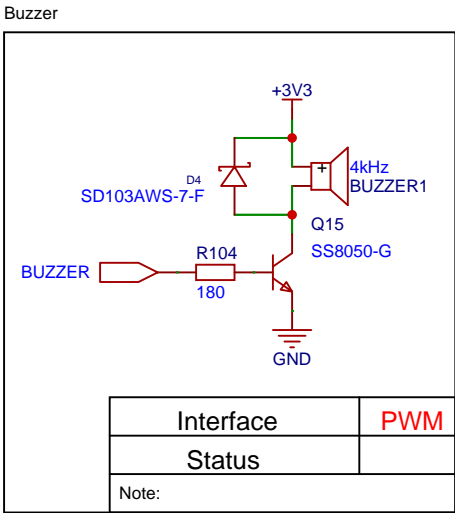
Strapping pins (DO NOT USE!):


IO0
IO2
IO5
IO12
IO15

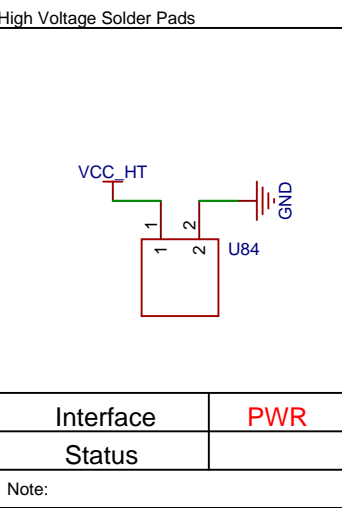
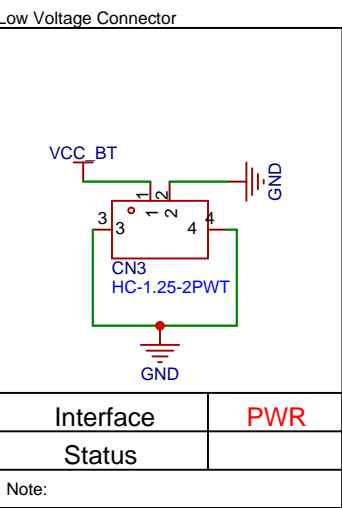
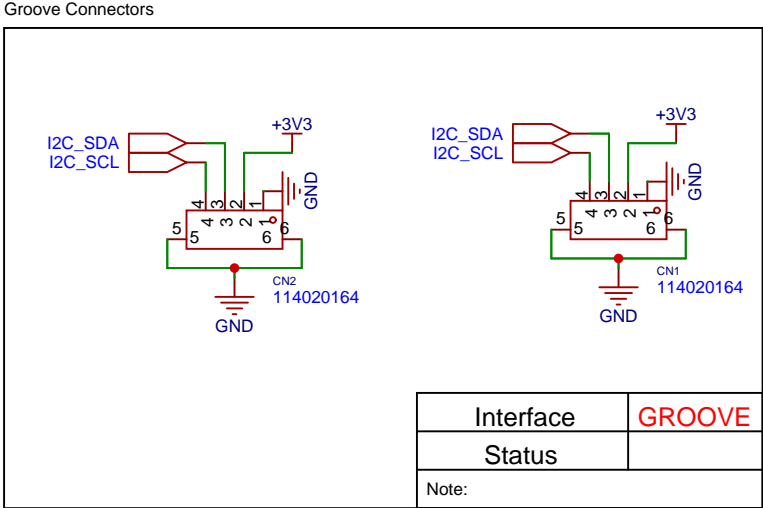
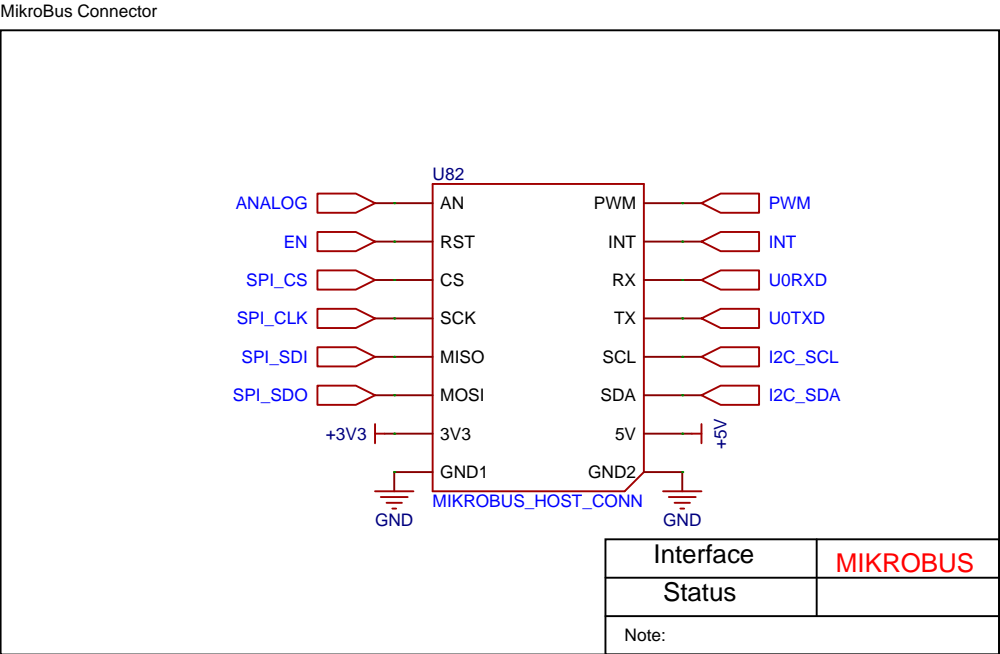
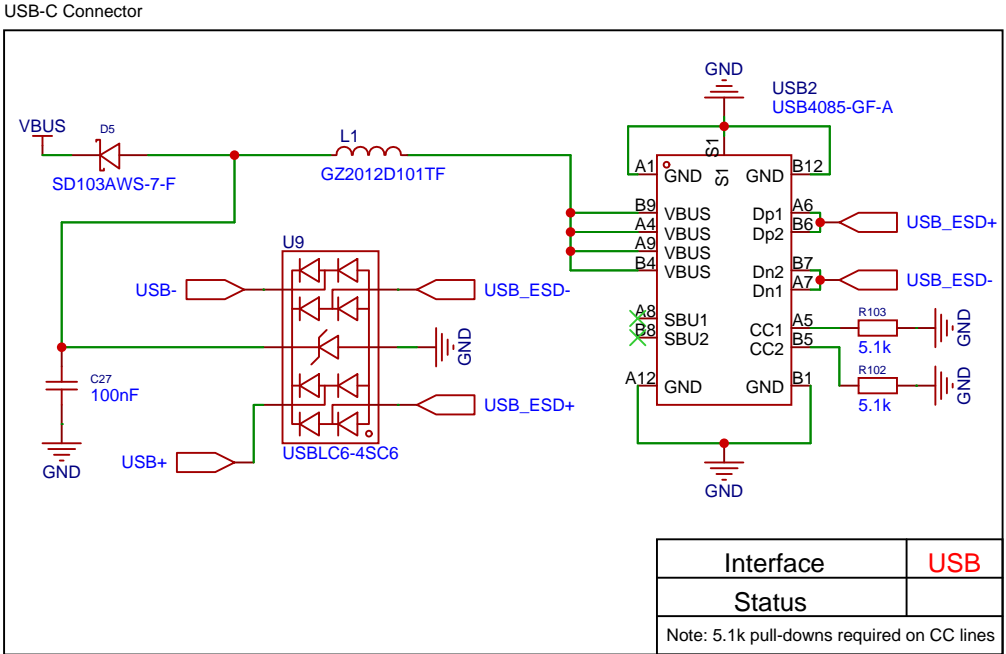
Attention (INPUT ONLY PINS):


IO34
IO35
IO36
IO39

Schematic	Schematics		Update Date	2024-12-16
Page	Microcontroller		Create Date	2024-11-04
Drawn	EasyEDA	ProjetElectronique_ESP32Board		
Reviewed	EasyEDA			
VER		SIZE	PAGE	3 OF 7
EasyEDA		V0.1	A4	EasyEDA.com




Schematic	Schematics			Update Date	2024-12-16
				Create Date	2024-11-04
Page	Peripherals			Part Number	JLPCB-002
Drawn	EasyEDA	ProjctElectronique_ESP32Board			
Reviewed	EasyEDA				
		VER	SIZE	PAGE	4 OF 7
		V0.1	A4	EasyEDA.com	




Schematic	Schematics			Update Date	2024-12-16
				Create Date	2024-11-04
Page	Connectors			Part Number	JLPCPB-002
Drawn	EasyEDA	ProjetElectronique_ESP32Board			
Reviewed	EasyEDA				
		VER	SIZE	PAGE	5 OF 7
		V0.1	A4	EasyEDA.com	

1	2	3	4	5	6
A					A
B					B
C					C
D					D
1	2	3	4	5	6

Schematic	Schematics			Update Date	2024-11-04
				Create Date	2024-11-04
Page	Stackup			Part Number	JLCPCB-002
Drawn	EasyEDA	ProjetElectronique_ESP32Board			
Reviewed	EasyEDA				
		VER	SIZE	PAGE 6 OF 7	
		V0.1	A4	EasyEDA.com	

1	2	3	4	5	6
A					A
B					B
C					C
D					D
1	2	3	4	5	6

Schematic	Schematics			Update Date	2024-11-04
				Create Date	2024-11-04
Page	Manufacturing			Part Number	JLCPCB-002
Drawn	EasyEDA	ProjetElectronique_ESP32Board			
Reviewed	EasyEDA				
		VER	SIZE	PAGE	7 OF 7
		V0.1	A4	EasyEDA.com	