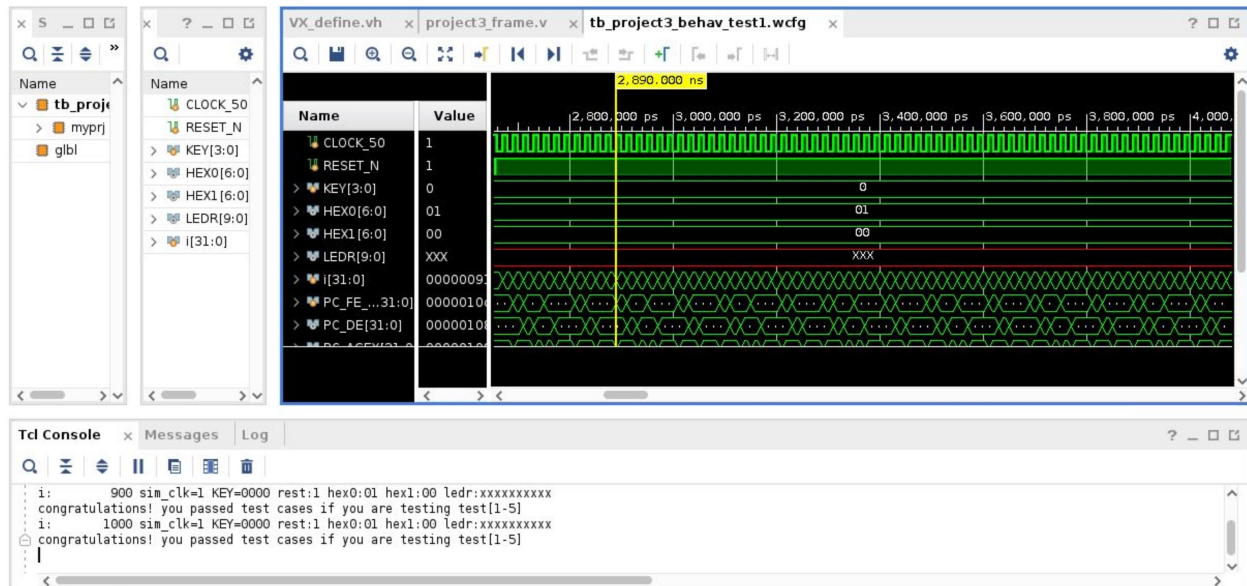
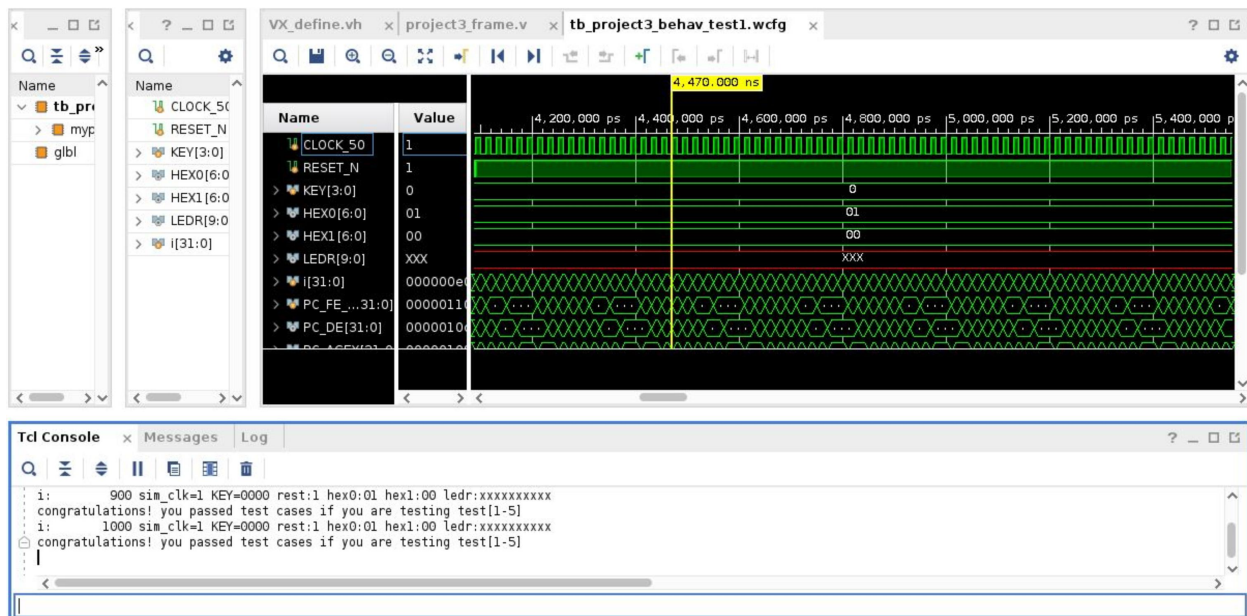
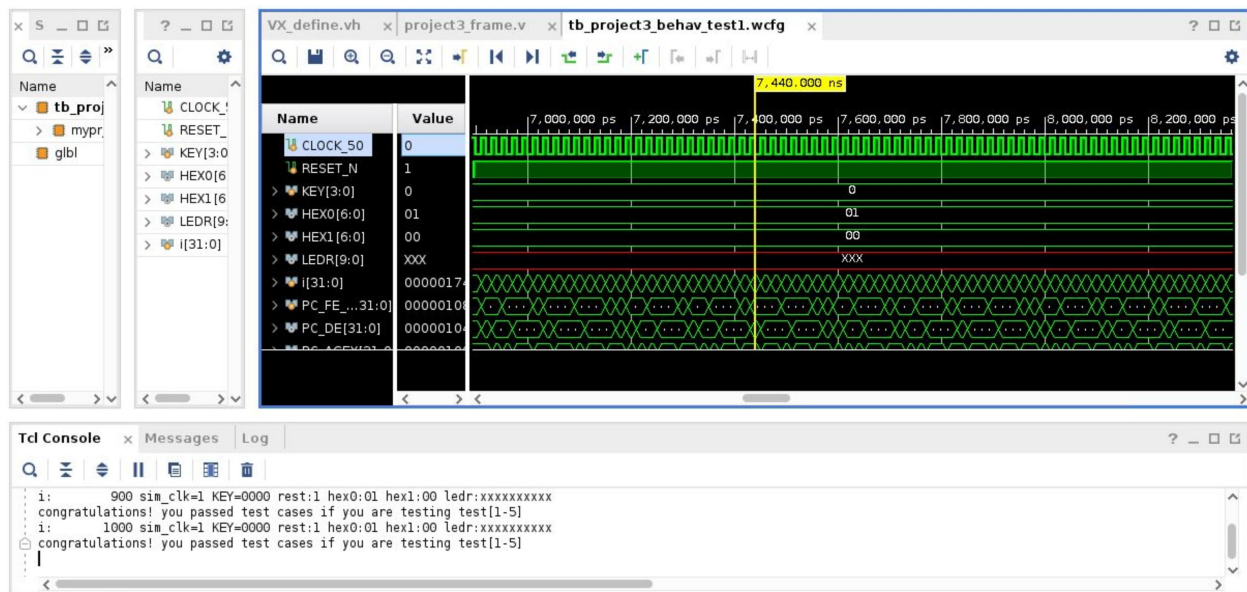
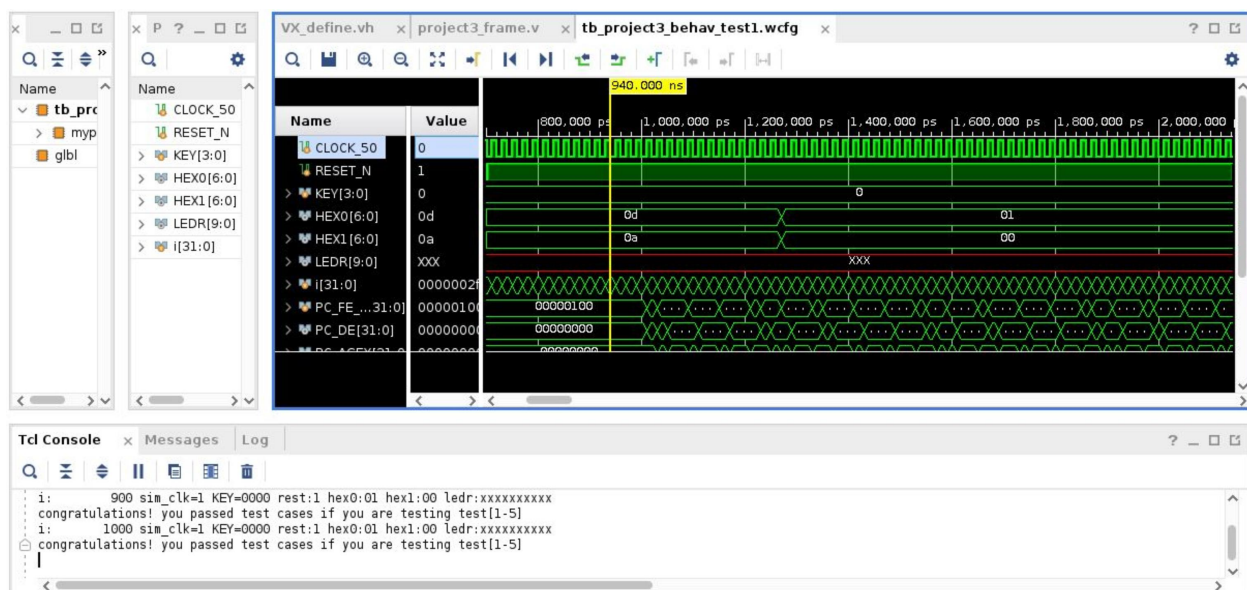


Test1.mem**Test2.mem** (the header file was changed for test2, but the waveform file is saved as “_test1”)

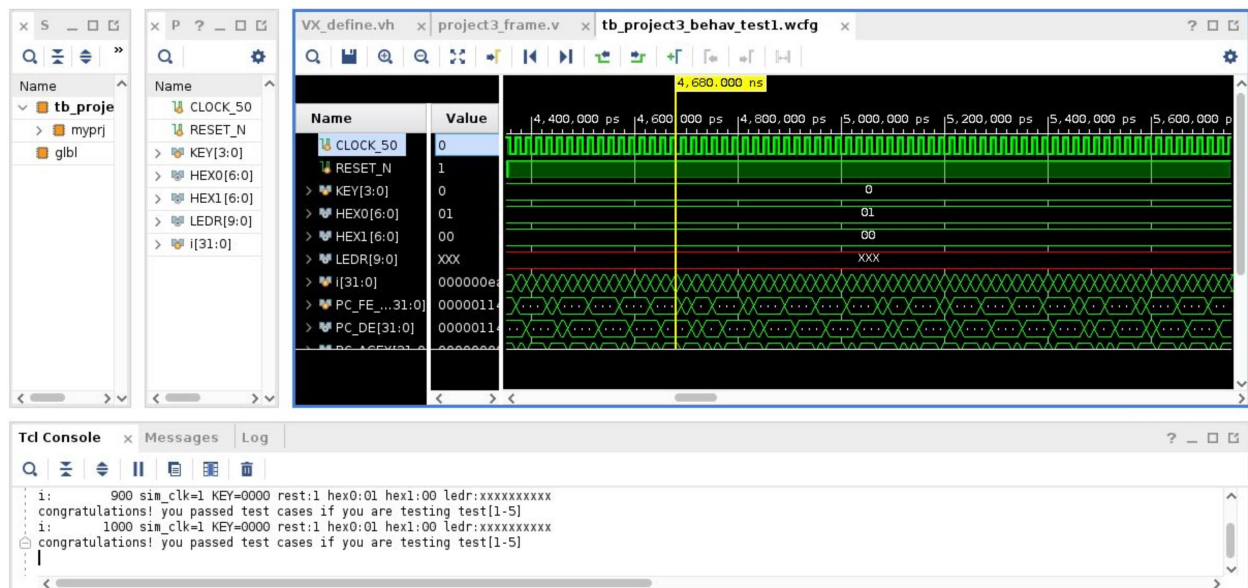
Test3.mem (the header file was changed for test3, but the waveform file is saved as “_test1”)



Test4.mem (the header file was changed for test4, but the waveform file is saved as “_test1”)



Test5.mem (the header file was changed for test5, but the waveform file is saved as “_test1”)



Synthesis and Implementation

Name	Constraints	Status	WNS	TNS	WHS	THS	TPWS	Total Power	Failed Routes	LUT	FF	BRAM	URAM	DSP	Start
✓ synth_1 (active)	constrs_1	synth_design Complete!								9452	571	0.0	0	0	3/4/2
✓ impl_1	constrs_1	route_design Complete!	0.138	0.000	0.050	0.000	0.000	0.230	0	9486	661	0.0	0	0	3/4/2

> Out-of-Context Module Runs