

Lab Report #03

2021.05.18. 실험 보고서

과 목 : 논리회로설계및실험

분 반 : 공통

담당교수 : 김석윤_{교수님}

실 험 일 : 2021.05.18.

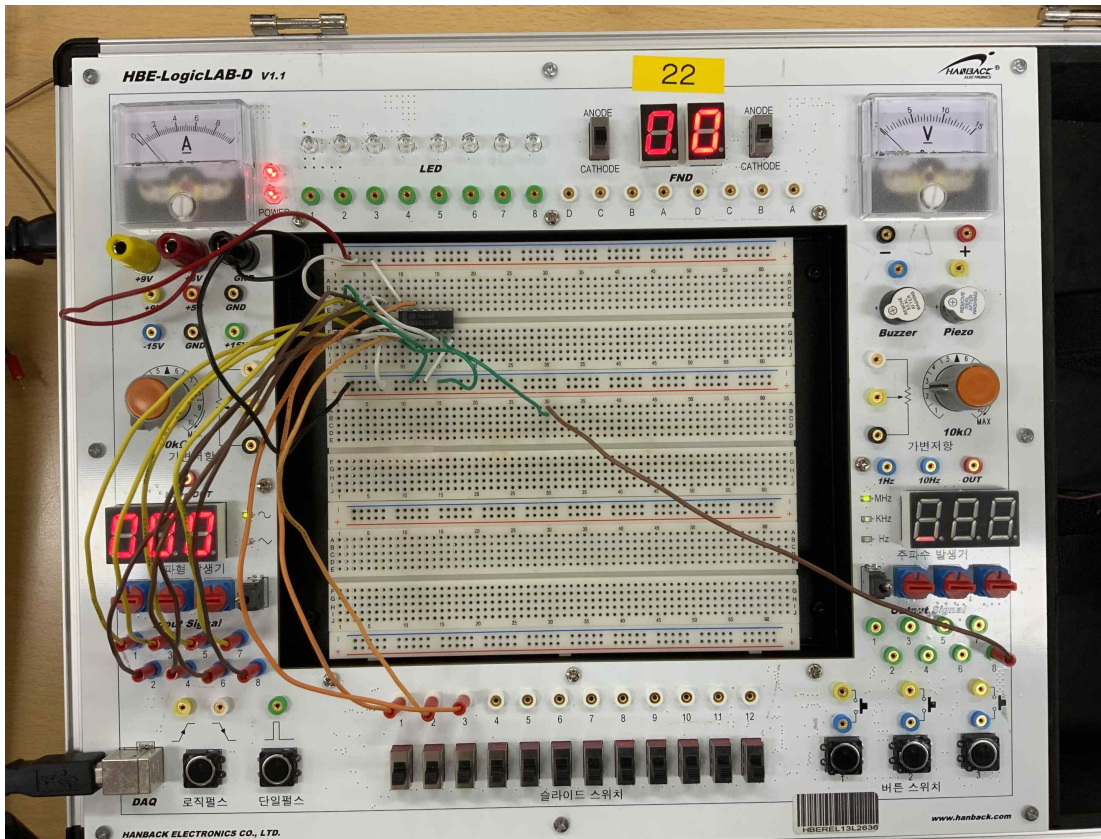
조 번 호 : 22

학 번 : 20163340 20193438

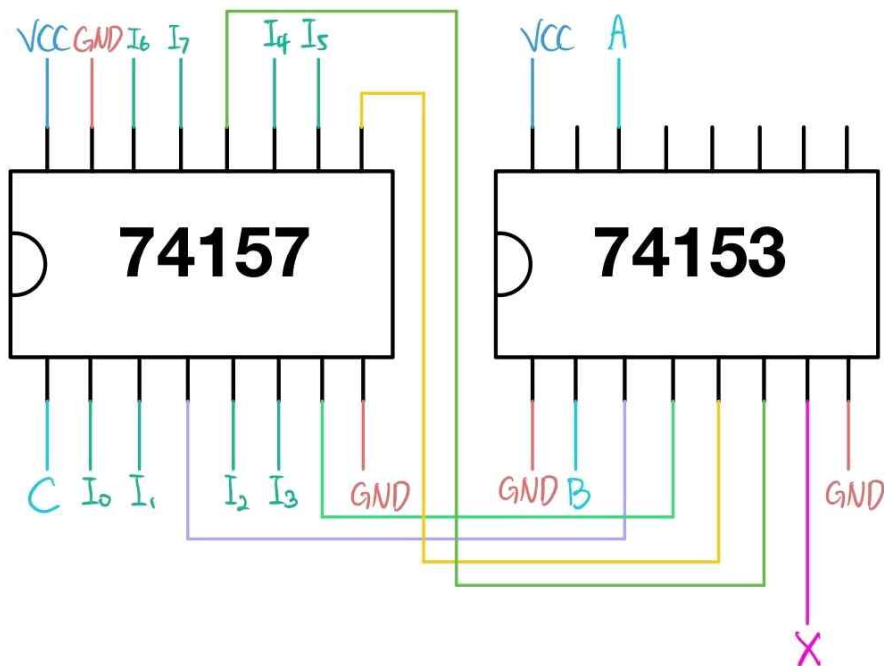
이 름 : 강 원 경 이 민 철

1) 실험 1

1. 사진

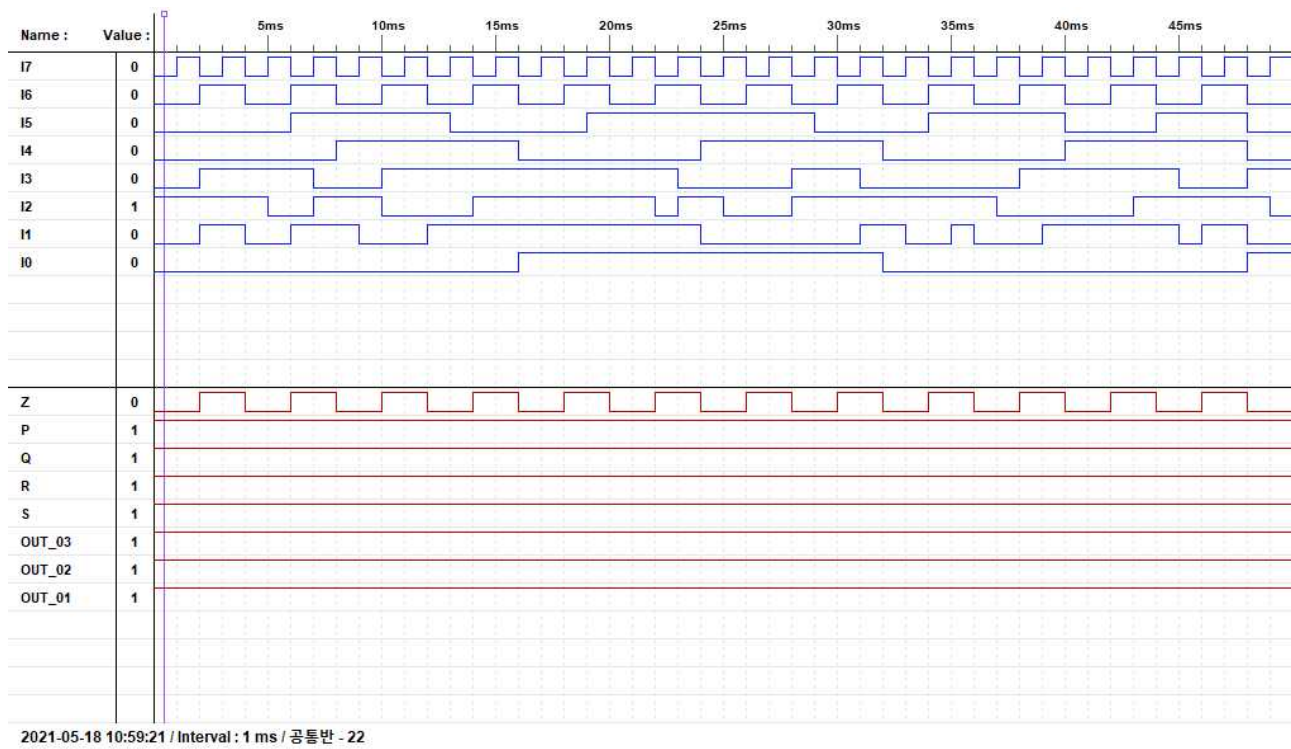


2. 회로도

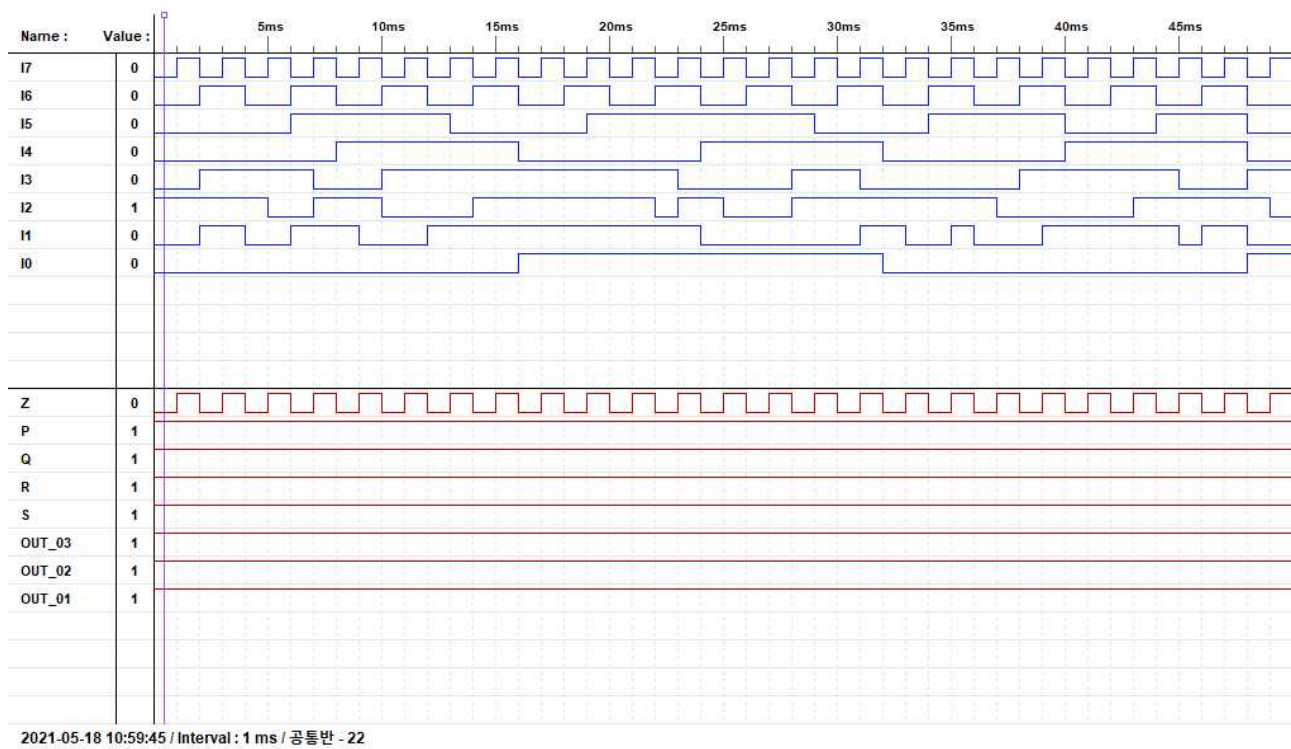


3. ABC = 000, 001, 110, 111일 때의 파형

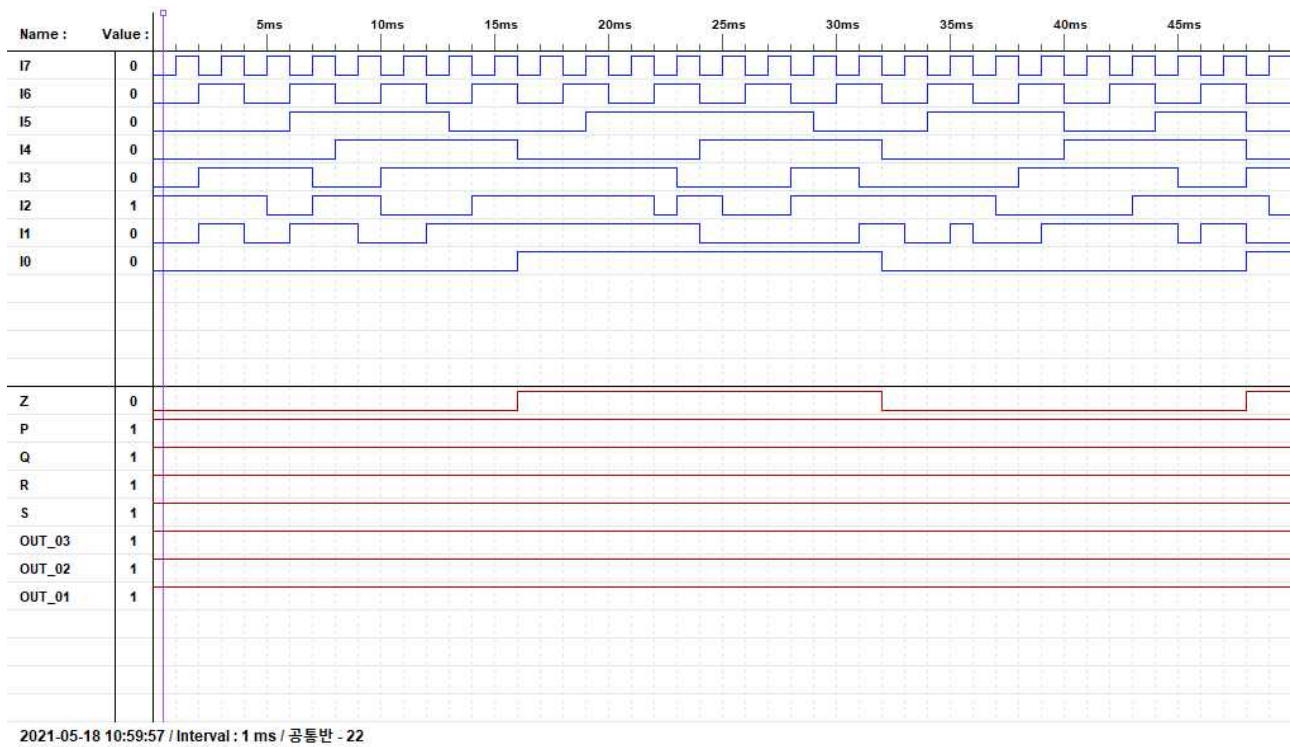
* 000



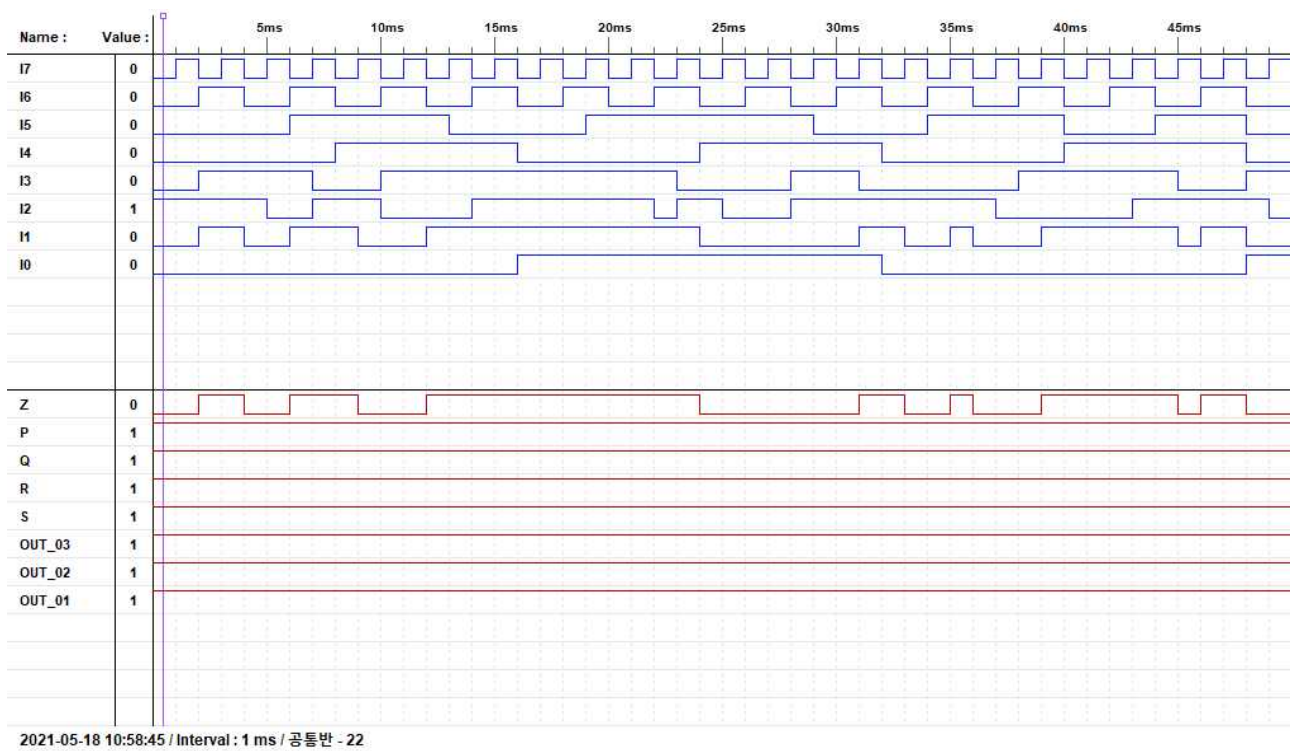
* 001



* 110



* 111



The diagram illustrates the connection of a 74153 decoder to a 7420 decoder. The 74153 decoder has inputs A, B, C, and two GND pins. Its outputs are connected to the inputs of the 7420 decoder. The 7420 decoder has inputs for f_1 , f_2 , and a GND pin. The diagram shows the wiring for the 74153's VCC, GND, and data inputs/outputs, and the 7420's VCC, GND, and data inputs/outputs.

3. 진리표

A	B	C	f1	f2
0	0	0	0	0
0	0	1	1	1
0	1	0	1	0
0	1	1	0	1
1	0	0	0	0
1	0	1	1	0
1	1	0	0	1
1	1	1	1	1

4. 실험 2에서 결과가 거꾸로 나오는 이유

: IC 74138에 V0~V7까지 모두 출력에 버블이 붙어있기 때문