```
::= 'INPUT' \langle ref-list\rangle 'OUTPUT' \langle ref-list\rangle 'VAR' \langle decl-list\rangle 'IN' \langle eqs \rangle \#
\langle program \rangle
                                                       := \langle ref\text{-}list\text{-}non\text{-}empty \rangle \mid \varepsilon
\langle ref-list\rangle
                                                      := \langle decl\text{-}list\text{-}non\text{-}empty \rangle \mid \varepsilon
\langle decl-list\rangle
\langle ref-list-non-empty\rangle ::= \langle ident \rangle, \langle ref-list-non-empty\rangle \mid \langle ident \rangle
\langle \mathit{decl\text{-}list\text{-}non\text{-}empty} \rangle \  \  \, ::= \  \, \langle \mathit{ident} \rangle \  \, \langle \mathit{size\text{-}spec} \rangle \  \, \text{`,'} \  \, \langle \mathit{decl\text{-}list\text{-}non\text{-}empty} \rangle \  \, \big| \  \, \langle \mathit{ident} \rangle \  \, \langle \mathit{size\text{-}spec} \rangle \  \, 
                                                        ::= \langle ident \rangle '=' \langle expr \rangle \langle eqs \rangle \mid \varepsilon
\langle eqs \rangle
\langle expr \rangle
                                                        := \langle arg \rangle
                                                                  'NOT' \langle arg \rangle
                                                                  'AND' \langle arg \rangle \langle arg \rangle
                                                                  'NAND' \langle arg \rangle \langle arg \rangle
                                                                  'OR' \langle arg \rangle \langle arg \rangle
                                                                  'XOR' \langle arg \rangle \langle arg \rangle
                                                                  'MUX' \langle arg \rangle \langle arg \rangle \langle arg \rangle
                                                                  'REG' \langle ident \rangle
                                                                  'CONCAT' \langle arg \rangle \langle arg \rangle
                                                                  'SELECT' \langle bus\text{-}size \rangle \langle arg \rangle
                                                                  'SLICE' \langle bus\text{-}size \rangle \langle bus\text{-}size \rangle \langle arg \rangle
                                                                  'ROM' \langle bus\text{-}size \rangle \langle bus\text{-}size \rangle \langle arg \rangle
                                                                   'RAM' \langle bus\text{-}size \rangle \ \langle bus\text{-}size \rangle \ \langle arg \rangle \ \langle arg \rangle \ \langle arg \rangle \ \langle arg \rangle
                                                        ::= \langle ident \rangle \mid \langle bin\text{-}digits \rangle \mid \langle bin\text{-}const \rangle \mid \langle dec\text{-}const \rangle \mid \langle hex\text{-}const \rangle
\langle arg \rangle
                                                       ::= ['a'-'z''A'-'Z''_-]['a'-'z''A'-'Z''0'-'9''_-]*
\langle ident \rangle
\langle bin\text{-}digits \rangle
                                                       ::= ['0''1']['0''1']*
\langle bin\text{-}const \rangle
                                                      ::= '0b'['0''1']['0''1']* \langle size\text{-}spec \rangle
                                                      := '0d'['0'-'9']['0'-'9']* ':' \langle bus\text{-}size \rangle
\langle dec\text{-}const \rangle
                                                      := `0x'[`0'-`9'`a'-`f'][`0'-`9'`a'-`f']* \langle size-spec \rangle
\langle hex\text{-}const \rangle
                                                    := ':' \langle bus\text{-}size \rangle \mid \varepsilon
\langle size\text{-}spec \rangle
```

::= ['1'-'9']['0'-'9']*

 $\langle bus\text{-}size \rangle$