

| RV1106 GPIO V1.0.0 | | | | | | | |
|--------------------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| Name | Config | Description | Register | Position | Value | I/O Command | |
| GPIO0A0 | IOMUX | UART0_RX_M0 | 0xFF388000 | [0, 2] | 1 | io -4 0xFF388000 0x00070001 | |
| | | CLK_32K | 0xFF388000 | [0, 2] | 2 | io -4 0xFF388000 0x00070002 | |
| | | CLK_REFOUT | 0xFF388000 | [0, 2] | 3 | io -4 0xFF388000 0x00070003 | |
| | | RTC_CLK0 | 0xFF388000 | [0, 2] | 4 | io -4 0xFF388000 0x00070004 | |
| | Pull | GPIO0_A0 | 0xFF388000 | [0, 2] | 0 | io -4 0xFF388000 0x00070000 | |
| | | normal | 0xFF388038 | [0, 1] | 0 | io -4 0xFF388038 0x00030000 | |
| | | pullup | 0xFF388038 | [0, 1] | 1 | io -4 0xFF388038 0x00030001 | |
| | I/O Mode | pulldown | 0xFF388038 | [0, 1] | 2 | io -4 0xFF388038 0x00030002 | |
| | | input | 0xFF380008 | [0, 0] | 0 | io -4 0xFF380008 0x00010000 | |
| | Output | output | 0xFF380008 | [0, 0] | 1 | io -4 0xFF380008 0x00010001 | |
| | | low | 0xFF380000 | [0, 0] | 0 | io -4 0xFF380000 0x00010000 | |
| | Input Ctrl | high | 0xFF380000 | [0, 0] | 1 | io -4 0xFF380000 0x00010001 | |
| | | disable | 0xFF388030 | [0, 0] | 0 | io -4 0xFF388030 0x00010000 | |
| | Schmitt | enable | 0xFF388030 | [0, 0] | 1 | io -4 0xFF388030 0x00010001 | |
| | | disable | 0xFF388058 | [0, 0] | 0 | io -4 0xFF388058 0x00010000 | |
| | Drive Strength | enable | 0xFF388058 | [0, 0] | 1 | io -4 0xFF388058 0x00010001 | |
| | | level0 | 0xFF388010 | [0, 5] | 1 | io -4 0xFF388010 0x003F0001 | |
| | | level1 | 0xFF388010 | [0, 5] | 3 | io -4 0xFF388010 0x003F0003 | |
| | | level2 | 0xFF388010 | [0, 5] | 7 | io -4 0xFF388010 0x003F0007 | |
| | | level3 | 0xFF388010 | [0, 5] | 15 | io -4 0xFF388010 0x003F000F | |
| GPIO0A1 | IOMUX | level4 | 0xFF388010 | [0, 5] | 31 | io -4 0xFF388010 0x003F001F | |
| | | level5 | 0xFF388010 | [0, 5] | 63 | io -4 0xFF388010 0x003F003F | |
| | ExtPort | read_gpio_data_in_input_mode | 0xFF380070 | [0, 0] | | io -4 0xFF380070 | |
| | IOMUX | UART0_TX_M0 | 0xFF388000 | [4, 6] | 1 | io -4 0xFF388000 0x00700010 | |
| | | PWM2_M0 | 0xFF388000 | [4, 6] | 2 | io -4 0xFF388000 0x00700020 | |
| | | TEST_CLK6_OUT | 0xFF388000 | [4, 6] | 3 | io -4 0xFF388000 0x00700030 | |
| | | GPIO0_A1 | 0xFF388000 | [4, 6] | 0 | io -4 0xFF388000 0x00700000 | |
| | Pull | normal | 0xFF388038 | [2, 3] | 0 | io -4 0xFF388038 0x000C0000 | |
| | | pullup | 0xFF388038 | [2, 3] | 1 | io -4 0xFF388038 0x000C0004 | |
| | | pulldown | 0xFF388038 | [2, 3] | 2 | io -4 0xFF388038 0x000C0008 | |
| | I/O Mode | input | 0xFF380008 | [1, 1] | 0 | io -4 0xFF380008 0x00020000 | |
| | | output | 0xFF380008 | [1, 1] | 1 | io -4 0xFF380008 0x00020002 | |
| | Output | low | 0xFF380000 | [1, 1] | 0 | io -4 0xFF380000 0x00020000 | |
| | | high | 0xFF380000 | [1, 1] | 1 | io -4 0xFF380000 0x00020002 | |
| | Input Ctrl | disable | 0xFF388030 | [1, 1] | 0 | io -4 0xFF388030 0x00020000 | |
| | | enable | 0xFF388030 | [1, 1] | 1 | io -4 0xFF388030 0x00020002 | |
| | Schmitt | disable | 0xFF388058 | [1, 1] | 0 | io -4 0xFF388058 0x00020000 | |
| | | enable | 0xFF388058 | [1, 1] | 1 | io -4 0xFF388058 0x00020002 | |
| | Drive Strength | level0 | 0xFF388010 | [8, 13] | 1 | io -4 0xFF388010 0x3F000100 | |
| | | level1 | 0xFF388010 | [8, 13] | 3 | io -4 0xFF388010 0x3F000300 | |
| | | level2 | 0xFF388010 | [8, 13] | 7 | io -4 0xFF388010 0x3F000700 | |
| | | level3 | 0xFF388010 | [8, 13] | 15 | io -4 0xFF388010 0x3F000F00 | |
| | | level4 | 0xFF388010 | [8, 13] | 31 | io -4 0xFF388010 0x3F001F00 | |
| | | level5 | 0xFF388010 | [8, 13] | 63 | io -4 0xFF388010 0x3F003F00 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|--------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIOA2 | ExtPort | read gpio data in input mode | 0xFF380070 | [1, 1] | | io -4 0xFF380070 | |
| | IOMUX | PWM3_IR_M0 | 0xFF388000 | [8, 10] | 1 | io -4 0xFF388000 0x07000100 | |
| | | TEST_CLK7_OUT | 0xFF388000 | [8, 10] | 3 | io -4 0xFF388000 0x07000300 | |
| | | GPIO0_A2 | 0xFF388000 | [8, 10] | 0 | io -4 0xFF388000 0x07000000 | |
| | Pull | normal | 0xFF388038 | [4, 5] | 0 | io -4 0xFF388038 0x00300000 | |
| | | pullup | 0xFF388038 | [4, 5] | 1 | io -4 0xFF388038 0x00300010 | |
| | | pulldown | 0xFF388038 | [4, 5] | 2 | io -4 0xFF388038 0x00300020 | |
| | I/O Mode | input | 0xFF380008 | [2, 2] | 0 | io -4 0xFF380008 0x00040000 | |
| | | output | 0xFF380008 | [2, 2] | 1 | io -4 0xFF380008 0x00040004 | |
| | Output | low | 0xFF380000 | [2, 2] | 0 | io -4 0xFF380000 0x00040000 | |
| | | high | 0xFF380000 | [2, 2] | 1 | io -4 0xFF380000 0x00040004 | |
| | Input Ctrl | disable | 0xFF388030 | [2, 2] | 0 | io -4 0xFF388030 0x00040000 | |
| | | enable | 0xFF388030 | [2, 2] | 1 | io -4 0xFF388030 0x00040004 | |
| | Schmitt | disable | 0xFF388058 | [2, 2] | 0 | io -4 0xFF388058 0x00040000 | |
| | | enable | 0xFF388058 | [2, 2] | 1 | io -4 0xFF388058 0x00040004 | |
| | Drive Strength | level0 | 0xFF388014 | [0, 5] | 1 | io -4 0xFF388014 0x003F0001 | |
| | | level1 | 0xFF388014 | [0, 5] | 3 | io -4 0xFF388014 0x003F0003 | |
| | | level2 | 0xFF388014 | [0, 5] | 7 | io -4 0xFF388014 0x003F0007 | |
| | | level3 | 0xFF388014 | [0, 5] | 15 | io -4 0xFF388014 0x003F000F | |
| | | level4 | 0xFF388014 | [0, 5] | 31 | io -4 0xFF388014 0x003F001F | |
| | | level5 | 0xFF388014 | [0, 5] | 63 | io -4 0xFF388014 0x003F003F | |
| | ExtPort | read gpio data in input mode | 0xFF380070 | [2, 2] | | io -4 0xFF380070 | |
| GPIOA3 | IOMUX | PMIC_SLEEP_M1 | 0xFF388000 | [12, 14] | 1 | io -4 0xFF388000 0x70001000 | |
| | | GPIO0_A3 | 0xFF388000 | [12, 14] | 0 | io -4 0xFF388000 0x70000000 | |
| | | normal | 0xFF388038 | [6, 7] | 0 | io -4 0xFF388038 0x00C00000 | |
| | Pull | pullup | 0xFF388038 | [6, 7] | 1 | io -4 0xFF388038 0x00C00040 | |
| | | pulldown | 0xFF388038 | [6, 7] | 2 | io -4 0xFF388038 0x00C00080 | |
| | | input | 0xFF380008 | [3, 3] | 0 | io -4 0xFF380008 0x00080000 | |
| | I/O Mode | output | 0xFF380008 | [3, 3] | 1 | io -4 0xFF380008 0x00080008 | |
| | | low | 0xFF380000 | [3, 3] | 0 | io -4 0xFF380000 0x00080000 | |
| | Output | high | 0xFF380000 | [3, 3] | 1 | io -4 0xFF380000 0x00080008 | |
| | | disable | 0xFF388030 | [3, 3] | 0 | io -4 0xFF388030 0x00080000 | |
| | Input Ctrl | enable | 0xFF388030 | [3, 3] | 1 | io -4 0xFF388030 0x00080008 | |
| | | disable | 0xFF388058 | [3, 3] | 0 | io -4 0xFF388058 0x00080000 | |
| | Schmitt | enable | 0xFF388058 | [3, 3] | 1 | io -4 0xFF388058 0x00080008 | |
| | | level0 | 0xFF388014 | [8, 13] | 1 | io -4 0xFF388014 0x3F000100 | |
| | Drive Strength | level1 | 0xFF388014 | [8, 13] | 3 | io -4 0xFF388014 0x3F000300 | |
| | | level2 | 0xFF388014 | [8, 13] | 7 | io -4 0xFF388014 0x3F000700 | |
| | | level3 | 0xFF388014 | [8, 13] | 15 | io -4 0xFF388014 0x3F000F00 | |
| | | level4 | 0xFF388014 | [8, 13] | 31 | io -4 0xFF388014 0x3F001F00 | |
| | | level5 | 0xFF388014 | [8, 13] | 63 | io -4 0xFF388014 0x3F003F00 | |
| | ExtPort | read gpio data in input mode | 0xFF380070 | [3, 3] | | io -4 0xFF380070 | |
| | IOMUX | PMIC_SLEEP_M0 | 0xFF388004 | [0, 2] | 1 | io -4 0xFF388004 0x00070001 | |
| | | PWM1_M0 | 0xFF388004 | [0, 2] | 2 | io -4 0xFF388004 0x00070002 | |
| | | GPIO0_A4 | 0xFF388004 | [0, 2] | 0 | io -4 0xFF388004 0x00070000 | |
| | Pull | normal | 0xFF388038 | [8, 9] | 0 | io -4 0xFF388038 0x03000000 | |
| | | pullup | 0xFF388038 | [8, 9] | 1 | io -4 0xFF388038 0x03000100 | |
| | | pulldown | 0xFF388038 | [8, 9] | 2 | io -4 0xFF388038 0x03000200 | |
| | I/O Mode | input | 0xFF380008 | [4, 4] | 0 | io -4 0xFF380008 0x00100000 | |
| | | output | 0xFF380008 | [4, 4] | 1 | io -4 0xFF380008 0x00100010 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|--------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIOA4 | Output | low | 0xFF380000 | [4, 4] | 0 | io -4 0xFF380000 0x00100000 | |
| | | high | 0xFF380000 | [4, 4] | 1 | io -4 0xFF380000 0x00100010 | |
| | Input Ctrl | disable | 0xFF388030 | [4, 4] | 0 | io -4 0xFF388030 0x00100000 | |
| | | enable | 0xFF388030 | [4, 4] | 1 | io -4 0xFF388030 0x00100010 | |
| | Schmitt | disable | 0xFF388058 | [4, 4] | 0 | io -4 0xFF388058 0x00100000 | |
| | | enable | 0xFF388058 | [4, 4] | 1 | io -4 0xFF388058 0x00100010 | |
| | Drive Strength | level0 | 0xFF388018 | [0, 5] | 1 | io -4 0xFF388018 0x003F0001 | |
| | | level1 | 0xFF388018 | [0, 5] | 3 | io -4 0xFF388018 0x003F0003 | |
| | | level2 | 0xFF388018 | [0, 5] | 7 | io -4 0xFF388018 0x003F0007 | |
| | | level3 | 0xFF388018 | [0, 5] | 15 | io -4 0xFF388018 0x003F000F | |
| | | level4 | 0xFF388018 | [0, 5] | 31 | io -4 0xFF388018 0x003F001F | |
| | | level5 | 0xFF388018 | [0, 5] | 63 | io -4 0xFF388018 0x003F003F | |
| | ExtPort | read gpio data in input mode | 0xFF380070 | [4, 4] | | io -4 0xFF380070 | |
| GPIOA5 | IOMUX | I2C1_SCL_M0 | 0xFF388004 | [4, 6] | 1 | io -4 0xFF388004 0x00700010 | |
| | | UART1_RTSN_M0 | 0xFF388004 | [4, 6] | 2 | io -4 0xFF388004 0x00700020 | |
| | | PWM5_M0 | 0xFF388004 | [4, 6] | 3 | io -4 0xFF388004 0x00700030 | |
| | | GPIO0_A5 | 0xFF388004 | [4, 6] | 0 | io -4 0xFF388004 0x00700000 | |
| | Pull | normal | 0xFF388038 | [10, 11] | 0 | io -4 0xFF388038 0x0C000000 | |
| | | pullup | 0xFF388038 | [10, 11] | 1 | io -4 0xFF388038 0x0C000040 | |
| | | pulldown | 0xFF388038 | [10, 11] | 2 | io -4 0xFF388038 0x0C000080 | |
| | I/O Mode | input | 0xFF380008 | [5, 5] | 0 | io -4 0xFF380008 0x00200000 | |
| | | output | 0xFF380008 | [5, 5] | 1 | io -4 0xFF380008 0x00200020 | |
| | Output | low | 0xFF380000 | [5, 5] | 0 | io -4 0xFF380000 0x00200000 | |
| | | high | 0xFF380000 | [5, 5] | 1 | io -4 0xFF380000 0x00200020 | |
| | Input Ctrl | disable | 0xFF388030 | [5, 5] | 0 | io -4 0xFF388030 0x00200000 | |
| | | enable | 0xFF388030 | [5, 5] | 1 | io -4 0xFF388030 0x00200020 | |
| | Schmitt | disable | 0xFF388058 | [5, 5] | 0 | io -4 0xFF388058 0x00200000 | |
| | | enable | 0xFF388058 | [5, 5] | 1 | io -4 0xFF388058 0x00200020 | |
| | Drive Strength | level0 | 0xFF388018 | [8, 13] | 1 | io -4 0xFF388018 0x3F000100 | |
| | | level1 | 0xFF388018 | [8, 13] | 3 | io -4 0xFF388018 0x3F000300 | |
| | | level2 | 0xFF388018 | [8, 13] | 7 | io -4 0xFF388018 0x3F000700 | |
| | | level3 | 0xFF388018 | [8, 13] | 15 | io -4 0xFF388018 0x3F000F00 | |
| | | level4 | 0xFF388018 | [8, 13] | 31 | io -4 0xFF388018 0x3F001F00 | |
| | | level5 | 0xFF388018 | [8, 13] | 63 | io -4 0xFF388018 0x3F003F00 | |
| | ExtPort | read gpio data in input mode | 0xFF380070 | [5, 5] | | io -4 0xFF380070 | |
| GPIOA6 | IOMUX | I2C1_SDA_M0 | 0xFF388004 | [8, 10] | 1 | io -4 0xFF388004 0x07000100 | |
| | | UART1_CTSN_M0 | 0xFF388004 | [8, 10] | 2 | io -4 0xFF388004 0x07000200 | |
| | | PWM6_M0 | 0xFF388004 | [8, 10] | 3 | io -4 0xFF388004 0x07000300 | |
| | | GPIO0_A6 | 0xFF388004 | [8, 10] | 0 | io -4 0xFF388004 0x07000000 | |
| | Pull | normal | 0xFF388038 | [12, 13] | 0 | io -4 0xFF388038 0x30000000 | |
| | | pullup | 0xFF388038 | [12, 13] | 1 | io -4 0xFF388038 0x30000100 | |
| | | pulldown | 0xFF388038 | [12, 13] | 2 | io -4 0xFF388038 0x30000200 | |
| | I/O Mode | input | 0xFF380008 | [6, 6] | 0 | io -4 0xFF380008 0x00400000 | |
| | | output | 0xFF380008 | [6, 6] | 1 | io -4 0xFF380008 0x00400040 | |
| | Output | low | 0xFF380000 | [6, 6] | 0 | io -4 0xFF380000 0x00400000 | |
| | | high | 0xFF380000 | [6, 6] | 1 | io -4 0xFF380000 0x00400040 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO000 | Input Ctrl | disable | 0xFF388030 | [6,6] | 0 | io -4 0xFF388030 0x00400000 | |
| | | enable | 0xFF388030 | [6,6] | 1 | io -4 0xFF388030 0x00400040 | |
| | Schmitt | disable | 0xFF388058 | [6,6] | 0 | io -4 0xFF388058 0x00400000 | |
| | | enable | 0xFF388058 | [6,6] | 1 | io -4 0xFF388058 0x00400040 | |
| | Drive Strength | level0 | 0xFF38801C | [0,5] | 1 | io -4 0xFF38801C 0x003F0001 | |
| | | level1 | 0xFF38801C | [0,5] | 3 | io -4 0xFF38801C 0x003F0003 | |
| | | level2 | 0xFF38801C | [0,5] | 7 | io -4 0xFF38801C 0x003F0007 | |
| | | level3 | 0xFF38801C | [0,5] | 15 | io -4 0xFF38801C 0x003F000F | |
| | | level4 | 0xFF38801C | [0,5] | 31 | io -4 0xFF38801C 0x003F001F | |
| | | level5 | 0xFF38801C | [0,5] | 63 | io -4 0xFF38801C 0x003F003F | |
| | ExtPort | read gpio data in input mode | 0xFF380070 | [6,6] | | io -4 0xFF380070 | |
| GPIO1A0 | IOMUX | UART3_TX_M0 | 0xFF538000 | [0,2] | 1 | io -4 0xFF538000 0x00070001 | |
| | | I2C2_SCL_M0 | 0xFF538000 | [0,2] | 2 | io -4 0xFF538000 0x00070002 | |
| | | PWM7_IR_M0 | 0xFF538000 | [0,2] | 3 | io -4 0xFF538000 0x00070003 | |
| | | GPIO1_A0 | 0xFF538000 | [0,2] | 0 | io -4 0xFF538000 0x00070000 | |
| | Pull | normal | 0xFF5381C0 | [0,1] | 0 | io -4 0xFF5381C0 0x00030000 | |
| | | pullup | 0xFF5381C0 | [0,1] | 1 | io -4 0xFF5381C0 0x00030001 | |
| | | pulldown | 0xFF5381C0 | [0,1] | 2 | io -4 0xFF5381C0 0x00030002 | |
| | I/O Mode | input | 0xFF530008 | [0,0] | 0 | io -4 0xFF530008 0x00010000 | |
| | | output | 0xFF530008 | [0,0] | 1 | io -4 0xFF530008 0x00010001 | |
| | Output | low | 0xFF530000 | [0,0] | 0 | io -4 0xFF530000 0x00010000 | |
| | | high | 0xFF530000 | [0,0] | 1 | io -4 0xFF530000 0x00010001 | |
| | Input Ctrl | disable | 0xFF538180 | [0,0] | 0 | io -4 0xFF538180 0x00010000 | |
| | | enable | 0xFF538180 | [0,0] | 1 | io -4 0xFF538180 0x00010001 | |
| | Schmitt | disable | 0xFF538280 | [0,0] | 0 | io -4 0xFF538280 0x00010000 | |
| | | enable | 0xFF538280 | [0,0] | 1 | io -4 0xFF538280 0x00010001 | |
| | Drive Strength | level0 | 0xFF538080 | [0,5] | 1 | io -4 0xFF538080 0x003F0001 | |
| | | level1 | 0xFF538080 | [0,5] | 3 | io -4 0xFF538080 0x003F0003 | |
| | | level2 | 0xFF538080 | [0,5] | 7 | io -4 0xFF538080 0x003F0007 | |
| | | level3 | 0xFF538080 | [0,5] | 15 | io -4 0xFF538080 0x003F000F | |
| | | level4 | 0xFF538080 | [0,5] | 31 | io -4 0xFF538080 0x003F001F | |
| | | level5 | 0xFF538080 | [0,5] | 63 | io -4 0xFF538080 0x003F003F | |
| | ExtPort | read gpio data in input mode | 0xFF530070 | [0,0] | | io -4 0xFF530070 | |
| GPIO1A1 | IOMUX | UART3_RX_M0 | 0xFF538000 | [4,6] | 1 | io -4 0xFF538000 0x00700010 | |
| | | I2C2_SDA_M0 | 0xFF538000 | [4,6] | 2 | io -4 0xFF538000 0x00700020 | |
| | | PMU_DEBUG | 0xFF538000 | [4,6] | 3 | io -4 0xFF538000 0x00700030 | |
| | | PWM4_M0 | 0xFF538000 | [4,6] | 4 | io -4 0xFF538000 0x00700040 | |
| | | GPIO1_A1 | 0xFF538000 | [4,6] | 0 | io -4 0xFF538000 0x00700000 | |
| | Pull | normal | 0xFF5381C0 | [2,3] | 0 | io -4 0xFF5381C0 0x000C0000 | |
| | | pullup | 0xFF5381C0 | [2,3] | 1 | io -4 0xFF5381C0 0x000C0004 | |
| | | pulldown | 0xFF5381C0 | [2,3] | 2 | io -4 0xFF5381C0 0x000C0008 | |
| | I/O Mode | input | 0xFF530008 | [1,1] | 0 | io -4 0xFF530008 0x00020000 | |
| | | output | 0xFF530008 | [1,1] | 1 | io -4 0xFF530008 0x00020002 | |
| | Output | low | 0xFF530000 | [1,1] | 0 | io -4 0xFF530000 0x00020000 | |
| | | high | 0xFF530000 | [1,1] | 1 | io -4 0xFF530000 0x00020002 | |
| | Input Ctrl | disable | 0xFF538180 | [1,1] | 0 | io -4 0xFF538180 0x00020000 | |
| | | enable | 0xFF538180 | [1,1] | 1 | io -4 0xFF538180 0x00020002 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| | Schmitt | disable | 0xFF538280 | [1, 1] | 0 | io -4 0xFF538280 0x00020000 | |
| | | enable | 0xFF538280 | [1, 1] | 1 | io -4 0xFF538280 0x00020002 | |
| | Drive Strength | level0 | 0xFF538080 | [8, 13] | 1 | io -4 0xFF538080 0x3F000100 | |
| | | level1 | 0xFF538080 | [8, 13] | 3 | io -4 0xFF538080 0x3F000300 | |
| | | level2 | 0xFF538080 | [8, 13] | 7 | io -4 0xFF538080 0x3F000700 | |
| | | level3 | 0xFF538080 | [8, 13] | 15 | io -4 0xFF538080 0x3F000F00 | |
| | | level4 | 0xFF538080 | [8, 13] | 31 | io -4 0xFF538080 0x3F001F00 | |
| | | level5 | 0xFF538080 | [8, 13] | 63 | io -4 0xFF538080 0x3F003F00 | |
| | ExtPort | read gpio data in input mode | 0xFF530070 | [1, 1] | | io -4 0xFF530070 | |
| GPIO1A2 | IOMUX | PWM0_M0 | 0xFF538000 | [8, 10] | 1 | io -4 0xFF538000 0x07000100 | |
| | | AVS_ARM | 0xFF538000 | [8, 10] | 2 | io -4 0xFF538000 0x07000200 | |
| | | VICAP_DO_M1 | 0xFF538000 | [8, 10] | 3 | io -4 0xFF538000 0x07000300 | |
| | | GPIO1_A2 | 0xFF538000 | [8, 10] | 0 | io -4 0xFF538000 0x07000000 | |
| | Pull | normal | 0xFF5381C0 | [4, 5] | 0 | io -4 0xFF5381C0 0x00300000 | |
| | | pullup | 0xFF5381C0 | [4, 5] | 1 | io -4 0xFF5381C0 0x00300010 | |
| | | pulldown | 0xFF5381C0 | [4, 5] | 2 | io -4 0xFF5381C0 0x00300020 | |
| | I/O Mode | input | 0xFF530008 | [2, 2] | 0 | io -4 0xFF530008 0x00040000 | |
| | | output | 0xFF530008 | [2, 2] | 1 | io -4 0xFF530008 0x00040004 | |
| | Output | low | 0xFF530000 | [2, 2] | 0 | io -4 0xFF530000 0x00040000 | |
| | | high | 0xFF530000 | [2, 2] | 1 | io -4 0xFF530000 0x00040004 | |
| | Input Ctrl | disable | 0xFF538180 | [2, 2] | 0 | io -4 0xFF538180 0x00040000 | |
| | | enable | 0xFF538180 | [2, 2] | 1 | io -4 0xFF538180 0x00040004 | |
| | Schmitt | disable | 0xFF538280 | [2, 2] | 0 | io -4 0xFF538280 0x00040000 | |
| | | enable | 0xFF538280 | [2, 2] | 1 | io -4 0xFF538280 0x00040004 | |
| | Drive Strength | level0 | 0xFF538084 | [0, 5] | 1 | io -4 0xFF538084 0x003F0001 | |
| | | level1 | 0xFF538084 | [0, 5] | 3 | io -4 0xFF538084 0x003F0003 | |
| | | level2 | 0xFF538084 | [0, 5] | 7 | io -4 0xFF538084 0x003F0007 | |
| | | level3 | 0xFF538084 | [0, 5] | 15 | io -4 0xFF538084 0x003F000F | |
| | | level4 | 0xFF538084 | [0, 5] | 31 | io -4 0xFF538084 0x003F001F | |
| | | level5 | 0xFF538084 | [0, 5] | 63 | io -4 0xFF538084 0x003F003F | |
| | ExtPort | read gpio data in input mode | 0xFF530070 | [2, 2] | | io -4 0xFF530070 | |
| GPIO1A3 | IOMUX | UART1_TX_M0 | 0xFF538000 | [12, 14] | 1 | io -4 0xFF538000 0x70001000 | |
| | | I2C0_SCL_M0 | 0xFF538000 | [12, 14] | 2 | io -4 0xFF538000 0x70002000 | |
| | | GPIO1_A3 | 0xFF538000 | [12, 14] | 0 | io -4 0xFF538000 0x70000000 | |
| | Pull | normal | 0xFF5381C0 | [6, 7] | 0 | io -4 0xFF5381C0 0x00C00000 | |
| | | pullup | 0xFF5381C0 | [6, 7] | 1 | io -4 0xFF5381C0 0x00C00040 | |
| | | pulldown | 0xFF5381C0 | [6, 7] | 2 | io -4 0xFF5381C0 0x00C00080 | |
| | I/O Mode | input | 0xFF530008 | [3, 3] | 0 | io -4 0xFF530008 0x00080000 | |
| | | output | 0xFF530008 | [3, 3] | 1 | io -4 0xFF530008 0x00080008 | |
| | Output | low | 0xFF530000 | [3, 3] | 0 | io -4 0xFF530000 0x00080000 | |
| | | high | 0xFF530000 | [3, 3] | 1 | io -4 0xFF530000 0x00080008 | |
| | Input Ctrl | disable | 0xFF538180 | [3, 3] | 0 | io -4 0xFF538180 0x00080000 | |
| | | enable | 0xFF538180 | [3, 3] | 1 | io -4 0xFF538180 0x00080008 | |
| | Schmitt | disable | 0xFF538280 | [3, 3] | 0 | io -4 0xFF538280 0x00080000 | |
| | | enable | 0xFF538280 | [3, 3] | 1 | io -4 0xFF538280 0x00080008 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| | Drive Strength | level0 | 0xFF538084 | [8, 13] | 1 | io -4 0xFF538084 0x3F000100 | |
| | | level1 | 0xFF538084 | [8, 13] | 3 | io -4 0xFF538084 0x3F000300 | |
| | | level2 | 0xFF538084 | [8, 13] | 7 | io -4 0xFF538084 0x3F000700 | |
| | | level3 | 0xFF538084 | [8, 13] | 15 | io -4 0xFF538084 0x3F000F00 | |
| | | level4 | 0xFF538084 | [8, 13] | 31 | io -4 0xFF538084 0x3F001F00 | |
| | ExtPort | read gpio data in input mode | 0xFF530070 | [3, 3] | | io -4 0xFF530070 | |
| GPIO1A4 | IOMUX | UART1_RX_M0 | 0xFF538004 | [0, 2] | 1 | io -4 0xFF538004 0x00070001 | |
| | | I2C0_SDA_M0 | 0xFF538004 | [0, 2] | 2 | io -4 0xFF538004 0x00070002 | |
| | | GPIO1_A4 | 0xFF538004 | [0, 2] | 0 | io -4 0xFF538004 0x00070000 | |
| | Pull | normal | 0xFF5381C0 | [8, 9] | 0 | io -4 0xFF5381C0 0x03000000 | |
| | | pullup | 0xFF5381C0 | [8, 9] | 1 | io -4 0xFF5381C0 0x03000100 | |
| | | pulldown | 0xFF5381C0 | [8, 9] | 2 | io -4 0xFF5381C0 0x03000200 | |
| | I/O Mode | input | 0xFF530008 | [4, 4] | 0 | io -4 0xFF530008 0x00100000 | |
| | | output | 0xFF530008 | [4, 4] | 1 | io -4 0xFF530008 0x00100010 | |
| | Output | low | 0xFF530000 | [4, 4] | 0 | io -4 0xFF530000 0x00100000 | |
| | | high | 0xFF530000 | [4, 4] | 1 | io -4 0xFF530000 0x00100010 | |
| | Input Ctrl | disable | 0xFF538180 | [4, 4] | 0 | io -4 0xFF538180 0x00100000 | |
| | | enable | 0xFF538180 | [4, 4] | 1 | io -4 0xFF538180 0x00100010 | |
| | Schmitt | disable | 0xFF538280 | [4, 4] | 0 | io -4 0xFF538280 0x00100000 | |
| | | enable | 0xFF538280 | [4, 4] | 1 | io -4 0xFF538280 0x00100010 | |
| | Drive Strength | level0 | 0xFF538088 | [0, 5] | 1 | io -4 0xFF538088 0x003F0001 | |
| | | level1 | 0xFF538088 | [0, 5] | 3 | io -4 0xFF538088 0x003F0003 | |
| | | level2 | 0xFF538088 | [0, 5] | 7 | io -4 0xFF538088 0x003F0007 | |
| | | level3 | 0xFF538088 | [0, 5] | 15 | io -4 0xFF538088 0x003F000F | |
| | | level4 | 0xFF538088 | [0, 5] | 31 | io -4 0xFF538088 0x003F001F | |
| | ExtPort | read gpio data in input mode | 0xFF530070 | [4, 4] | | io -4 0xFF530070 | |
| GPIO1B0 | IOMUX | UART4_RX_M0 | 0xFF538008 | [0, 2] | 1 | io -4 0xFF538008 0x00070001 | |
| | | PWM3_IR_M1 | 0xFF538008 | [0, 2] | 2 | io -4 0xFF538008 0x00070002 | |
| | | GPIO1_B0 | 0xFF538008 | [0, 2] | 0 | io -4 0xFF538008 0x00070000 | |
| | Pull | normal | 0xFF5381C4 | [0, 1] | 0 | io -4 0xFF5381C4 0x00030000 | |
| | | pullup | 0xFF5381C4 | [0, 1] | 1 | io -4 0xFF5381C4 0x00030001 | |
| | | pulldown | 0xFF5381C4 | [0, 1] | 2 | io -4 0xFF5381C4 0x00030002 | |
| | I/O Mode | input | 0xFF530008 | [8, 8] | 0 | io -4 0xFF530008 0x01000000 | |
| | | output | 0xFF530008 | [8, 8] | 1 | io -4 0xFF530008 0x01000100 | |
| | Output | low | 0xFF530000 | [8, 8] | 0 | io -4 0xFF530000 0x01000000 | |
| | | high | 0xFF530000 | [8, 8] | 1 | io -4 0xFF530000 0x01000100 | |
| | Input Ctrl | disable | 0xFF538184 | [0, 0] | 0 | io -4 0xFF538184 0x00010000 | |
| | | enable | 0xFF538184 | [0, 0] | 1 | io -4 0xFF538184 0x00010001 | |
| | Schmitt | disable | 0xFF538284 | [0, 0] | 0 | io -4 0xFF538284 0x00010000 | |
| | | enable | 0xFF538284 | [0, 0] | 1 | io -4 0xFF538284 0x00010001 | |
| | Drive Strength | level0 | 0xFF538090 | [0, 5] | 1 | io -4 0xFF538090 0x003F0001 | |
| | | level1 | 0xFF538090 | [0, 5] | 3 | io -4 0xFF538090 0x003F0003 | |
| | | level2 | 0xFF538090 | [0, 5] | 7 | io -4 0xFF538090 0x003F0007 | |
| | | level3 | 0xFF538090 | [0, 5] | 15 | io -4 0xFF538090 0x003F000F | |
| | | level4 | 0xFF538090 | [0, 5] | 31 | io -4 0xFF538090 0x003F001F | |
| | ExtPort | read gpio data in input mode | 0xFF530070 | [8, 8] | | io -4 0xFF530070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO1B1 | IOMUX | UART4_TX_M0 | 0xFF538008 | [4, 6] | 1 | io -4 0xFF538008 0x00700010 | |
| | | PWM7_IR_M1 | 0xFF538008 | [4, 6] | 2 | io -4 0xFF538008 0x00700020 | |
| | | SPI1_CSIN_M0 | 0xFF538008 | [4, 6] | 3 | io -4 0xFF538008 0x00700030 | |
| | | VICAP_D1_M1 | 0xFF538008 | [4, 6] | 4 | io -4 0xFF538008 0x00700040 | |
| | | GPIO1_B1 | 0xFF538008 | [4, 6] | 0 | io -4 0xFF538008 0x00700000 | |
| | Pull | normal | 0xFF5381C4 | [2, 3] | 0 | io -4 0xFF5381C4 0x000C0000 | |
| | | pullup | 0xFF5381C4 | [2, 3] | 1 | io -4 0xFF5381C4 0x000C0004 | |
| | | pulldown | 0xFF5381C4 | [2, 3] | 2 | io -4 0xFF5381C4 0x000C0008 | |
| | I/O Mode | input | 0xFF530008 | [9, 9] | 0 | io -4 0xFF530008 0x02000000 | |
| | | output | 0xFF530008 | [9, 9] | 1 | io -4 0xFF530008 0x02000200 | |
| | Output | low | 0xFF530000 | [9, 9] | 0 | io -4 0xFF530000 0x02000000 | |
| | | high | 0xFF530000 | [9, 9] | 1 | io -4 0xFF530000 0x02000200 | |
| | Input Ctrl | disable | 0xFF538184 | [1, 1] | 0 | io -4 0xFF538184 0x00020000 | |
| | | enable | 0xFF538184 | [1, 1] | 1 | io -4 0xFF538184 0x00020002 | |
| | Schmitt | disable | 0xFF538284 | [1, 1] | 0 | io -4 0xFF538284 0x00020000 | |
| | | enable | 0xFF538284 | [1, 1] | 1 | io -4 0xFF538284 0x00020002 | |
| | Drive Strength | level0 | 0xFF538090 | [8, 13] | 1 | io -4 0xFF538090 0x3F000100 | |
| | | level1 | 0xFF538090 | [8, 13] | 3 | io -4 0xFF538090 0x3F000300 | |
| | | level2 | 0xFF538090 | [8, 13] | 7 | io -4 0xFF538090 0x3F000700 | |
| | | level3 | 0xFF538090 | [8, 13] | 15 | io -4 0xFF538090 0x3F000F00 | |
| | | level4 | 0xFF538090 | [8, 13] | 31 | io -4 0xFF538090 0x3F001F00 | |
| | | level5 | 0xFF538090 | [8, 13] | 63 | io -4 0xFF538090 0x3F003F00 | |
| | ExtPort | read gpio data in input mode | 0xFF530070 | [9, 9] | | io -4 0xFF530070 | |
| GPIO1B2 | IOMUX | A7_JTAG_TCK_M1 | 0xFF538008 | [8, 10] | 1 | io -4 0xFF538008 0x07000100 | |
| | | UART2_TX_M1 | 0xFF538008 | [8, 10] | 2 | io -4 0xFF538008 0x07000200 | |
| | | HPMCU_JTAG_TCK_M0 | 0xFF538008 | [8, 10] | 3 | io -4 0xFF538008 0x07000300 | |
| | | LPMCU_JTAG_TCK_M0 | 0xFF538008 | [8, 10] | 4 | io -4 0xFF538008 0x07000400 | |
| | | GPIO1_B2 | 0xFF538008 | [8, 10] | 0 | io -4 0xFF538008 0x07000000 | |
| | Pull | normal | 0xFF5381C4 | [4, 5] | 0 | io -4 0xFF5381C4 0x00300000 | |
| | | pullup | 0xFF5381C4 | [4, 5] | 1 | io -4 0xFF5381C4 0x00300010 | |
| | | pulldown | 0xFF5381C4 | [4, 5] | 2 | io -4 0xFF5381C4 0x00300020 | |
| | I/O Mode | input | 0xFF530008 | [10, 10] | 0 | io -4 0xFF530008 0x04000000 | |
| | | output | 0xFF530008 | [10, 10] | 1 | io -4 0xFF530008 0x04000400 | |
| | Output | low | 0xFF530000 | [10, 10] | 0 | io -4 0xFF530000 0x04000000 | |
| | | high | 0xFF530000 | [10, 10] | 1 | io -4 0xFF530000 0x04000400 | |
| | Input Ctrl | disable | 0xFF538184 | [2, 2] | 0 | io -4 0xFF538184 0x00040000 | |
| | | enable | 0xFF538184 | [2, 2] | 1 | io -4 0xFF538184 0x00040004 | |
| | Schmitt | disable | 0xFF538284 | [2, 2] | 0 | io -4 0xFF538284 0x00040000 | |
| | | enable | 0xFF538284 | [2, 2] | 1 | io -4 0xFF538284 0x00040004 | |
| | Drive Strength | level0 | 0xFF538094 | [0, 5] | 1 | io -4 0xFF538094 0x003F0001 | |
| | | level1 | 0xFF538094 | [0, 5] | 3 | io -4 0xFF538094 0x003F0003 | |
| | | level2 | 0xFF538094 | [0, 5] | 7 | io -4 0xFF538094 0x003F0007 | |
| | | level3 | 0xFF538094 | [0, 5] | 15 | io -4 0xFF538094 0x003F000F | |
| | | level4 | 0xFF538094 | [0, 5] | 31 | io -4 0xFF538094 0x003F001F | |
| | | level5 | 0xFF538094 | [0, 5] | 63 | io -4 0xFF538094 0x003F003F | |
| | ExtPort | read gpio data in input mode | 0xFF530070 | [10, 10] | | io -4 0xFF530070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO1B3 | IOMUX | A7_JTAG_TMS_M1 | 0xFF538008 | [12, 14] | 1 | io -4 0xFF538008 0x70001000 | |
| | | UART2_RX_M1 | 0xFF538008 | [12, 14] | 2 | io -4 0xFF538008 0x70002000 | |
| | | HPMCU_JTAG_TMS_M0 | 0xFF538008 | [12, 14] | 3 | io -4 0xFF538008 0x70003000 | |
| | | LPMCU_JTAG_TMS_M0 | 0xFF538008 | [12, 14] | 4 | io -4 0xFF538008 0x70004000 | |
| | | GPIO1_B3 | 0xFF538008 | [12, 14] | 0 | io -4 0xFF538008 0x70000000 | |
| | Pull | normal | 0xFF5381C4 | [6, 7] | 0 | io -4 0xFF5381C4 0x00C00000 | |
| | | pullup | 0xFF5381C4 | [6, 7] | 1 | io -4 0xFF5381C4 0x00C00040 | |
| | | pulldown | 0xFF5381C4 | [6, 7] | 2 | io -4 0xFF5381C4 0x00C00080 | |
| | I/O Mode | input | 0xFF530008 | [11, 11] | 0 | io -4 0xFF530008 0x08000000 | |
| | | output | 0xFF530008 | [11, 11] | 1 | io -4 0xFF530008 0x08000800 | |
| | Output | low | 0xFF530000 | [11, 11] | 0 | io -4 0xFF530000 0x08000000 | |
| | | high | 0xFF530000 | [11, 11] | 1 | io -4 0xFF530000 0x08000800 | |
| | Input Ctrl | disable | 0xFF538184 | [3, 3] | 0 | io -4 0xFF538184 0x00080000 | |
| | | enable | 0xFF538184 | [3, 3] | 1 | io -4 0xFF538184 0x00080008 | |
| | Schmitt | disable | 0xFF538284 | [3, 3] | 0 | io -4 0xFF538284 0x00080000 | |
| | | enable | 0xFF538284 | [3, 3] | 1 | io -4 0xFF538284 0x00080008 | |
| | Drive Strength | level0 | 0xFF538094 | [8, 13] | 1 | io -4 0xFF538094 0x3F000100 | |
| | | level1 | 0xFF538094 | [8, 13] | 3 | io -4 0xFF538094 0x3F000300 | |
| | | level2 | 0xFF538094 | [8, 13] | 7 | io -4 0xFF538094 0x3F000700 | |
| | | level3 | 0xFF538094 | [8, 13] | 15 | io -4 0xFF538094 0x3F000F00 | |
| | | level4 | 0xFF538094 | [8, 13] | 31 | io -4 0xFF538094 0x3F001F00 | |
| | | level5 | 0xFF538094 | [8, 13] | 63 | io -4 0xFF538094 0x3F003F00 | |
| | ExtPort | read gpio data in input mode | 0xFF530070 | [11, 11] | | io -4 0xFF530070 | |
| GPIO1C0 | IOMUX | LCD_D7 | 0xFF538010 | [0, 2] | 1 | io -4 0xFF538010 0x00070001 | |
| | | VICAP_D2_M1 | 0xFF538010 | [0, 2] | 2 | io -4 0xFF538010 0x00070002 | |
| | | PWM2_M2 | 0xFF538010 | [0, 2] | 3 | io -4 0xFF538010 0x00070003 | |
| | | SPI0_CSON_M0 | 0xFF538010 | [0, 2] | 4 | io -4 0xFF538010 0x00070004 | |
| | | SDMMC1_D1_M1 | 0xFF538010 | [0, 2] | 5 | io -4 0xFF538010 0x00070005 | |
| | | GPIO1_C0 | 0xFF538010 | [0, 2] | 0 | io -4 0xFF538010 0x00070000 | |
| | Pull | normal | 0xFF5381C8 | [0, 1] | 0 | io -4 0xFF5381C8 0x00030000 | |
| | | pullup | 0xFF5381C8 | [0, 1] | 1 | io -4 0xFF5381C8 0x00030001 | |
| | | pulldown | 0xFF5381C8 | [0, 1] | 2 | io -4 0xFF5381C8 0x00030002 | |
| | I/O Mode | input | 0xFF53000C | [0, 0] | 0 | io -4 0xFF53000C 0x00010000 | |
| | | output | 0xFF53000C | [0, 0] | 1 | io -4 0xFF53000C 0x00010001 | |
| | Output | low | 0xFF530004 | [0, 0] | 0 | io -4 0xFF530004 0x00010000 | |
| | | high | 0xFF530004 | [0, 0] | 1 | io -4 0xFF530004 0x00010001 | |
| | Input Ctrl | disable | 0xFF538188 | [0, 0] | 0 | io -4 0xFF538188 0x00010000 | |
| | | enable | 0xFF538188 | [0, 0] | 1 | io -4 0xFF538188 0x00010001 | |
| | Schmitt | disable | 0xFF538288 | [0, 0] | 0 | io -4 0xFF538288 0x00010000 | |
| | | enable | 0xFF538288 | [0, 0] | 1 | io -4 0xFF538288 0x00010001 | |
| | Drive Strength | level0 | 0xFF5380A0 | [0, 5] | 1 | io -4 0xFF5380A0 0x003F0001 | |
| | | level1 | 0xFF5380A0 | [0, 5] | 3 | io -4 0xFF5380A0 0x003F0003 | |
| | | level2 | 0xFF5380A0 | [0, 5] | 7 | io -4 0xFF5380A0 0x003F0007 | |
| | | level3 | 0xFF5380A0 | [0, 5] | 15 | io -4 0xFF5380A0 0x003F000F | |
| | | level4 | 0xFF5380A0 | [0, 5] | 31 | io -4 0xFF5380A0 0x003F001F | |
| | | level5 | 0xFF5380A0 | [0, 5] | 63 | io -4 0xFF5380A0 0x003F003F | |
| | ExtPort | read gpio data in input mode | 0xFF530070 | [16, 16] | | io -4 0xFF530070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO1C1 | IOMUX | LCD D6 | 0xFF538010 | [4, 6] | 1 | io -4 0xFF538010 0x00700010 | |
| | | VICAP D3 M1 | 0xFF538010 | [4, 6] | 2 | io -4 0xFF538010 0x00700020 | |
| | | PWM4 M2 | 0xFF538010 | [4, 6] | 3 | io -4 0xFF538010 0x00700030 | |
| | | SPI0 CLK M0 | 0xFF538010 | [4, 6] | 4 | io -4 0xFF538010 0x00700040 | |
| | | SDMMC1 DO M1 | 0xFF538010 | [4, 6] | 5 | io -4 0xFF538010 0x00700050 | |
| | Pull | GPIO1 C1 | 0xFF538010 | [4, 6] | 0 | io -4 0xFF538010 0x00700000 | |
| | | normal | 0xFF5381C8 | [2, 3] | 0 | io -4 0xFF5381C8 0x000C0000 | |
| | | pullup | 0xFF5381C8 | [2, 3] | 1 | io -4 0xFF5381C8 0x000C0004 | |
| | I/O Mode | pulldown | 0xFF5381C8 | [2, 3] | 2 | io -4 0xFF5381C8 0x000C0008 | |
| | | input | 0xFF53000C | [1, 1] | 0 | io -4 0xFF53000C 0x00020000 | |
| | Output | output | 0xFF53000C | [1, 1] | 1 | io -4 0xFF53000C 0x00020002 | |
| | | low | 0xFF530004 | [1, 1] | 0 | io -4 0xFF530004 0x00020000 | |
| | Input Ctrl | high | 0xFF530004 | [1, 1] | 1 | io -4 0xFF530004 0x00020002 | |
| | | disable | 0xFF538188 | [1, 1] | 0 | io -4 0xFF538188 0x00020000 | |
| | Schmitt | enable | 0xFF538188 | [1, 1] | 1 | io -4 0xFF538188 0x00020002 | |
| | | disable | 0xFF538288 | [1, 1] | 0 | io -4 0xFF538288 0x00020000 | |
| | Drive Strength | enable | 0xFF538288 | [1, 1] | 1 | io -4 0xFF538288 0x00020002 | |
| | | level0 | 0xFF5380A0 | [8, 13] | 1 | io -4 0xFF5380A0 0x3F000100 | |
| | | level1 | 0xFF5380A0 | [8, 13] | 3 | io -4 0xFF5380A0 0x3F000300 | |
| | | level2 | 0xFF5380A0 | [8, 13] | 7 | io -4 0xFF5380A0 0x3F000700 | |
| | | level3 | 0xFF5380A0 | [8, 13] | 15 | io -4 0xFF5380A0 0x3F000F00 | |
| | | level4 | 0xFF5380A0 | [8, 13] | 31 | io -4 0xFF5380A0 0x3F001F00 | |
| | ExtPort | level5 | 0xFF5380A0 | [8, 13] | 63 | io -4 0xFF5380A0 0x3F003F00 | |
| | | read gpio data in input mode | 0xFF530070 | [17, 17] | | io -4 0xFF530070 | |
| GPIO1C2 | IOMUX | LCD D5 | 0xFF538010 | [8, 10] | 1 | io -4 0xFF538010 0x07000100 | |
| | | VICAP D4 M1 | 0xFF538010 | [8, 10] | 2 | io -4 0xFF538010 0x07000200 | |
| | | PWM5 M2 | 0xFF538010 | [8, 10] | 3 | io -4 0xFF538010 0x07000300 | |
| | | I2C4 SCL M1 | 0xFF538010 | [8, 10] | 4 | io -4 0xFF538010 0x07000400 | |
| | | SDMMC1 CLK M1 | 0xFF538010 | [8, 10] | 5 | io -4 0xFF538010 0x07000500 | |
| | | SPI0 MOSI M0 | 0xFF538010 | [8, 10] | 6 | io -4 0xFF538010 0x07000600 | |
| | | GPIO1 C2 | 0xFF538010 | [8, 10] | 0 | io -4 0xFF538010 0x07000000 | |
| | Pull | normal | 0xFF5381C8 | [4, 5] | 0 | io -4 0xFF5381C8 0x00300000 | |
| | | pullup | 0xFF5381C8 | [4, 5] | 1 | io -4 0xFF5381C8 0x00300010 | |
| | | pulldown | 0xFF5381C8 | [4, 5] | 2 | io -4 0xFF5381C8 0x00300020 | |
| | I/O Mode | input | 0xFF53000C | [2, 2] | 0 | io -4 0xFF53000C 0x00040000 | |
| | | output | 0xFF53000C | [2, 2] | 1 | io -4 0xFF53000C 0x00040004 | |
| | Output | low | 0xFF530004 | [2, 2] | 0 | io -4 0xFF530004 0x00040000 | |
| | | high | 0xFF530004 | [2, 2] | 1 | io -4 0xFF530004 0x00040004 | |
| | Input Ctrl | disable | 0xFF538188 | [2, 2] | 0 | io -4 0xFF538188 0x00040000 | |
| | | enable | 0xFF538188 | [2, 2] | 1 | io -4 0xFF538188 0x00040004 | |
| | Schmitt | disable | 0xFF538288 | [2, 2] | 0 | io -4 0xFF538288 0x00040000 | |
| | | enable | 0xFF538288 | [2, 2] | 1 | io -4 0xFF538288 0x00040004 | |
| | Drive Strength | level0 | 0xFF5380A4 | [0, 5] | 1 | io -4 0xFF5380A4 0x003F0001 | |
| | | level1 | 0xFF5380A4 | [0, 5] | 3 | io -4 0xFF5380A4 0x003F0003 | |
| | | level2 | 0xFF5380A4 | [0, 5] | 7 | io -4 0xFF5380A4 0x003F0007 | |
| | | level3 | 0xFF5380A4 | [0, 5] | 15 | io -4 0xFF5380A4 0x003F000F | |
| | | level4 | 0xFF5380A4 | [0, 5] | 31 | io -4 0xFF5380A4 0x003F001F | |
| | | level5 | 0xFF5380A4 | [0, 5] | 63 | io -4 0xFF5380A4 0x003F003F | |
| | ExtPort | read gpio data in input mode | 0xFF530070 | [18, 18] | | io -4 0xFF530070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO1C3 | IOMUX | LCD D4 | 0xFF538010 | [12, 14] | 1 | io -4 0xFF538010 0x70001000 | |
| | | VICAP D5 M1 | 0xFF538010 | [12, 14] | 2 | io -4 0xFF538010 0x70002000 | |
| | | PWM6 M2 | 0xFF538010 | [12, 14] | 3 | io -4 0xFF538010 0x70003000 | |
| | | I2C4 SDA M1 | 0xFF538010 | [12, 14] | 4 | io -4 0xFF538010 0x70004000 | |
| | | SDMMC1 CMD M1 | 0xFF538010 | [12, 14] | 5 | io -4 0xFF538010 0x70005000 | |
| | | SPI0 MISO M0 | 0xFF538010 | [12, 14] | 6 | io -4 0xFF538010 0x70006000 | |
| | Pull | GPIO1 C3 | 0xFF538010 | [12, 14] | 0 | io -4 0xFF538010 0x70000000 | |
| | | normal | 0xFF5381C8 | [6, 7] | 0 | io -4 0xFF5381C8 0x00C00000 | |
| | | pullup | 0xFF5381C8 | [6, 7] | 1 | io -4 0xFF5381C8 0x00C00040 | |
| | I/O Mode | pulldown | 0xFF5381C8 | [6, 7] | 2 | io -4 0xFF5381C8 0x00C00080 | |
| | | input | 0xFF53000C | [3, 3] | 0 | io -4 0xFF53000C 0x00080000 | |
| | Output | output | 0xFF53000C | [3, 3] | 1 | io -4 0xFF53000C 0x00080008 | |
| | | low | 0xFF530004 | [3, 3] | 0 | io -4 0xFF530004 0x00080000 | |
| | Input Ctrl | high | 0xFF530004 | [3, 3] | 1 | io -4 0xFF530004 0x00080008 | |
| | | disable | 0xFF538188 | [3, 3] | 0 | io -4 0xFF538188 0x00080000 | |
| | Schmitt | enable | 0xFF538188 | [3, 3] | 1 | io -4 0xFF538188 0x00080008 | |
| | | disable | 0xFF538288 | [3, 3] | 0 | io -4 0xFF538288 0x00080000 | |
| | Drive Strength | enable | 0xFF538288 | [3, 3] | 1 | io -4 0xFF538288 0x00080008 | |
| | | level0 | 0xFF5380A4 | [8, 13] | 1 | io -4 0xFF5380A4 0x3F000100 | |
| | | level1 | 0xFF5380A4 | [8, 13] | 3 | io -4 0xFF5380A4 0x3F000300 | |
| | | level2 | 0xFF5380A4 | [8, 13] | 7 | io -4 0xFF5380A4 0x3F000700 | |
| | | level3 | 0xFF5380A4 | [8, 13] | 15 | io -4 0xFF5380A4 0x3F000F00 | |
| GPIO1C4 | IOMUX | level4 | 0xFF5380A4 | [8, 13] | 31 | io -4 0xFF5380A4 0x3F001F00 | |
| | | level5 | 0xFF5380A4 | [8, 13] | 63 | io -4 0xFF5380A4 0x3F003F00 | |
| | ExtPort | read gpio data in input mode | 0xFF530070 | [19, 19] | | io -4 0xFF530070 | |
| | IOMUX | LCD D3 | 0xFF538014 | [0, 2] | 1 | io -4 0xFF538014 0x00070001 | |
| | | VICAP D6 M1 | 0xFF538014 | [0, 2] | 2 | io -4 0xFF538014 0x00070002 | |
| | | PWM8 M1 | 0xFF538014 | [0, 2] | 3 | io -4 0xFF538014 0x00070003 | |
| | | UART4 RX M1 | 0xFF538014 | [0, 2] | 4 | io -4 0xFF538014 0x00070004 | |
| | | SDMMC1 D3 M1 | 0xFF538014 | [0, 2] | 5 | io -4 0xFF538014 0x00070005 | |
| | Pull | GPIO1 C4 | 0xFF538014 | [0, 2] | 0 | io -4 0xFF538014 0x00070000 | |
| | | normal | 0xFF5381C8 | [8, 9] | 0 | io -4 0xFF5381C8 0x03000000 | |
| | | pullup | 0xFF5381C8 | [8, 9] | 1 | io -4 0xFF5381C8 0x03000100 | |
| | I/O Mode | pulldown | 0xFF5381C8 | [8, 9] | 2 | io -4 0xFF5381C8 0x03000200 | |
| | | input | 0xFF53000C | [4, 4] | 0 | io -4 0xFF53000C 0x00100000 | |
| | Output | output | 0xFF53000C | [4, 4] | 1 | io -4 0xFF53000C 0x00100010 | |
| | | low | 0xFF530004 | [4, 4] | 0 | io -4 0xFF530004 0x00100000 | |
| | Input Ctrl | high | 0xFF530004 | [4, 4] | 1 | io -4 0xFF530004 0x00100010 | |
| | | disable | 0xFF538188 | [4, 4] | 0 | io -4 0xFF538188 0x00100000 | |
| | Schmitt | enable | 0xFF538188 | [4, 4] | 1 | io -4 0xFF538188 0x00100010 | |
| | | disable | 0xFF538288 | [4, 4] | 0 | io -4 0xFF538288 0x00100000 | |
| | Drive Strength | enable | 0xFF538288 | [4, 4] | 1 | io -4 0xFF538288 0x00100010 | |
| | | level0 | 0xFF5380A8 | [0, 5] | 1 | io -4 0xFF5380A8 0x003F0001 | |
| | | level1 | 0xFF5380A8 | [0, 5] | 3 | io -4 0xFF5380A8 0x003F0003 | |
| | | level2 | 0xFF5380A8 | [0, 5] | 7 | io -4 0xFF5380A8 0x003F0007 | |
| | | level3 | 0xFF5380A8 | [0, 5] | 15 | io -4 0xFF5380A8 0x003F000F | |
| | | level4 | 0xFF5380A8 | [0, 5] | 31 | io -4 0xFF5380A8 0x003F001F | |
| | | level5 | 0xFF5380A8 | [0, 5] | 63 | io -4 0xFF5380A8 0x003F003F | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| | ExtPort | read gpio data in input mode | 0xFF530070 | [20, 20] | | io -4 0xFF530070 | |
| GPIO1C5 | IOMUX | LCD D2 | 0xFF538014 | [4, 6] | 1 | io -4 0xFF538014 0x00700010 | |
| | | VICAP D7 M1 | 0xFF538014 | [4, 6] | 2 | io -4 0xFF538014 0x00700020 | |
| | | PWM9 M1 | 0xFF538014 | [4, 6] | 3 | io -4 0xFF538014 0x00700030 | |
| | | UART4 TX M1 | 0xFF538014 | [4, 6] | 4 | io -4 0xFF538014 0x00700040 | |
| | | SDMMC1 D2 M1 | 0xFF538014 | [4, 6] | 5 | io -4 0xFF538014 0x00700050 | |
| | Pull | GPIO1 C5 | 0xFF538014 | [4, 6] | 0 | io -4 0xFF538014 0x00700000 | |
| | | normal | 0xFF5381C8 | [10, 11] | 0 | io -4 0xFF5381C8 0x0C000000 | |
| | | pullup | 0xFF5381C8 | [10, 11] | 1 | io -4 0xFF5381C8 0x0C000400 | |
| | I/O Mode | pulldown | 0xFF5381C8 | [10, 11] | 2 | io -4 0xFF5381C8 0x0C000800 | |
| | | input | 0xFF53000C | [5, 5] | 0 | io -4 0xFF53000C 0x00200000 | |
| | Output | output | 0xFF53000C | [5, 5] | 1 | io -4 0xFF53000C 0x00200020 | |
| | | low | 0xFF530004 | [5, 5] | 0 | io -4 0xFF530004 0x00200000 | |
| | Input Ctrl | high | 0xFF530004 | [5, 5] | 1 | io -4 0xFF530004 0x00200020 | |
| | | disable | 0xFF538188 | [5, 5] | 0 | io -4 0xFF538188 0x00200000 | |
| | Schmitt | enable | 0xFF538188 | [5, 5] | 1 | io -4 0xFF538188 0x00200020 | |
| | | disable | 0xFF538288 | [5, 5] | 0 | io -4 0xFF538288 0x00200000 | |
| | Drive Strength | enable | 0xFF538288 | [5, 5] | 1 | io -4 0xFF538288 0x00200020 | |
| | | level0 | 0xFF5380A8 | [8, 13] | 1 | io -4 0xFF5380A8 0x3F000100 | |
| | | level1 | 0xFF5380A8 | [8, 13] | 3 | io -4 0xFF5380A8 0x3F000300 | |
| | | level2 | 0xFF5380A8 | [8, 13] | 7 | io -4 0xFF5380A8 0x3F000700 | |
| | | level3 | 0xFF5380A8 | [8, 13] | 15 | io -4 0xFF5380A8 0x3F000F00 | |
| GPIO1C6 | IOMUX | level4 | 0xFF5380A8 | [8, 13] | 31 | io -4 0xFF5380A8 0x3F001F00 | |
| | | level5 | 0xFF5380A8 | [8, 13] | 63 | io -4 0xFF5380A8 0x3F003F00 | |
| | ExtPort | read gpio data in input mode | 0xFF530070 | [21, 21] | | io -4 0xFF530070 | |
| | IOMUX | LCD D1 | 0xFF538014 | [8, 10] | 1 | io -4 0xFF538014 0x07000100 | |
| | | VICAP D8 M1 | 0xFF538014 | [8, 10] | 2 | io -4 0xFF538014 0x07000200 | |
| | | PWM10 M1 | 0xFF538014 | [8, 10] | 3 | io -4 0xFF538014 0x07000300 | |
| | | UART4 RTSN M1 | 0xFF538014 | [8, 10] | 4 | io -4 0xFF538014 0x07000400 | |
| | | GPIO1 C6 | 0xFF538014 | [8, 10] | 0 | io -4 0xFF538014 0x07000000 | |
| | Pull | normal | 0xFF5381C8 | [12, 13] | 0 | io -4 0xFF5381C8 0x30000000 | |
| | | pullup | 0xFF5381C8 | [12, 13] | 1 | io -4 0xFF5381C8 0x30001000 | |
| | | pulldown | 0xFF5381C8 | [12, 13] | 2 | io -4 0xFF5381C8 0x30002000 | |
| | I/O Mode | input | 0xFF53000C | [6, 6] | 0 | io -4 0xFF53000C 0x00400000 | |
| | | output | 0xFF53000C | [6, 6] | 1 | io -4 0xFF53000C 0x00400040 | |
| | Output | low | 0xFF530004 | [6, 6] | 0 | io -4 0xFF530004 0x00400000 | |
| | | high | 0xFF530004 | [6, 6] | 1 | io -4 0xFF530004 0x00400040 | |
| | Input Ctrl | disable | 0xFF538188 | [6, 6] | 0 | io -4 0xFF538188 0x00400000 | |
| | | enable | 0xFF538188 | [6, 6] | 1 | io -4 0xFF538188 0x00400040 | |
| | Schmitt | disable | 0xFF538288 | [6, 6] | 0 | io -4 0xFF538288 0x00400000 | |
| | | enable | 0xFF538288 | [6, 6] | 1 | io -4 0xFF538288 0x00400040 | |
| | Drive Strength | level0 | 0xFF5380AC | [0, 5] | 1 | io -4 0xFF5380AC 0x003F0001 | |
| | | level1 | 0xFF5380AC | [0, 5] | 3 | io -4 0xFF5380AC 0x003F0003 | |
| | | level2 | 0xFF5380AC | [0, 5] | 7 | io -4 0xFF5380AC 0x003F0007 | |
| | | level3 | 0xFF5380AC | [0, 5] | 15 | io -4 0xFF5380AC 0x003F000F | |
| | | level4 | 0xFF5380AC | [0, 5] | 31 | io -4 0xFF5380AC 0x003F001F | |
| | ExtPort | level5 | 0xFF5380AC | [0, 5] | 63 | io -4 0xFF5380AC 0x003F003F | |
| | | read gpio data in input mode | 0xFF530070 | [22, 22] | | io -4 0xFF530070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO1C7 | IOMUX | LCD_D0 | 0xFF538014 | [12, 14] | 1 | io -4 0xFF538014 0x70001000 | |
| | | VICAP_D9_M1 | 0xFF538014 | [12, 14] | 2 | io -4 0xFF538014 0x70002000 | |
| | | PWM11_IR_M1 | 0xFF538014 | [12, 14] | 3 | io -4 0xFF538014 0x70003000 | |
| | | UART4_CTSN_M1 | 0xFF538014 | [12, 14] | 4 | io -4 0xFF538014 0x70004000 | |
| | | GPIO1_C7 | 0xFF538014 | [12, 14] | 0 | io -4 0xFF538014 0x70000000 | |
| | Pull | normal | 0xFF5381C8 | [14, 15] | 0 | io -4 0xFF5381C8 0xC0000000 | |
| | | pullup | 0xFF5381C8 | [14, 15] | 1 | io -4 0xFF5381C8 0xC0004000 | |
| | | pulldown | 0xFF5381C8 | [14, 15] | 2 | io -4 0xFF5381C8 0xC0008000 | |
| | I/O Mode | input | 0xFF53000C | [7, 7] | 0 | io -4 0xFF53000C 0x00800000 | |
| | | output | 0xFF53000C | [7, 7] | 1 | io -4 0xFF53000C 0x00800080 | |
| | Output | low | 0xFF530004 | [7, 7] | 0 | io -4 0xFF530004 0x00800000 | |
| | | high | 0xFF530004 | [7, 7] | 1 | io -4 0xFF530004 0x00800080 | |
| | Input Ctrl | disable | 0xFF538188 | [7, 7] | 0 | io -4 0xFF538188 0x00800000 | |
| | | enable | 0xFF538188 | [7, 7] | 1 | io -4 0xFF538188 0x00800080 | |
| | Schmitt | disable | 0xFF538288 | [7, 7] | 0 | io -4 0xFF538288 0x00800000 | |
| | | enable | 0xFF538288 | [7, 7] | 1 | io -4 0xFF538288 0x00800080 | |
| | Drive Strength | level0 | 0xFF5380AC | [8, 13] | 1 | io -4 0xFF5380AC 0x3F000100 | |
| | | level1 | 0xFF5380AC | [8, 13] | 3 | io -4 0xFF5380AC 0x3F000300 | |
| | | level2 | 0xFF5380AC | [8, 13] | 7 | io -4 0xFF5380AC 0x3F000700 | |
| | | level3 | 0xFF5380AC | [8, 13] | 15 | io -4 0xFF5380AC 0x3F000F00 | |
| | | level4 | 0xFF5380AC | [8, 13] | 31 | io -4 0xFF5380AC 0x3F001F00 | |
| | | level5 | 0xFF5380AC | [8, 13] | 63 | io -4 0xFF5380AC 0x3F003F00 | |
| | ExtPort | read gpio data in input mode | 0xFF530070 | [23, 23] | | io -4 0xFF530070 | |
| GPIO1D0 | IOMUX | LCD_DEN | 0xFF538018 | [0, 2] | 1 | io -4 0xFF538018 0x00070001 | |
| | | VICAP_CLKIN_M1 | 0xFF538018 | [0, 2] | 2 | io -4 0xFF538018 0x00070002 | |
| | | PWM3_IR_M2 | 0xFF538018 | [0, 2] | 3 | io -4 0xFF538018 0x00070003 | |
| | | UART5_RTSN_M1 | 0xFF538018 | [0, 2] | 4 | io -4 0xFF538018 0x00070004 | |
| | | UART3_TX_M1 | 0xFF538018 | [0, 2] | 5 | io -4 0xFF538018 0x00070005 | |
| | | GPIO1_D0 | 0xFF538018 | [0, 2] | 0 | io -4 0xFF538018 0x00070000 | |
| | Pull | normal | 0xFF5381CC | [0, 1] | 0 | io -4 0xFF5381CC 0x00030000 | |
| | | pullup | 0xFF5381CC | [0, 1] | 1 | io -4 0xFF5381CC 0x00030001 | |
| | | pulldown | 0xFF5381CC | [0, 1] | 2 | io -4 0xFF5381CC 0x00030002 | |
| | I/O Mode | input | 0xFF53000C | [8, 8] | 0 | io -4 0xFF53000C 0x01000000 | |
| | | output | 0xFF53000C | [8, 8] | 1 | io -4 0xFF53000C 0x01000100 | |
| | Output | low | 0xFF530004 | [8, 8] | 0 | io -4 0xFF530004 0x01000000 | |
| | | high | 0xFF530004 | [8, 8] | 1 | io -4 0xFF530004 0x01000100 | |
| | Input Ctrl | disable | 0xFF53818C | [0, 0] | 0 | io -4 0xFF53818C 0x00010000 | |
| | | enable | 0xFF53818C | [0, 0] | 1 | io -4 0xFF53818C 0x00010001 | |
| | Schmitt | disable | 0xFF53828C | [0, 0] | 0 | io -4 0xFF53828C 0x00010000 | |
| | | enable | 0xFF53828C | [0, 0] | 1 | io -4 0xFF53828C 0x00010001 | |
| | Drive Strength | level0 | 0xFF5380B0 | [0, 5] | 1 | io -4 0xFF5380B0 0x003F0001 | |
| | | level1 | 0xFF5380B0 | [0, 5] | 3 | io -4 0xFF5380B0 0x003F0003 | |
| | | level2 | 0xFF5380B0 | [0, 5] | 7 | io -4 0xFF5380B0 0x003F0007 | |
| | | level3 | 0xFF5380B0 | [0, 5] | 15 | io -4 0xFF5380B0 0x003F000F | |
| | | level4 | 0xFF5380B0 | [0, 5] | 31 | io -4 0xFF5380B0 0x003F001F | |
| | | level5 | 0xFF5380B0 | [0, 5] | 63 | io -4 0xFF5380B0 0x003F003F | |
| | ExtPort | read gpio data in input mode | 0xFF530070 | [24, 24] | | io -4 0xFF530070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO1D1 | IOMUX | LCD HSYNC | 0xFF538018 | [4, 6] | 1 | io -4 0xFF538018 0x00700010 | |
| | | VICAP HSYNC M1 | 0xFF538018 | [4, 6] | 2 | io -4 0xFF538018 0x00700020 | |
| | | PWM10 M2 | 0xFF538018 | [4, 6] | 3 | io -4 0xFF538018 0x00700030 | |
| | | UART5 CTSN M1 | 0xFF538018 | [4, 6] | 4 | io -4 0xFF538018 0x00700040 | |
| | | UART3 RX M1 | 0xFF538018 | [4, 6] | 5 | io -4 0xFF538018 0x00700050 | |
| | Pull | GPIO1 D1 | 0xFF538018 | [4, 6] | 0 | io -4 0xFF538018 0x00700000 | |
| | | normal | 0xFF5381CC | [2, 3] | 0 | io -4 0xFF5381CC 0x000C0000 | |
| | | pullup | 0xFF5381CC | [2, 3] | 1 | io -4 0xFF5381CC 0x000C0004 | |
| | I/O Mode | pulldown | 0xFF5381CC | [2, 3] | 2 | io -4 0xFF5381CC 0x000C0008 | |
| | | input | 0xFF53000C | [9, 9] | 0 | io -4 0xFF53000C 0x02000000 | |
| | Output | output | 0xFF53000C | [9, 9] | 1 | io -4 0xFF53000C 0x02000200 | |
| | | low | 0xFF530004 | [9, 9] | 0 | io -4 0xFF530004 0x02000000 | |
| | Input Ctrl | high | 0xFF530004 | [9, 9] | 1 | io -4 0xFF530004 0x02000200 | |
| | | disable | 0xFF53818C | [1, 1] | 0 | io -4 0xFF53818C 0x00020000 | |
| | Schmitt | enable | 0xFF53818C | [1, 1] | 1 | io -4 0xFF53818C 0x00020002 | |
| | | disable | 0xFF53828C | [1, 1] | 0 | io -4 0xFF53828C 0x00020000 | |
| | Drive Strength | enable | 0xFF53828C | [1, 1] | 1 | io -4 0xFF53828C 0x00020002 | |
| | | level0 | 0xFF5380B0 | [8, 13] | 1 | io -4 0xFF5380B0 0x3F000100 | |
| | | level1 | 0xFF5380B0 | [8, 13] | 3 | io -4 0xFF5380B0 0x3F000300 | |
| | | level2 | 0xFF5380B0 | [8, 13] | 7 | io -4 0xFF5380B0 0x3F000700 | |
| | | level3 | 0xFF5380B0 | [8, 13] | 15 | io -4 0xFF5380B0 0x3F000F00 | |
| | | level4 | 0xFF5380B0 | [8, 13] | 31 | io -4 0xFF5380B0 0x3F001F00 | |
| | ExtPort | level5 | 0xFF5380B0 | [8, 13] | 63 | io -4 0xFF5380B0 0x3F003F00 | |
| | | read gpio data in input mode | 0xFF530070 | [25, 25] | | io -4 0xFF530070 | |
| GPIO1D2 | IOMUX | LCD VSYNC | 0xFF538018 | [8, 10] | 1 | io -4 0xFF538018 0x07000100 | |
| | | VICAP VSYNC M1 | 0xFF538018 | [8, 10] | 2 | io -4 0xFF538018 0x07000200 | |
| | | I2C3 SDA M1 | 0xFF538018 | [8, 10] | 3 | io -4 0xFF538018 0x07000300 | |
| | | UART5 RX M1 | 0xFF538018 | [8, 10] | 4 | io -4 0xFF538018 0x07000400 | |
| | | SPI0 CSIN M0 | 0xFF538018 | [8, 10] | 5 | io -4 0xFF538018 0x07000500 | |
| | | PWM0 M1 | 0xFF538018 | [8, 10] | 6 | io -4 0xFF538018 0x07000600 | |
| | | DSMAUDIO P | 0xFF538018 | [8, 10] | 7 | io -4 0xFF538018 0x07000700 | |
| | | GPIO1 D2 | 0xFF538018 | [8, 10] | 0 | io -4 0xFF538018 0x07000000 | |
| | Pull | normal | 0xFF5381CC | [4, 5] | 0 | io -4 0xFF5381CC 0x00300000 | |
| | | pullup | 0xFF5381CC | [4, 5] | 1 | io -4 0xFF5381CC 0x00300010 | |
| | | pulldown | 0xFF5381CC | [4, 5] | 2 | io -4 0xFF5381CC 0x00300020 | |
| | I/O Mode | input | 0xFF53000C | [10, 10] | 0 | io -4 0xFF53000C 0x04000000 | |
| | | output | 0xFF53000C | [10, 10] | 1 | io -4 0xFF53000C 0x04000400 | |
| | Output | low | 0xFF530004 | [10, 10] | 0 | io -4 0xFF530004 0x04000000 | |
| | | high | 0xFF530004 | [10, 10] | 1 | io -4 0xFF530004 0x04000400 | |
| | Input Ctrl | disable | 0xFF53818C | [2, 2] | 0 | io -4 0xFF53818C 0x00040000 | |
| | | enable | 0xFF53818C | [2, 2] | 1 | io -4 0xFF53818C 0x00040004 | |
| | Schmitt | disable | 0xFF53828C | [2, 2] | 0 | io -4 0xFF53828C 0x00040000 | |
| | | enable | 0xFF53828C | [2, 2] | 1 | io -4 0xFF53828C 0x00040004 | |
| | Drive Strength | level0 | 0xFF5380B4 | [0, 5] | 1 | io -4 0xFF5380B4 0x003F0001 | |
| | | level1 | 0xFF5380B4 | [0, 5] | 3 | io -4 0xFF5380B4 0x003F0003 | |
| | | level2 | 0xFF5380B4 | [0, 5] | 7 | io -4 0xFF5380B4 0x003F0007 | |
| | | level3 | 0xFF5380B4 | [0, 5] | 15 | io -4 0xFF5380B4 0x003F000F | |
| | | level4 | 0xFF5380B4 | [0, 5] | 31 | io -4 0xFF5380B4 0x003F001F | |
| | | level5 | 0xFF5380B4 | [0, 5] | 63 | io -4 0xFF5380B4 0x003F003F | |
| | ExtPort | read gpio data in input mode | 0xFF530070 | [26, 26] | | io -4 0xFF530070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO1D3 | IOMUX | LCD CLK | 0xFF538018 | [12, 14] | 1 | io -4 0xFF538018 0x70001000 | |
| | | VICAP_CLKOUT_M1 | 0xFF538018 | [12, 14] | 2 | io -4 0xFF538018 0x70002000 | |
| | | I2C3_SCL_M1 | 0xFF538018 | [12, 14] | 3 | io -4 0xFF538018 0x70003000 | |
| | | UART5_TX_M1 | 0xFF538018 | [12, 14] | 4 | io -4 0xFF538018 0x70004000 | |
| | | PWM11_IR_M2 | 0xFF538018 | [12, 14] | 5 | io -4 0xFF538018 0x70005000 | |
| | | DSMAUDIO_N | 0xFF538018 | [12, 14] | 7 | io -4 0xFF538018 0x70007000 | |
| | Pull | GPIO1_D3 | 0xFF538018 | [12, 14] | 0 | io -4 0xFF538018 0x70000000 | |
| | | normal | 0xFF5381CC | [6, 7] | 0 | io -4 0xFF5381CC 0x00C00000 | |
| | | pullup | 0xFF5381CC | [6, 7] | 1 | io -4 0xFF5381CC 0x00C00040 | |
| | | pulldown | 0xFF5381CC | [6, 7] | 2 | io -4 0xFF5381CC 0x00C00080 | |
| | I/O Mode | input | 0xFF53000C | [11, 11] | 0 | io -4 0xFF53000C 0x08000000 | |
| | | output | 0xFF53000C | [11, 11] | 1 | io -4 0xFF53000C 0x08000800 | |
| | Output | low | 0xFF530004 | [11, 11] | 0 | io -4 0xFF530004 0x08000000 | |
| | | high | 0xFF530004 | [11, 11] | 1 | io -4 0xFF530004 0x08000800 | |
| | Input Ctrl | disable | 0xFF53818C | [3, 3] | 0 | io -4 0xFF53818C 0x00080000 | |
| | | enable | 0xFF53818C | [3, 3] | 1 | io -4 0xFF53818C 0x00080008 | |
| | Schmitt | disable | 0xFF53828C | [3, 3] | 0 | io -4 0xFF53828C 0x00080000 | |
| | | enable | 0xFF53828C | [3, 3] | 1 | io -4 0xFF53828C 0x00080008 | |
| | Drive Strength | level0 | 0xFF5380B4 | [8, 13] | 1 | io -4 0xFF5380B4 0x3F000100 | |
| | | level1 | 0xFF5380B4 | [8, 13] | 3 | io -4 0xFF5380B4 0x3F000300 | |
| | | level2 | 0xFF5380B4 | [8, 13] | 7 | io -4 0xFF5380B4 0x3F000700 | |
| | | level3 | 0xFF5380B4 | [8, 13] | 15 | io -4 0xFF5380B4 0x3F000F00 | |
| | | level4 | 0xFF5380B4 | [8, 13] | 31 | io -4 0xFF5380B4 0x3F001F00 | |
| | | level5 | 0xFF5380B4 | [8, 13] | 63 | io -4 0xFF5380B4 0x3F003F00 | |
| | ExtPort | read gpio data in input mode | 0xFF530070 | [27, 27] | | io -4 0xFF530070 | |
| GPIO2A0 | IOMUX | SDMMC1_D1_M0 | 0xFF548020 | [0, 2] | 1 | io -4 0xFF548020 0x00070001 | |
| | | I2S0_SCLK | 0xFF548020 | [0, 2] | 2 | io -4 0xFF548020 0x00070002 | |
| | | LCD_D8 | 0xFF548020 | [0, 2] | 3 | io -4 0xFF548020 0x00070003 | |
| | | UART1_CTSN_M1 | 0xFF548020 | [0, 2] | 4 | io -4 0xFF548020 0x00070004 | |
| | | I2C4_SDA_M0 | 0xFF548020 | [0, 2] | 5 | io -4 0xFF548020 0x00070005 | |
| | | GPIO2_A0 | 0xFF548020 | [0, 2] | 0 | io -4 0xFF548020 0x00070000 | |
| | Pull | normal | 0xFF5481D0 | [0, 1] | 0 | io -4 0xFF5481D0 0x00030000 | |
| | | pullup | 0xFF5481D0 | [0, 1] | 1 | io -4 0xFF5481D0 0x00030001 | |
| | | pulldown | 0xFF5481D0 | [0, 1] | 2 | io -4 0xFF5481D0 0x00030002 | |
| | I/O Mode | input | 0xFF540008 | [0, 0] | 0 | io -4 0xFF540008 0x00010000 | |
| | | output | 0xFF540008 | [0, 0] | 1 | io -4 0xFF540008 0x00010001 | |
| | Output | low | 0xFF540000 | [0, 0] | 0 | io -4 0xFF540000 0x00010000 | |
| | | high | 0xFF540000 | [0, 0] | 1 | io -4 0xFF540000 0x00010001 | |
| | Input Ctrl | disable | 0xFF548190 | [0, 0] | 0 | io -4 0xFF548190 0x00010000 | |
| | | enable | 0xFF548190 | [0, 0] | 1 | io -4 0xFF548190 0x00010001 | |
| | Schmitt | disable | 0xFF548290 | [0, 0] | 0 | io -4 0xFF548290 0x00010000 | |
| | | enable | 0xFF548290 | [0, 0] | 1 | io -4 0xFF548290 0x00010001 | |
| | Drive Strength | level0 | 0xFF5480C0 | [0, 5] | 1 | io -4 0xFF5480C0 0x003F0001 | |
| | | level1 | 0xFF5480C0 | [0, 5] | 3 | io -4 0xFF5480C0 0x003F0003 | |
| | | level2 | 0xFF5480C0 | [0, 5] | 7 | io -4 0xFF5480C0 0x003F0007 | |
| | | level3 | 0xFF5480C0 | [0, 5] | 15 | io -4 0xFF5480C0 0x003F000F | |
| | | level4 | 0xFF5480C0 | [0, 5] | 31 | io -4 0xFF5480C0 0x003F001F | |
| | | level5 | 0xFF5480C0 | [0, 5] | 63 | io -4 0xFF5480C0 0x003F003F | |
| | ExtPort | read gpio data in input mode | 0xFF540070 | [0, 0] | | io -4 0xFF540070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO2A1 | IOMUX | SDMMC1_DO_M0 | 0xFF548020 | [4, 6] | 1 | io -4 0xFF548020 0x00700010 | |
| | | I2S0_LRCK | 0xFF548020 | [4, 6] | 2 | io -4 0xFF548020 0x00700020 | |
| | | LCD_D9 | 0xFF548020 | [4, 6] | 3 | io -4 0xFF548020 0x00700030 | |
| | | UART1_RTSN_M1 | 0xFF548020 | [4, 6] | 4 | io -4 0xFF548020 0x00700040 | |
| | | I2C4_SCL_M0 | 0xFF548020 | [4, 6] | 5 | io -4 0xFF548020 0x00700050 | |
| | Pull | GPIO2_A1 | 0xFF548020 | [4, 6] | 0 | io -4 0xFF548020 0x00700000 | |
| | | normal | 0xFF5481D0 | [2, 3] | 0 | io -4 0xFF5481D0 0x000C0000 | |
| | | pullup | 0xFF5481D0 | [2, 3] | 1 | io -4 0xFF5481D0 0x000C0004 | |
| | I/O Mode | pulldown | 0xFF5481D0 | [2, 3] | 2 | io -4 0xFF5481D0 0x000C0008 | |
| | | input | 0xFF540008 | [1, 1] | 0 | io -4 0xFF540008 0x00020000 | |
| | Output | output | 0xFF540008 | [1, 1] | 1 | io -4 0xFF540008 0x00020002 | |
| | | low | 0xFF540000 | [1, 1] | 0 | io -4 0xFF540000 0x00020000 | |
| | Input Ctrl | high | 0xFF540000 | [1, 1] | 1 | io -4 0xFF540000 0x00020002 | |
| | | disable | 0xFF548190 | [1, 1] | 0 | io -4 0xFF548190 0x00020000 | |
| | Schmitt | enable | 0xFF548190 | [1, 1] | 1 | io -4 0xFF548190 0x00020002 | |
| | | disable | 0xFF548290 | [1, 1] | 0 | io -4 0xFF548290 0x00020000 | |
| | Drive Strength | enable | 0xFF548290 | [1, 1] | 1 | io -4 0xFF548290 0x00020002 | |
| | | level0 | 0xFF5480C0 | [8, 13] | 1 | io -4 0xFF5480C0 0x3F000100 | |
| | | level1 | 0xFF5480C0 | [8, 13] | 3 | io -4 0xFF5480C0 0x3F000300 | |
| | | level2 | 0xFF5480C0 | [8, 13] | 7 | io -4 0xFF5480C0 0x3F000700 | |
| | | level3 | 0xFF5480C0 | [8, 13] | 15 | io -4 0xFF5480C0 0x3F000F00 | |
| | ExtPort | level4 | 0xFF5480C0 | [8, 13] | 31 | io -4 0xFF5480C0 0x3F001F00 | |
| | | level5 | 0xFF5480C0 | [8, 13] | 63 | io -4 0xFF5480C0 0x3F003F00 | |
| | ExtPort | read gpio data in input mode | 0xFF540070 | [1, 1] | | io -4 0xFF540070 | |
| GPIO2A2 | IOMUX | SDMMC1_CLK_M0 | 0xFF548020 | [8, 10] | 1 | io -4 0xFF548020 0x07000100 | |
| | | I2S0_MCLK | 0xFF548020 | [8, 10] | 2 | io -4 0xFF548020 0x07000200 | |
| | | LCD_D10 | 0xFF548020 | [8, 10] | 3 | io -4 0xFF548020 0x07000300 | |
| | | GPIO2_A2 | 0xFF548020 | [8, 10] | 0 | io -4 0xFF548020 0x07000000 | |
| | Pull | normal | 0xFF5481D0 | [4, 5] | 0 | io -4 0xFF5481D0 0x00300000 | |
| | | pullup | 0xFF5481D0 | [4, 5] | 1 | io -4 0xFF5481D0 0x00300010 | |
| | | pulldown | 0xFF5481D0 | [4, 5] | 2 | io -4 0xFF5481D0 0x00300020 | |
| | I/O Mode | input | 0xFF540008 | [2, 2] | 0 | io -4 0xFF540008 0x00040000 | |
| | | output | 0xFF540008 | [2, 2] | 1 | io -4 0xFF540008 0x00040004 | |
| | Output | low | 0xFF540000 | [2, 2] | 0 | io -4 0xFF540000 0x00040000 | |
| | | high | 0xFF540000 | [2, 2] | 1 | io -4 0xFF540000 0x00040004 | |
| | Input Ctrl | disable | 0xFF548190 | [2, 2] | 0 | io -4 0xFF548190 0x00040000 | |
| | | enable | 0xFF548190 | [2, 2] | 1 | io -4 0xFF548190 0x00040004 | |
| | Schmitt | disable | 0xFF548290 | [2, 2] | 0 | io -4 0xFF548290 0x00040000 | |
| | | enable | 0xFF548290 | [2, 2] | 1 | io -4 0xFF548290 0x00040004 | |
| | Drive Strength | level0 | 0xFF5480C4 | [0, 5] | 1 | io -4 0xFF5480C4 0x003F0001 | |
| | | level1 | 0xFF5480C4 | [0, 5] | 3 | io -4 0xFF5480C4 0x003F0003 | |
| | | level2 | 0xFF5480C4 | [0, 5] | 7 | io -4 0xFF5480C4 0x003F0007 | |
| | | level3 | 0xFF5480C4 | [0, 5] | 15 | io -4 0xFF5480C4 0x003F000F | |
| | | level4 | 0xFF5480C4 | [0, 5] | 31 | io -4 0xFF5480C4 0x003F001F | |
| | ExtPort | level5 | 0xFF5480C4 | [0, 5] | 63 | io -4 0xFF5480C4 0x003F003F | |
| | | read gpio data in input mode | 0xFF540070 | [2, 2] | | io -4 0xFF540070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO2A3 | IOMUX | SDMMC1_CMD_M0 | 0xFF548020 | [12, 14] | 1 | io -4 0xFF548020 0x70001000 | |
| | | I2S0_SD03_SD11 | 0xFF548020 | [12, 14] | 2 | io -4 0xFF548020 0x70002000 | |
| | | LCD_D11 | 0xFF548020 | [12, 14] | 3 | io -4 0xFF548020 0x70003000 | |
| | | GPIO2_A3 | 0xFF548020 | [12, 14] | 0 | io -4 0xFF548020 0x70000000 | |
| | Pull | normal | 0xFF5481D0 | [6, 7] | 0 | io -4 0xFF5481D0 0x00C00000 | |
| | | pullup | 0xFF5481D0 | [6, 7] | 1 | io -4 0xFF5481D0 0x00C00040 | |
| | | pulldown | 0xFF5481D0 | [6, 7] | 2 | io -4 0xFF5481D0 0x00C00080 | |
| | I/O Mode | input | 0xFF540008 | [3, 3] | 0 | io -4 0xFF540008 0x00080000 | |
| | | output | 0xFF540008 | [3, 3] | 1 | io -4 0xFF540008 0x00080008 | |
| | Output | low | 0xFF540000 | [3, 3] | 0 | io -4 0xFF540000 0x00080000 | |
| | | high | 0xFF540000 | [3, 3] | 1 | io -4 0xFF540000 0x00080008 | |
| | Input Ctrl | disable | 0xFF548190 | [3, 3] | 0 | io -4 0xFF548190 0x00080000 | |
| | | enable | 0xFF548190 | [3, 3] | 1 | io -4 0xFF548190 0x00080008 | |
| | Schmitt | disable | 0xFF548290 | [3, 3] | 0 | io -4 0xFF548290 0x00080000 | |
| | | enable | 0xFF548290 | [3, 3] | 1 | io -4 0xFF548290 0x00080008 | |
| | Drive Strength | level0 | 0xFF5480C4 | [8, 13] | 1 | io -4 0xFF5480C4 0x3F000100 | |
| | | level1 | 0xFF5480C4 | [8, 13] | 3 | io -4 0xFF5480C4 0x3F000300 | |
| | | level2 | 0xFF5480C4 | [8, 13] | 7 | io -4 0xFF5480C4 0x3F000700 | |
| | | level3 | 0xFF5480C4 | [8, 13] | 15 | io -4 0xFF5480C4 0x3F000F00 | |
| | | level4 | 0xFF5480C4 | [8, 13] | 31 | io -4 0xFF5480C4 0x3F001F00 | |
| | | level5 | 0xFF5480C4 | [8, 13] | 63 | io -4 0xFF5480C4 0x3F003F00 | |
| | ExtPort | read gpio data in input mode | 0xFF540070 | [3, 3] | | io -4 0xFF540070 | |
| GPIO2A4 | IOMUX | SDMMC1_D3_M0 | 0xFF548024 | [0, 2] | 1 | io -4 0xFF548024 0x00070001 | |
| | | I2S0_SD00 | 0xFF548024 | [0, 2] | 2 | io -4 0xFF548024 0x00070002 | |
| | | LCD_D12 | 0xFF548024 | [0, 2] | 3 | io -4 0xFF548024 0x00070003 | |
| | | UART1_TX_M1 | 0xFF548024 | [0, 2] | 4 | io -4 0xFF548024 0x00070004 | |
| | | GPIO2_A4 | 0xFF548024 | [0, 2] | 0 | io -4 0xFF548024 0x00070000 | |
| | Pull | normal | 0xFF5481D0 | [8, 9] | 0 | io -4 0xFF5481D0 0x03000000 | |
| | | pullup | 0xFF5481D0 | [8, 9] | 1 | io -4 0xFF5481D0 0x03000100 | |
| | | pulldown | 0xFF5481D0 | [8, 9] | 2 | io -4 0xFF5481D0 0x03000200 | |
| | I/O Mode | input | 0xFF540008 | [4, 4] | 0 | io -4 0xFF540008 0x00100000 | |
| | | output | 0xFF540008 | [4, 4] | 1 | io -4 0xFF540008 0x00100010 | |
| | Output | low | 0xFF540000 | [4, 4] | 0 | io -4 0xFF540000 0x00100000 | |
| | | high | 0xFF540000 | [4, 4] | 1 | io -4 0xFF540000 0x00100010 | |
| | Input Ctrl | disable | 0xFF548190 | [4, 4] | 0 | io -4 0xFF548190 0x00100000 | |
| | | enable | 0xFF548190 | [4, 4] | 1 | io -4 0xFF548190 0x00100010 | |
| | Schmitt | disable | 0xFF548290 | [4, 4] | 0 | io -4 0xFF548290 0x00100000 | |
| | | enable | 0xFF548290 | [4, 4] | 1 | io -4 0xFF548290 0x00100010 | |
| | Drive Strength | level0 | 0xFF5480C8 | [0, 5] | 1 | io -4 0xFF5480C8 0x003F0001 | |
| | | level1 | 0xFF5480C8 | [0, 5] | 3 | io -4 0xFF5480C8 0x003F0003 | |
| | | level2 | 0xFF5480C8 | [0, 5] | 7 | io -4 0xFF5480C8 0x003F0007 | |
| | | level3 | 0xFF5480C8 | [0, 5] | 15 | io -4 0xFF5480C8 0x003F000F | |
| | | level4 | 0xFF5480C8 | [0, 5] | 31 | io -4 0xFF5480C8 0x003F001F | |
| | | level5 | 0xFF5480C8 | [0, 5] | 63 | io -4 0xFF5480C8 0x003F003F | |
| | ExtPort | read gpio data in input mode | 0xFF540070 | [4, 4] | | io -4 0xFF540070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO2A5 | IOMUX | SDMMC1_D2_M0 | 0xFF548024 | [4, 6] | 1 | io -4 0xFF548024 0x00700010 | |
| | | I2S0_SDIO | 0xFF548024 | [4, 6] | 2 | io -4 0xFF548024 0x00700020 | |
| | | LCD_D13 | 0xFF548024 | [4, 6] | 3 | io -4 0xFF548024 0x00700030 | |
| | | UART1_RX_M1 | 0xFF548024 | [4, 6] | 4 | io -4 0xFF548024 0x00700040 | |
| | | GPIO2_A5 | 0xFF548024 | [4, 6] | 0 | io -4 0xFF548024 0x00700000 | |
| | Pull | normal | 0xFF5481D0 | [10, 11] | 0 | io -4 0xFF5481D0 0x0C000000 | |
| | | pullup | 0xFF5481D0 | [10, 11] | 1 | io -4 0xFF5481D0 0x0C000400 | |
| | | pulldown | 0xFF5481D0 | [10, 11] | 2 | io -4 0xFF5481D0 0x0C000800 | |
| | I/O Mode | input | 0xFF540008 | [5, 5] | 0 | io -4 0xFF540008 0x00200000 | |
| | | output | 0xFF540008 | [5, 5] | 1 | io -4 0xFF540008 0x00200020 | |
| | Output | low | 0xFF540000 | [5, 5] | 0 | io -4 0xFF540000 0x00200000 | |
| | | high | 0xFF540000 | [5, 5] | 1 | io -4 0xFF540000 0x00200020 | |
| | Input Ctrl | disable | 0xFF548190 | [5, 5] | 0 | io -4 0xFF548190 0x00200000 | |
| | | enable | 0xFF548190 | [5, 5] | 1 | io -4 0xFF548190 0x00200020 | |
| | Schmitt | disable | 0xFF548290 | [5, 5] | 0 | io -4 0xFF548290 0x00200000 | |
| | | enable | 0xFF548290 | [5, 5] | 1 | io -4 0xFF548290 0x00200020 | |
| | Drive Strength | level0 | 0xFF5480C8 | [8, 13] | 1 | io -4 0xFF5480C8 0x3F000100 | |
| | | level1 | 0xFF5480C8 | [8, 13] | 3 | io -4 0xFF5480C8 0x3F000300 | |
| | | level2 | 0xFF5480C8 | [8, 13] | 7 | io -4 0xFF5480C8 0x3F000700 | |
| | | level3 | 0xFF5480C8 | [8, 13] | 15 | io -4 0xFF5480C8 0x3F000F00 | |
| | | level4 | 0xFF5480C8 | [8, 13] | 31 | io -4 0xFF5480C8 0x3F001F00 | |
| | | level5 | 0xFF5480C8 | [8, 13] | 63 | io -4 0xFF5480C8 0x3F003F00 | |
| | ExtPort | read gpio data in input mode | 0xFF540070 | [5, 5] | | io -4 0xFF540070 | |
| GPIO2A6 | IOMUX | UART0_RTSN_M1 | 0xFF548024 | [8, 10] | 1 | io -4 0xFF548024 0x07000100 | |
| | | I2S0_SDO2_SD12 | 0xFF548024 | [8, 10] | 2 | io -4 0xFF548024 0x07000200 | |
| | | LCD_D14 | 0xFF548024 | [8, 10] | 3 | io -4 0xFF548024 0x07000300 | |
| | | PWM2_M1 | 0xFF548024 | [8, 10] | 4 | io -4 0xFF548024 0x07000400 | |
| | | I2C3_SCL_M0 | 0xFF548024 | [8, 10] | 5 | io -4 0xFF548024 0x07000500 | |
| | | FLASH_TRIG_OUT | 0xFF548024 | [8, 10] | 6 | io -4 0xFF548024 0x07000600 | |
| | | GPIO2_A6 | 0xFF548024 | [8, 10] | 0 | io -4 0xFF548024 0x07000000 | |
| | Pull | normal | 0xFF5481D0 | [12, 13] | 0 | io -4 0xFF5481D0 0x30000000 | |
| | | pullup | 0xFF5481D0 | [12, 13] | 1 | io -4 0xFF5481D0 0x30001000 | |
| | | pulldown | 0xFF5481D0 | [12, 13] | 2 | io -4 0xFF5481D0 0x30002000 | |
| | I/O Mode | input | 0xFF540008 | [6, 6] | 0 | io -4 0xFF540008 0x00400000 | |
| | | output | 0xFF540008 | [6, 6] | 1 | io -4 0xFF540008 0x00400040 | |
| | Output | low | 0xFF540000 | [6, 6] | 0 | io -4 0xFF540000 0x00400000 | |
| | | high | 0xFF540000 | [6, 6] | 1 | io -4 0xFF540000 0x00400040 | |
| | Input Ctrl | disable | 0xFF548190 | [6, 6] | 0 | io -4 0xFF548190 0x00400000 | |
| | | enable | 0xFF548190 | [6, 6] | 1 | io -4 0xFF548190 0x00400040 | |
| | Schmitt | disable | 0xFF548290 | [6, 6] | 0 | io -4 0xFF548290 0x00400000 | |
| | | enable | 0xFF548290 | [6, 6] | 1 | io -4 0xFF548290 0x00400040 | |
| | Drive Strength | level0 | 0xFF5480CC | [0, 5] | 1 | io -4 0xFF5480CC 0x003F0001 | |
| | | level1 | 0xFF5480CC | [0, 5] | 3 | io -4 0xFF5480CC 0x003F0003 | |
| | | level2 | 0xFF5480CC | [0, 5] | 7 | io -4 0xFF5480CC 0x003F0007 | |
| | | level3 | 0xFF5480CC | [0, 5] | 15 | io -4 0xFF5480CC 0x003F000F | |
| | | level4 | 0xFF5480CC | [0, 5] | 31 | io -4 0xFF5480CC 0x003F001F | |
| | | level5 | 0xFF5480CC | [0, 5] | 63 | io -4 0xFF5480CC 0x003F003F | |
| | ExtPort | read gpio data in input mode | 0xFF540070 | [6, 6] | | io -4 0xFF540070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO2A7 | IOMUX | UART0_CTSN_M1 | 0xFF548024 | [12, 14] | 1 | io -4 0xFF548024 0x70001000 | |
| | | I2S0_SDO1_SD13 | 0xFF548024 | [12, 14] | 2 | io -4 0xFF548024 0x70002000 | |
| | | LCD_D15 | 0xFF548024 | [12, 14] | 3 | io -4 0xFF548024 0x70003000 | |
| | | PWM4_M1 | 0xFF548024 | [12, 14] | 4 | io -4 0xFF548024 0x70004000 | |
| | | I2C3_SDA_M0 | 0xFF548024 | [12, 14] | 5 | io -4 0xFF548024 0x70005000 | |
| | | PRELIGHT_TRIG_OUT | 0xFF548024 | [12, 14] | 6 | io -4 0xFF548024 0x70006000 | |
| | Pull | GPIO2_A7 | 0xFF548024 | [12, 14] | 0 | io -4 0xFF548024 0x70000000 | |
| | | normal | 0xFF5481D0 | [14, 15] | 0 | io -4 0xFF5481D0 0xC0000000 | |
| | | pullup | 0xFF5481D0 | [14, 15] | 1 | io -4 0xFF5481D0 0xC0004000 | |
| | I/O Mode | pulldown | 0xFF5481D0 | [14, 15] | 2 | io -4 0xFF5481D0 0xC0008000 | |
| | | input | 0xFF540008 | [7, 7] | 0 | io -4 0xFF540008 0x00800000 | |
| | Output | output | 0xFF540008 | [7, 7] | 1 | io -4 0xFF540008 0x00800080 | |
| | | low | 0xFF540000 | [7, 7] | 0 | io -4 0xFF540000 0x00800000 | |
| | Input Ctrl | high | 0xFF540000 | [7, 7] | 1 | io -4 0xFF540000 0x00800080 | |
| | | disable | 0xFF548190 | [7, 7] | 0 | io -4 0xFF548190 0x00800000 | |
| | Schmitt | enable | 0xFF548190 | [7, 7] | 1 | io -4 0xFF548190 0x00800080 | |
| | | disable | 0xFF548290 | [7, 7] | 0 | io -4 0xFF548290 0x00800000 | |
| | Drive Strength | enable | 0xFF548290 | [7, 7] | 1 | io -4 0xFF548290 0x00800080 | |
| | | level0 | 0xFF5480CC | [8, 13] | 1 | io -4 0xFF5480CC 0x3F000100 | |
| | | level1 | 0xFF5480CC | [8, 13] | 3 | io -4 0xFF5480CC 0x3F000300 | |
| | | level2 | 0xFF5480CC | [8, 13] | 7 | io -4 0xFF5480CC 0x3F000700 | |
| | | level3 | 0xFF5480CC | [8, 13] | 15 | io -4 0xFF5480CC 0x3F000F00 | |
| | ExtPort | level4 | 0xFF5480CC | [8, 13] | 31 | io -4 0xFF5480CC 0x3F001F00 | |
| | | level5 | 0xFF5480CC | [8, 13] | 63 | io -4 0xFF5480CC 0x3F003F00 | |
| | ExtPort | read_gpio_data_in_input_mode | 0xFF540070 | [7, 7] | | io -4 0xFF540070 | |
| GPIO2B0 | IOMUX | UART0_RX_M1 | 0xFF548028 | [0, 2] | 1 | io -4 0xFF548028 0x00070001 | |
| | | I2C1_SDA_M1 | 0xFF548028 | [4, 6] | 2 | io -4 0xFF548028 0x00070002 | |
| | | LCD_D16 | 0xFF548028 | [0, 2] | 3 | io -4 0xFF548028 0x00070003 | |
| | | PWM5_M1 | 0xFF548028 | [0, 2] | 4 | io -4 0xFF548028 0x00070004 | |
| | | TEST_CLK4_OUT | 0xFF548028 | [0, 2] | 5 | io -4 0xFF548028 0x00070005 | |
| | | GPIO2_B0 | 0xFF548028 | [0, 2] | 0 | io -4 0xFF548028 0x00070000 | |
| | Pull | normal | 0xFF5481D4 | [0, 1] | 0 | io -4 0xFF5481D4 0x00030000 | |
| | | pullup | 0xFF5481D4 | [0, 1] | 1 | io -4 0xFF5481D4 0x00030001 | |
| | | pulldown | 0xFF5481D4 | [0, 1] | 2 | io -4 0xFF5481D4 0x00030002 | |
| | I/O Mode | input | 0xFF540008 | [8, 8] | 0 | io -4 0xFF540008 0x01000000 | |
| | | output | 0xFF540008 | [8, 8] | 1 | io -4 0xFF540008 0x01000100 | |
| | Output | low | 0xFF540000 | [8, 8] | 0 | io -4 0xFF540000 0x01000000 | |
| | | high | 0xFF540000 | [8, 8] | 1 | io -4 0xFF540000 0x01000100 | |
| | Input Ctrl | disable | 0xFF548194 | [0, 0] | 0 | io -4 0xFF548194 0x00010000 | |
| | | enable | 0xFF548194 | [0, 0] | 1 | io -4 0xFF548194 0x00010001 | |
| | Schmitt | disable | 0xFF548294 | [0, 0] | 0 | io -4 0xFF548294 0x00010000 | |
| | | enable | 0xFF548294 | [0, 0] | 1 | io -4 0xFF548294 0x00010001 | |
| | Drive Strength | level0 | 0xFF5480D0 | [0, 5] | 1 | io -4 0xFF5480D0 0x003F0001 | |
| | | level1 | 0xFF5480D0 | [0, 5] | 3 | io -4 0xFF5480D0 0x003F0003 | |
| | | level2 | 0xFF5480D0 | [0, 5] | 7 | io -4 0xFF5480D0 0x003F0007 | |
| | | level3 | 0xFF5480D0 | [0, 5] | 15 | io -4 0xFF5480D0 0x003F000F | |
| | | level4 | 0xFF5480D0 | [0, 5] | 31 | io -4 0xFF5480D0 0x003F001F | |
| | ExtPort | level5 | 0xFF5480D0 | [0, 5] | 63 | io -4 0xFF5480D0 0x003F003F | |
| | | read_gpio_data_in_input_mode | 0xFF540070 | [8, 8] | | io -4 0xFF540070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO2B1 | IOMUX | UART0_TX_M1 | 0xFF548028 | [4, 6] | 1 | io -4 0xFF548028 0x00700010 | |
| | | I2C1_SCL_M1 | 0xFF548028 | [0, 2] | 2 | io -4 0xFF548028 0x00700002 | |
| | | LCD_D17 | 0xFF548028 | [4, 6] | 3 | io -4 0xFF548028 0x00700030 | |
| | | PWM6_M1 | 0xFF548028 | [4, 6] | 4 | io -4 0xFF548028 0x00700040 | |
| | | TEST_CLK5_OUT | 0xFF548028 | [4, 6] | 5 | io -4 0xFF548028 0x00700050 | |
| | Pull | GPIO2_B1 | 0xFF548028 | [4, 6] | 0 | io -4 0xFF548028 0x00700000 | |
| | | normal | 0xFF5481D4 | [2, 3] | 0 | io -4 0xFF5481D4 0x000C0000 | |
| | | pullup | 0xFF5481D4 | [2, 3] | 1 | io -4 0xFF5481D4 0x000C0004 | |
| | I/O Mode | pulldown | 0xFF5481D4 | [2, 3] | 2 | io -4 0xFF5481D4 0x000C0008 | |
| | | input | 0xFF540008 | [9, 9] | 0 | io -4 0xFF540008 0x02000000 | |
| | Output | output | 0xFF540008 | [9, 9] | 1 | io -4 0xFF540008 0x02000200 | |
| | | low | 0xFF540000 | [9, 9] | 0 | io -4 0xFF540000 0x02000000 | |
| | Input Ctrl | high | 0xFF540000 | [9, 9] | 1 | io -4 0xFF540000 0x02000200 | |
| | | disable | 0xFF548194 | [1, 1] | 0 | io -4 0xFF548194 0x00020000 | |
| | Schmitt | enable | 0xFF548194 | [1, 1] | 1 | io -4 0xFF548194 0x00020002 | |
| | | disable | 0xFF548294 | [1, 1] | 0 | io -4 0xFF548294 0x00020000 | |
| | Drive Strength | enable | 0xFF548294 | [1, 1] | 1 | io -4 0xFF548294 0x00020002 | |
| | | level0 | 0xFF5480D0 | [8, 13] | 1 | io -4 0xFF5480D0 0x3F000100 | |
| | | level1 | 0xFF5480D0 | [8, 13] | 3 | io -4 0xFF5480D0 0x3F000300 | |
| | | level2 | 0xFF5480D0 | [8, 13] | 7 | io -4 0xFF5480D0 0x3F000700 | |
| | | level3 | 0xFF5480D0 | [8, 13] | 15 | io -4 0xFF5480D0 0x3F000F00 | |
| | | level4 | 0xFF5480D0 | [8, 13] | 31 | io -4 0xFF5480D0 0x3F001F00 | |
| | ExtPort | level5 | 0xFF5480D0 | [8, 13] | 63 | io -4 0xFF5480D0 0x3F003F00 | |
| | | read gpio data in input mode | 0xFF540070 | [9, 9] | | io -4 0xFF540070 | |
| GPIO3A1 | IOMUX | SDMMC0_DET | 0xFF558040 | [4, 6] | 1 | io -4 0xFF558040 0x00700010 | |
| | | GPIO3_A1 | 0xFF558040 | [4, 6] | 0 | io -4 0xFF558040 0x00700000 | |
| | Pull | normal | 0xFF5581E0 | [2, 3] | 0 | io -4 0xFF5581E0 0x000C0000 | |
| | | pullup | 0xFF5581E0 | [2, 3] | 1 | io -4 0xFF5581E0 0x000C0004 | |
| | | pulldown | 0xFF5581E0 | [2, 3] | 2 | io -4 0xFF5581E0 0x000C0008 | |
| | I/O Mode | input | 0xFF550008 | [1, 1] | 0 | io -4 0xFF550008 0x00020000 | |
| | | output | 0xFF550008 | [1, 1] | 1 | io -4 0xFF550008 0x00020002 | |
| | Output | low | 0xFF550000 | [1, 1] | 0 | io -4 0xFF550000 0x00020000 | |
| | | high | 0xFF550000 | [1, 1] | 1 | io -4 0xFF550000 0x00020002 | |
| | Input Ctrl | disable | 0xFF5581A0 | [1, 1] | 0 | io -4 0xFF5581A0 0x00020000 | |
| | | enable | 0xFF5581A0 | [1, 1] | 1 | io -4 0xFF5581A0 0x00020002 | |
| | Schmitt | disable | 0xFF5582A0 | [1, 1] | 0 | io -4 0xFF5582A0 0x00020000 | |
| | | enable | 0xFF5582A0 | [1, 1] | 1 | io -4 0xFF5582A0 0x00020002 | |
| | Drive Strength | level0 | 0xFF558100 | [8, 13] | 1 | io -4 0xFF558100 0x3F000100 | |
| | | level1 | 0xFF558100 | [8, 13] | 3 | io -4 0xFF558100 0x3F000300 | |
| | | level2 | 0xFF558100 | [8, 13] | 7 | io -4 0xFF558100 0x3F000700 | |
| | | level3 | 0xFF558100 | [8, 13] | 15 | io -4 0xFF558100 0x3F000F00 | |
| | | level4 | 0xFF558100 | [8, 13] | 31 | io -4 0xFF558100 0x3F001F00 | |
| | | level5 | 0xFF558100 | [8, 13] | 63 | io -4 0xFF558100 0x3F003F00 | |
| | ExtPort | read gpio data in input mode | 0xFF550070 | [1, 1] | | io -4 0xFF550070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO3A2 | IOMUX | SDMMCO D1 | 0xFF558040 | [8, 10] | 1 | io -4 0xFF558040 0x07000100 | |
| | | UART2 TX M0 | 0xFF558040 | [8, 10] | 2 | io -4 0xFF558040 0x07000200 | |
| | | TEST CLK0 OUT | 0xFF558040 | [8, 10] | 3 | io -4 0xFF558040 0x07000300 | |
| | | PWM9 M0 | 0xFF558040 | [8, 10] | 4 | io -4 0xFF558040 0x07000400 | |
| | Pull | GPIO3 A2 | 0xFF558040 | [8, 10] | 0 | io -4 0xFF558040 0x07000000 | |
| | | normal | 0xFF5581E0 | [4, 5] | 0 | io -4 0xFF5581E0 0x00300000 | |
| | | pullup | 0xFF5581E0 | [4, 5] | 1 | io -4 0xFF5581E0 0x00300010 | |
| | I/O Mode | pulldown | 0xFF5581E0 | [4, 5] | 2 | io -4 0xFF5581E0 0x00300020 | |
| | | input | 0xFF550008 | [2, 2] | 0 | io -4 0xFF550008 0x00040000 | |
| | Output | output | 0xFF550008 | [2, 2] | 1 | io -4 0xFF550008 0x00040004 | |
| | | low | 0xFF550000 | [2, 2] | 0 | io -4 0xFF550000 0x00040000 | |
| | Input Ctrl | high | 0xFF550000 | [2, 2] | 1 | io -4 0xFF550000 0x00040004 | |
| | | disable | 0xFF5581A0 | [2, 2] | 0 | io -4 0xFF5581A0 0x00040000 | |
| | Schmitt | enable | 0xFF5581A0 | [2, 2] | 1 | io -4 0xFF5581A0 0x00040004 | |
| | | disable | 0xFF5582A0 | [2, 2] | 0 | io -4 0xFF5582A0 0x00040000 | |
| | Drive Strength | enable | 0xFF5582A0 | [2, 2] | 1 | io -4 0xFF5582A0 0x00040004 | |
| | | level0 | 0xFF558104 | [0, 5] | 1 | io -4 0xFF558104 0x003F0001 | |
| | | level1 | 0xFF558104 | [0, 5] | 3 | io -4 0xFF558104 0x003F0003 | |
| | | level2 | 0xFF558104 | [0, 5] | 7 | io -4 0xFF558104 0x003F0007 | |
| | | level3 | 0xFF558104 | [0, 5] | 15 | io -4 0xFF558104 0x003F000F | |
| GPIO3A3 | IOMUX | level4 | 0xFF558104 | [0, 5] | 31 | io -4 0xFF558104 0x003F001F | |
| | | level5 | 0xFF558104 | [0, 5] | 63 | io -4 0xFF558104 0x003F003F | |
| | ExtPort | read gpio data in input mode | 0xFF550070 | [2, 2] | | io -4 0xFF550070 | |
| | IOMUX | SDMMCO D0 | 0xFF558040 | [12, 14] | 1 | io -4 0xFF558040 0x70001000 | |
| | | UART2 RX M0 | 0xFF558040 | [12, 14] | 2 | io -4 0xFF558040 0x70002000 | |
| | | TEST CLK1 OUT | 0xFF558040 | [12, 14] | 3 | io -4 0xFF558040 0x70003000 | |
| | | PWM8 M0 | 0xFF558040 | [12, 14] | 4 | io -4 0xFF558040 0x70004000 | |
| | | GPIO3 A3 | 0xFF558040 | [12, 14] | 0 | io -4 0xFF558040 0x70000000 | |
| | Pull | normal | 0xFF5581E0 | [6, 7] | 0 | io -4 0xFF5581E0 0x00C00000 | |
| | | pullup | 0xFF5581E0 | [6, 7] | 1 | io -4 0xFF5581E0 0x00C00040 | |
| | | pulldown | 0xFF5581E0 | [6, 7] | 2 | io -4 0xFF5581E0 0x00C00080 | |
| | I/O Mode | input | 0xFF550008 | [3, 3] | 0 | io -4 0xFF550008 0x00080000 | |
| | | output | 0xFF550008 | [3, 3] | 1 | io -4 0xFF550008 0x00080008 | |
| | Output | low | 0xFF550000 | [3, 3] | 0 | io -4 0xFF550000 0x00080000 | |
| | | high | 0xFF550000 | [3, 3] | 1 | io -4 0xFF550000 0x00080008 | |
| | Input Ctrl | disable | 0xFF5581A0 | [3, 3] | 0 | io -4 0xFF5581A0 0x00080000 | |
| | | enable | 0xFF5581A0 | [3, 3] | 1 | io -4 0xFF5581A0 0x00080008 | |
| | Schmitt | disable | 0xFF5582A0 | [3, 3] | 0 | io -4 0xFF5582A0 0x00080000 | |
| | | enable | 0xFF5582A0 | [3, 3] | 1 | io -4 0xFF5582A0 0x00080008 | |
| | Drive Strength | level0 | 0xFF558104 | [8, 13] | 1 | io -4 0xFF558104 0x3F000100 | |
| | | level1 | 0xFF558104 | [8, 13] | 3 | io -4 0xFF558104 0x3F000300 | |
| | | level2 | 0xFF558104 | [8, 13] | 7 | io -4 0xFF558104 0x3F000700 | |
| | | level3 | 0xFF558104 | [8, 13] | 15 | io -4 0xFF558104 0x3F000F00 | |
| | | level4 | 0xFF558104 | [8, 13] | 31 | io -4 0xFF558104 0x3F001F00 | |
| | ExtPort | read gpio data in input mode | 0xFF550070 | [3, 3] | 63 | io -4 0xFF558104 0x3F003F00 | |
| | ExtPort | read gpio data in input mode | 0xFF550070 | [3, 3] | | io -4 0xFF550070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO3A4 | IOMUX | SDMMCO_CLK | 0xFF558044 | [0, 2] | 1 | io -4 0xFF558044 0x00070001 | |
| | | UART5_RTSN_M0 | 0xFF558044 | [0, 2] | 2 | io -4 0xFF558044 0x00070002 | |
| | | I2CO_SCL_M2 | 0xFF558044 | [0, 2] | 3 | io -4 0xFF558044 0x00070003 | |
| | | LPMCU_JTAG_TCK_M1 | 0xFF558044 | [0, 2] | 4 | io -4 0xFF558044 0x00070004 | |
| | | PWM10_M0 | 0xFF558044 | [0, 2] | 5 | io -4 0xFF558044 0x00070005 | |
| | Pull | GPIO3_A4 | 0xFF558044 | [0, 2] | 0 | io -4 0xFF558044 0x00070000 | |
| | | normal | 0xFF5581E0 | [8, 9] | 0 | io -4 0xFF5581E0 0x03000000 | |
| | | pullup | 0xFF5581E0 | [8, 9] | 1 | io -4 0xFF5581E0 0x03000100 | |
| | I/O Mode | pulldown | 0xFF5581E0 | [8, 9] | 2 | io -4 0xFF5581E0 0x03000200 | |
| | | input | 0xFF550008 | [4, 4] | 0 | io -4 0xFF550008 0x00100000 | |
| | Output | output | 0xFF550008 | [4, 4] | 1 | io -4 0xFF550008 0x00100010 | |
| | | low | 0xFF550000 | [4, 4] | 0 | io -4 0xFF550000 0x00100000 | |
| | Input Ctrl | high | 0xFF550000 | [4, 4] | 1 | io -4 0xFF550000 0x00100010 | |
| | | disable | 0xFF5581A0 | [4, 4] | 0 | io -4 0xFF5581A0 0x00100000 | |
| | Schmitt | enable | 0xFF5581A0 | [4, 4] | 1 | io -4 0xFF5581A0 0x00100010 | |
| | | disable | 0xFF5582A0 | [4, 4] | 0 | io -4 0xFF5582A0 0x00100000 | |
| | Drive Strength | enable | 0xFF5582A0 | [4, 4] | 1 | io -4 0xFF5582A0 0x00100010 | |
| | | level0 | 0xFF558108 | [0, 5] | 1 | io -4 0xFF558108 0x003F0001 | |
| | | level1 | 0xFF558108 | [0, 5] | 3 | io -4 0xFF558108 0x003F0003 | |
| | | level2 | 0xFF558108 | [0, 5] | 7 | io -4 0xFF558108 0x003F0007 | |
| | | level3 | 0xFF558108 | [0, 5] | 15 | io -4 0xFF558108 0x003F000F | |
| | | level4 | 0xFF558108 | [0, 5] | 31 | io -4 0xFF558108 0x003F001F | |
| | | level5 | 0xFF558108 | [0, 5] | 63 | io -4 0xFF558108 0x003F003F | |
| | ExtPort | read gpio data in input mode | 0xFF550070 | [4, 4] | | io -4 0xFF550070 | |
| GPIO3A5 | IOMUX | SDMMCO_CMD | 0xFF558044 | [4, 6] | 1 | io -4 0xFF558044 0x00700010 | |
| | | UART5_CTSN_M0 | 0xFF558044 | [4, 6] | 2 | io -4 0xFF558044 0x00700020 | |
| | | I2CO_SDA_M2 | 0xFF558044 | [4, 6] | 3 | io -4 0xFF558044 0x00700030 | |
| | | LPMCU_JTAG_TMS_M1 | 0xFF558044 | [4, 6] | 4 | io -4 0xFF558044 0x00700040 | |
| | | PWM11_IR_M0 | 0xFF558044 | [4, 6] | 5 | io -4 0xFF558044 0x00700050 | |
| | Pull | GPIO3_A5 | 0xFF558044 | [4, 6] | 0 | io -4 0xFF558044 0x00700000 | |
| | | normal | 0xFF5581E0 | [10, 11] | 0 | io -4 0xFF5581E0 0x0C000000 | |
| | | pullup | 0xFF5581E0 | [10, 11] | 1 | io -4 0xFF5581E0 0x0C000400 | |
| | I/O Mode | pulldown | 0xFF5581E0 | [10, 11] | 2 | io -4 0xFF5581E0 0x0C000800 | |
| | | input | 0xFF550008 | [5, 5] | 0 | io -4 0xFF550008 0x00200000 | |
| | Output | output | 0xFF550008 | [5, 5] | 1 | io -4 0xFF550008 0x00200020 | |
| | | low | 0xFF550000 | [5, 5] | 0 | io -4 0xFF550000 0x00200000 | |
| | Input Ctrl | high | 0xFF550000 | [5, 5] | 1 | io -4 0xFF550000 0x00200020 | |
| | | disable | 0xFF5581A0 | [5, 5] | 0 | io -4 0xFF5581A0 0x00200000 | |
| | Schmitt | enable | 0xFF5581A0 | [5, 5] | 1 | io -4 0xFF5581A0 0x00200020 | |
| | | disable | 0xFF5582A0 | [5, 5] | 0 | io -4 0xFF5582A0 0x00200000 | |
| | Drive Strength | enable | 0xFF5582A0 | [5, 5] | 1 | io -4 0xFF5582A0 0x00200020 | |
| | | level0 | 0xFF558108 | [8, 13] | 1 | io -4 0xFF558108 0x3F000100 | |
| | | level1 | 0xFF558108 | [8, 13] | 3 | io -4 0xFF558108 0x3F000300 | |
| | | level2 | 0xFF558108 | [8, 13] | 7 | io -4 0xFF558108 0x3F000700 | |
| | | level3 | 0xFF558108 | [8, 13] | 15 | io -4 0xFF558108 0x3F000F00 | |
| | | level4 | 0xFF558108 | [8, 13] | 31 | io -4 0xFF558108 0x3F001F00 | |
| | | level5 | 0xFF558108 | [8, 13] | 63 | io -4 0xFF558108 0x3F003F00 | |
| | ExtPort | read gpio data in input mode | 0xFF550070 | [5, 5] | | io -4 0xFF550070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO3A6 | IOMUX | SDMMC0_D3 | 0xFF558044 | [8, 10] | 1 | io -4 0xFF558044 0x07000100 | |
| | | UART5_TX_M0 | 0xFF558044 | [8, 10] | 2 | io -4 0xFF558044 0x07000200 | |
| | | A7_JTAG_TMS_M0 | 0xFF558044 | [8, 10] | 3 | io -4 0xFF558044 0x07000300 | |
| | | HPMCU_JTAG_TMS_M1 | 0xFF558044 | [8, 10] | 4 | io -4 0xFF558044 0x07000400 | |
| | Pull | GPIO3_A6 | 0xFF558044 | [8, 10] | 0 | io -4 0xFF558044 0x07000000 | |
| | | normal | 0xFF5581E0 | [12, 13] | 0 | io -4 0xFF5581E0 0x30000000 | |
| | | pullup | 0xFF5581E0 | [12, 13] | 1 | io -4 0xFF5581E0 0x30001000 | |
| | I/O Mode | pulldown | 0xFF5581E0 | [12, 13] | 2 | io -4 0xFF5581E0 0x30002000 | |
| | | input | 0xFF550008 | [6, 6] | 0 | io -4 0xFF550008 0x00400000 | |
| | Output | output | 0xFF550008 | [6, 6] | 1 | io -4 0xFF550008 0x00400040 | |
| | | low | 0xFF550000 | [6, 6] | 0 | io -4 0xFF550000 0x00400000 | |
| | Input Ctrl | high | 0xFF550000 | [6, 6] | 1 | io -4 0xFF550000 0x00400040 | |
| | | disable | 0xFF5581A0 | [6, 6] | 0 | io -4 0xFF5581A0 0x00400000 | |
| | Schmitt | enable | 0xFF5581A0 | [6, 6] | 1 | io -4 0xFF5581A0 0x00400040 | |
| | | disable | 0xFF5582A0 | [6, 6] | 0 | io -4 0xFF5582A0 0x00400000 | |
| | Drive Strength | enable | 0xFF5582A0 | [6, 6] | 1 | io -4 0xFF5582A0 0x00400040 | |
| | | level0 | 0xFF55810C | [0, 5] | 1 | io -4 0xFF55810C 0x003F0001 | |
| | | level1 | 0xFF55810C | [0, 5] | 3 | io -4 0xFF55810C 0x003F0003 | |
| | | level2 | 0xFF55810C | [0, 5] | 7 | io -4 0xFF55810C 0x003F0007 | |
| | | level3 | 0xFF55810C | [0, 5] | 15 | io -4 0xFF55810C 0x003F000F | |
| | ExtPort | level4 | 0xFF55810C | [0, 5] | 31 | io -4 0xFF55810C 0x003F001F | |
| | | level5 | 0xFF55810C | [0, 5] | 63 | io -4 0xFF55810C 0x003F003F | |
| | | read_gpio_data_in_input_mode | 0xFF550070 | [6, 6] | | io -4 0xFF550070 | |
| GPIO3A7 | IOMUX | SDMMC0_D2 | 0xFF558044 | [12, 14] | 1 | io -4 0xFF558044 0x70001000 | |
| | | UART5_RX_M0 | 0xFF558044 | [12, 14] | 2 | io -4 0xFF558044 0x70002000 | |
| | | A7_JTAG_TCK_M0 | 0xFF558044 | [12, 14] | 3 | io -4 0xFF558044 0x70003000 | |
| | | HPMCU_JTAG_TCK_M1 | 0xFF558044 | [12, 14] | 4 | io -4 0xFF558044 0x70004000 | |
| | Pull | GPIO3_A7 | 0xFF558044 | [12, 14] | 0 | io -4 0xFF558044 0x70000000 | |
| | | normal | 0xFF5581E0 | [14, 15] | 0 | io -4 0xFF5581E0 0xC0000000 | |
| | | pullup | 0xFF5581E0 | [14, 15] | 1 | io -4 0xFF5581E0 0xC0004000 | |
| | I/O Mode | pulldown | 0xFF5581E0 | [14, 15] | 2 | io -4 0xFF5581E0 0xC0008000 | |
| | | input | 0xFF550008 | [7, 7] | 0 | io -4 0xFF550008 0x00800000 | |
| | Output | output | 0xFF550008 | [7, 7] | 1 | io -4 0xFF550008 0x00800080 | |
| | | low | 0xFF550000 | [7, 7] | 0 | io -4 0xFF550000 0x00800000 | |
| | Input Ctrl | high | 0xFF550000 | [7, 7] | 1 | io -4 0xFF550000 0x00800080 | |
| | | disable | 0xFF5581A0 | [7, 7] | 0 | io -4 0xFF5581A0 0x00800000 | |
| | Schmitt | enable | 0xFF5581A0 | [7, 7] | 1 | io -4 0xFF5581A0 0x00800080 | |
| | | disable | 0xFF5582A0 | [7, 7] | 0 | io -4 0xFF5582A0 0x00800000 | |
| | Drive Strength | enable | 0xFF5582A0 | [7, 7] | 1 | io -4 0xFF5582A0 0x00800080 | |
| | | level0 | 0xFF55810C | [8, 13] | 1 | io -4 0xFF55810C 0x3F000100 | |
| | | level1 | 0xFF55810C | [8, 13] | 3 | io -4 0xFF55810C 0x3F000300 | |
| | | level2 | 0xFF55810C | [8, 13] | 7 | io -4 0xFF55810C 0x3F000700 | |
| | | level3 | 0xFF55810C | [8, 13] | 15 | io -4 0xFF55810C 0x3F000F00 | |
| | ExtPort | level4 | 0xFF55810C | [8, 13] | 31 | io -4 0xFF55810C 0x3F001F00 | |
| | | level5 | 0xFF55810C | [8, 13] | 63 | io -4 0xFF55810C 0x3F003F00 | |
| | | read_gpio_data_in_input_mode | 0xFF550070 | [7, 7] | | io -4 0xFF550070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|---|------------|
| GPIO3B0 | IOMUX | VICAP_D0_M0 | 0xFF558048 | [0,2] | 1 | io -4 0xFF558048 0x00070001 | |
| | | MIPI_LVDS_D3N | 0xFF558048 | [0,2] | 2 | io -4 0xFF558048 0x00070002 | |
| | | GPIO3_B0 | 0xFF558048 | [0,2] | 0 | io -4 0xFF558048 0x00070000 io -4 0xFF3B4804 0x40000000 io -4 0xFE3E8000 0x0000007D io -4 0xFE3E8080 0x0000005F io -4 0xFE3E884C 0x00000001 | input only |
| | | Pull | | | | | |
| | I/O Mode | input | 0xFF550008 | [8,8] | 0 | io -4 0xFF550008 0x01000000 | |
| | | | | | | | |
| | Output | | | | | | |
| | | | | | | | |
| | Input Ctrl | | | | | | |
| | Schmitt | | | | | | |
| GPIO3B1 | IOMUX | read gpio data in input mode | 0xFF550070 | [8,8] | | io -4 0xFF550070 | |
| | | VICAP_D1_M0 | 0xFF558048 | [4,6] | 1 | io -4 0xFF558048 0x00700010 | |
| | | MIPI_LVDS_D3P | 0xFF558048 | [4,6] | 2 | io -4 0xFF558048 0x00700020 | |
| | | GPIO3_B1 | 0xFF558048 | [4,6] | 0 | io -4 0xFF558048 0x00700000 io -4 0xFF3B4804 0x40000000 io -4 0xFE3E8000 0x0000007D io -4 0xFE3E8080 0x0000005F io -4 0xFE3E884C 0x00000001 | input only |
| | Pull | | | | | | |
| | I/O Mode | input | 0xFF550008 | [9,9] | 0 | io -4 0xFF550008 0x02000000 | |
| | | | | | | | |
| | Output | | | | | | |
| | | | | | | | |
| | Input Ctrl | | | | | | |
| GPIO3B2 | IOMUX | read gpio data in input mode | 0xFF550070 | [9,9] | | io -4 0xFF550070 | |
| | | VICAP_D2_M0 | 0xFF558048 | [8,10] | 1 | io -4 0xFF558048 0x07000100 | |
| | | MIPI_LVDS_CK1N | 0xFF558048 | [8,10] | 2 | io -4 0xFF558048 0x07000200 | |
| | | GPIO3_B2 | 0xFF558048 | [8,10] | 0 | io -4 0xFF558048 0x07000000 io -4 0xFF3B4804 0x40000000 io -4 0xFE3E8000 0x0000007D io -4 0xFE3E8080 0x0000005F io -4 0xFE3E884C 0x00000001 | input only |
| | Pull | | | | | | |
| | I/O Mode | input | 0xFF550008 | [10,10] | 0 | io -4 0xFF550008 0x04000000 | |
| | | | | | | | |
| | Output | | | | | | |
| | | | | | | | |
| | Input Ctrl | | | | | | |
| GPIO3B2 | IOMUX | read gpio data in input mode | 0xFF550070 | [10,10] | | io -4 0xFF550070 | |
| | | VICAP_D2_M0 | 0xFF558048 | [8,10] | 1 | io -4 0xFF558048 0x07000100 | |
| | | MIPI_LVDS_CK1N | 0xFF558048 | [8,10] | 2 | io -4 0xFF558048 0x07000200 | |
| | | GPIO3_B2 | 0xFF558048 | [8,10] | 0 | io -4 0xFF558048 0x07000000 io -4 0xFF3B4804 0x40000000 io -4 0xFE3E8000 0x0000007D io -4 0xFE3E8080 0x0000005F io -4 0xFE3E884C 0x00000001 | input only |
| | Pull | | | | | | |
| | I/O Mode | input | 0xFF550008 | [10,10] | 0 | io -4 0xFF550008 0x04000000 | |
| | | | | | | | |
| | Output | | | | | | |
| | | | | | | | |
| | Input Ctrl | | | | | | |
| | Schmitt | | | | | | |
| | Drive Strength | | | | | | |
| | ExtPort | | | | | | |

| Name | Config | Description | Register | Position | Value | IO Command | |
|---------|----------------|------------------------------|------------|----------|-------|---|------------|
| GPIO3B3 | IOMUX | VICAP_D3_M0 | 0xFF558048 | [12, 14] | 1 | io -4 0xFF558048 0x70001000 | |
| | | MIPI_LVDS_CK1P | 0xFF558048 | [12, 14] | 2 | io -4 0xFF558048 0x70002000 | |
| | | GPIO3_B3 | 0xFF558048 | [12, 14] | 0 | io -4 0xFF558048 0x70000000 io -4 0xFF3B4804 0x40000000 io -4 0xFE3E8000 0x0000007D io -4 0xFE3E8080 0x0000005F io -4 0xFE3E884C 0x00000001 | input only |
| | | Pull | | | | | |
| | I/O Mode | input | 0xFF550008 | [11, 11] | 0 | io -4 0xFF550008 0x80000000 | |
| | | | | | | | |
| | Output | | | | | | |
| | | | | | | | |
| | Input Ctrl | | | | | | |
| | Schmitt | | | | | | |
| GPIO3B4 | IOMUX | read gpio data in input mode | 0xFF550070 | [11, 11] | | io -4 0xFF550070 | |
| | | VICAP_D4_M0 | 0xFF55804C | [0, 2] | 1 | io -4 0xFF55804C 0x00070001 | |
| | | MIPI_LVDS_D2N | 0xFF55804C | [0, 2] | 2 | io -4 0xFF55804C 0x00070002 | |
| | | GPIO3_B4 | 0xFF55804C | [0, 2] | 0 | io -4 0xFF55804C 0x00070000 io -4 0xFF3B4804 0x40000000 io -4 0xFE3E8000 0x0000007D io -4 0xFE3E8080 0x0000005F io -4 0xFE3E884C 0x00000001 | input only |
| | Pull | | | | | | |
| | I/O Mode | input | 0xFF550008 | [12, 12] | 0 | io -4 0xFF550008 0x10000000 | |
| | | | | | | | |
| | Output | | | | | | |
| | | | | | | | |
| | Input Ctrl | | | | | | |
| GPIO3B5 | IOMUX | read gpio data in input mode | 0xFF550070 | [12, 12] | | io -4 0xFF550070 | |
| | | VICAP_D5_M0 | 0xFF55804C | [4, 6] | 1 | io -4 0xFF55804C 0x00700010 | |
| | | MIPI_LVDS_D2P | 0xFF55804C | [4, 6] | 2 | io -4 0xFF55804C 0x00700020 | |
| | | GPIO3_B5 | 0xFF55804C | [4, 6] | 0 | io -4 0xFF55804C 0x00700000 io -4 0xFF3B4804 0x40000000 io -4 0xFE3E8000 0x0000007D io -4 0xFE3E8080 0x0000005F io -4 0xFE3E884C 0x00000001 | input only |
| | Pull | | | | | | |
| | I/O Mode | input | 0xFF550008 | [13, 13] | 0 | io -4 0xFF550008 0x20000000 | |
| | | | | | | | |
| | Output | | | | | | |
| | | | | | | | |
| | Input Ctrl | | | | | | |
| GPIO3B5 | IOMUX | read gpio data in input mode | 0xFF550070 | [13, 13] | | io -4 0xFF550070 | |
| | | VICAP_D5_M0 | 0xFF55804C | [4, 6] | 1 | io -4 0xFF55804C 0x00700010 | |
| | | MIPI_LVDS_D2P | 0xFF55804C | [4, 6] | 2 | io -4 0xFF55804C 0x00700020 | |
| | | GPIO3_B5 | 0xFF55804C | [4, 6] | 0 | io -4 0xFF55804C 0x00700000 io -4 0xFF3B4804 0x40000000 io -4 0xFE3E8000 0x0000007D io -4 0xFE3E8080 0x0000005F io -4 0xFE3E884C 0x00000001 | input only |
| | Pull | | | | | | |
| | I/O Mode | input | 0xFF550008 | [13, 13] | 0 | io -4 0xFF550008 0x20000000 | |
| | | | | | | | |
| | Output | | | | | | |
| | | | | | | | |
| | Input Ctrl | | | | | | |
| | Schmitt | | | | | | |
| | Drive Strength | | | | | | |
| | ExtPort | read gpio data in input mode | 0xFF550070 | [13, 13] | | io -4 0xFF550070 | |

| Name | Config | Description | Register | Position | Value | IO Command | |
|---------|----------------|------------------------------|------------|----------|-------|---|------------|
| GPIO3B6 | IOMUX | VICAP_D6_M0 | 0xFF55804C | [8, 10] | 1 | io -4 0xFF55804C 0x07000100 | |
| | | MIPI_LVDS_D1N | 0xFF55804C | [8, 10] | 2 | io -4 0xFF55804C 0x07000200 | |
| | | GPIO3_B6 | 0xFF55804C | [8, 10] | 0 | io -4 0xFF55804C 0x07000000 io -4 0xFF3B4804 0x40000000 io -4 0xFE3E8000 0x0000007D io -4 0xFE3E8080 0x0000005F io -4 0xFE3E844C 0x00000001 | input only |
| | | Pull | | | | | |
| | I/O Mode | input | 0xFF550008 | [14, 14] | 0 | io -4 0xFF550008 0x40000000 | |
| | | | | | | | |
| | Output | | | | | | |
| | | | | | | | |
| | Input Ctrl | | | | | | |
| | Schmitt | | | | | | |
| | Drive Strength | | | | | | |
| | ExtPort | read gpio data in input mode | 0xFF550070 | [14, 14] | | io -4 0xFF550070 | |
| GPIO3B7 | IOMUX | VICAP_D7_M0 | 0xFF55804C | [12, 14] | 1 | io -4 0xFF55804C 0x70001000 | |
| | | MIPI_LVDS_D1P | 0xFF55804C | [12, 14] | 2 | io -4 0xFF55804C 0x70002000 | |
| | | GPIO3_B7 | 0xFF55804C | [12, 14] | 0 | io -4 0xFF55804C 0x70000000 io -4 0xFF3B4804 0x40000000 io -4 0xFE3E8000 0x0000007D io -4 0xFE3E8080 0x0000005F io -4 0xFE3E844C 0x00000001 | input only |
| | | Pull | | | | | |
| | I/O Mode | input | 0xFF550008 | [15, 15] | 0 | io -4 0xFF550008 0x80000000 | |
| | | | | | | | |
| | Output | | | | | | |
| | | | | | | | |
| | Input Ctrl | | | | | | |
| | Schmitt | | | | | | |
| | Drive Strength | | | | | | |
| | ExtPort | read gpio data in input mode | 0xFF550070 | [15, 15] | | io -4 0xFF550070 | |
| GPIO3C0 | IOMUX | VICAP_D8_M0 | 0xFF558050 | [0, 2] | 1 | io -4 0xFF558050 0x00070001 | |
| | | MIPI_LVDS_CKON | 0xFF558050 | [0, 2] | 2 | io -4 0xFF558050 0x00070002 | |
| | | GPIO3_C0 | 0xFF558050 | [0, 2] | 0 | io -4 0xFF558050 0x00070000 io -4 0xFF3B4804 0x40000000 io -4 0xFE3E8000 0x0000007D io -4 0xFE3E8080 0x0000005F io -4 0xFE3E844C 0x00000001 | input only |
| | | Pull | | | | | |
| | I/O Mode | input | 0xFF55000C | [0, 0] | 0 | io -4 0xFF55000C 0x00010000 | |
| | | | | | | | |
| | Output | | | | | | |
| | | | | | | | |
| | Input Ctrl | | | | | | |
| | Schmitt | | | | | | |
| | Drive Strength | | | | | | |
| | ExtPort | read gpio data in input mode | 0xFF550070 | [16, 16] | | io -4 0xFF550070 | |

| Name | Config | Description | Register | Position | Value | IO Command | |
|---------|----------------|------------------------------|------------|----------|-------|---|------------|
| GPIO3C1 | IOMUX | VICAP_D9_M0 | 0xFF558050 | [4, 6] | 1 | io -4 0xFF558050 0x00700010 | |
| | | MIPI_LVDS_CK0P | 0xFF558050 | [4, 6] | 2 | io -4 0xFF558050 0x00700020 | |
| | | GPIO3_C1 | 0xFF558050 | [4, 6] | 0 | io -4 0xFF558050 0x00700000 io -4 0xFF3B4804 0x40000000 io -4 0xFE3E8000 0x0000007D io -4 0xFE3E8080 0x0000005F io -4 0xFE3E844C 0x00000001 | input only |
| | | Pull | | | | | |
| | I/O Mode | input | 0xFF55000C | [1, 1] | 0 | io -4 0xFF55000C 0x00020000 | |
| | | | | | | | |
| | Output | | | | | | |
| | | | | | | | |
| | Input Ctrl | | | | | | |
| | Schmitt | | | | | | |
| | Drive Strength | | | | | | |
| | ExtPort | read gpio data in input mode | 0xFF550070 | [17, 17] | | io -4 0xFF550070 | |
| GPIO3C2 | IOMUX | VICAP_CLKIN_M0 | 0xFF558050 | [8, 10] | 1 | io -4 0xFF558050 0x07000100 | |
| | | MIPI_LVDS_D0N | 0xFF558050 | [8, 10] | 2 | io -4 0xFF558050 0x07000200 | |
| | | GPIO3_C2 | 0xFF558050 | [8, 10] | 0 | io -4 0xFF558050 0x07000000 io -4 0xFF3B4804 0x40000000 io -4 0xFE3E8000 0x0000007D io -4 0xFE3E8080 0x0000005F io -4 0xFE3E844C 0x00000001 | input only |
| | | Pull | | | | | |
| | I/O Mode | input | 0xFF55000C | [2, 2] | 0 | io -4 0xFF55000C 0x00040000 | |
| | | | | | | | |
| | Output | | | | | | |
| | | | | | | | |
| | Input Ctrl | | | | | | |
| | Schmitt | | | | | | |
| | Drive Strength | | | | | | |
| | ExtPort | read gpio data in input mode | 0xFF550070 | [18, 18] | | io -4 0xFF550070 | |
| GPIO3C3 | IOMUX | VICAP_HSYNC_M0 | 0xFF558050 | [12, 14] | 1 | io -4 0xFF558050 0x70001000 | |
| | | MIPI_LVDS_D0P | 0xFF558050 | [12, 14] | 2 | io -4 0xFF558050 0x70002000 | |
| | | GPIO3_C3 | 0xFF558050 | [12, 14] | 0 | io -4 0xFF558050 0x70000000 io -4 0xFF3B4804 0x40000000 io -4 0xFE3E8000 0x0000007D io -4 0xFE3E8080 0x0000005F io -4 0xFE3E844C 0x00000001 | input only |
| | | Pull | | | | | |
| | I/O Mode | input | 0xFF55000C | [3, 3] | 0 | io -4 0xFF55000C 0x00080000 | |
| | | | | | | | |
| | Output | | | | | | |
| | | | | | | | |
| | Input Ctrl | | | | | | |
| | Schmitt | | | | | | |
| | Drive Strength | | | | | | |
| | ExtPort | read gpio data in input mode | 0xFF550070 | [19, 19] | | io -4 0xFF550070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO3C4 | IOMUX | VICAP_CLKOUT_M0 | 0xFF558054 | [0, 2] | 1 | io -4 0xFF558054 0x00070001 | |
| | | MIPI_REFCLK_OUT0 | 0xFF558054 | [0, 2] | 2 | io -4 0xFF558054 0x00070002 | |
| | Pull | GPIO3_C4 | 0xFF558054 | [0, 2] | 0 | io -4 0xFF558054 0x00070000 | |
| | | normal | 0xFF5581E8 | [8, 9] | 0 | io -4 0xFF5581E8 0x03000000 | |
| | | pullup | 0xFF5581E8 | [8, 9] | 1 | io -4 0xFF5581E8 0x03000100 | |
| | | pulldown | 0xFF5581E8 | [8, 9] | 2 | io -4 0xFF5581E8 0x03000200 | |
| | I/O Mode | input | 0xFF55000C | [4, 4] | 0 | io -4 0xFF55000C 0x00100000 | |
| | | output | 0xFF55000C | [4, 4] | 1 | io -4 0xFF55000C 0x00100010 | |
| | Output | low | 0xFF550004 | [4, 4] | 0 | io -4 0xFF550004 0x00100000 | |
| | | high | 0xFF550004 | [4, 4] | 1 | io -4 0xFF550004 0x00100010 | |
| | Input Ctrl | disable | 0xFF5581A8 | [4, 4] | 0 | io -4 0xFF5581A8 0x00100000 | |
| | | enable | 0xFF5581A8 | [4, 4] | 1 | io -4 0xFF5581A8 0x00100010 | |
| | Schmitt | disable | 0xFF5582A8 | [4, 4] | 0 | io -4 0xFF5582A8 0x00100000 | |
| | | enable | 0xFF5582A8 | [4, 4] | 1 | io -4 0xFF5582A8 0x00100010 | |
| | Drive Strength | level0 | 0xFF558128 | [0, 5] | 1 | io -4 0xFF558128 0x003F0001 | |
| | | level1 | 0xFF558128 | [0, 5] | 3 | io -4 0xFF558128 0x003F0003 | |
| | | level2 | 0xFF558128 | [0, 5] | 7 | io -4 0xFF558128 0x003F0007 | |
| | | level3 | 0xFF558128 | [0, 5] | 15 | io -4 0xFF558128 0x003F000F | |
| | | level4 | 0xFF558128 | [0, 5] | 31 | io -4 0xFF558128 0x003F001F | |
| | ExtPort | level5 | 0xFF558128 | [0, 5] | 63 | io -4 0xFF558128 0x003F003F | |
| | | read_gpio_data_in_input_mode | 0xFF550070 | [20, 20] | | io -4 0xFF550070 | |
| GPIO3C5 | IOMUX | VICAP_VSYNC_M0 | 0xFF558054 | [4, 6] | 1 | io -4 0xFF558054 0x00700010 | |
| | | GPIO3_C5 | 0xFF558054 | [4, 6] | 0 | io -4 0xFF558054 0x00700000 | |
| | Pull | normal | 0xFF5581E8 | [10, 11] | 0 | io -4 0xFF5581E8 0x0C000000 | |
| | | pullup | 0xFF5581E8 | [10, 11] | 1 | io -4 0xFF5581E8 0x0C000400 | |
| | | pulldown | 0xFF5581E8 | [10, 11] | 2 | io -4 0xFF5581E8 0x0C000800 | |
| | I/O Mode | input | 0xFF55000C | [5, 5] | 0 | io -4 0xFF55000C 0x00200000 | |
| | | output | 0xFF55000C | [5, 5] | 1 | io -4 0xFF55000C 0x00200020 | |
| | Output | low | 0xFF550004 | [5, 5] | 0 | io -4 0xFF550004 0x00200000 | |
| | | high | 0xFF550004 | [5, 5] | 1 | io -4 0xFF550004 0x00200020 | |
| | Input Ctrl | disable | 0xFF5581A8 | [5, 5] | 0 | io -4 0xFF5581A8 0x00200000 | |
| | | enable | 0xFF5581A8 | [5, 5] | 1 | io -4 0xFF5581A8 0x00200020 | |
| | Schmitt | disable | 0xFF5582A8 | [5, 5] | 0 | io -4 0xFF5582A8 0x00200000 | |
| | | enable | 0xFF5582A8 | [5, 5] | 1 | io -4 0xFF5582A8 0x00200020 | |
| | Drive Strength | level0 | 0xFF558128 | [8, 13] | 1 | io -4 0xFF558128 0x3F000100 | |
| | | level1 | 0xFF558128 | [8, 13] | 3 | io -4 0xFF558128 0x3F000300 | |
| | | level2 | 0xFF558128 | [8, 13] | 7 | io -4 0xFF558128 0x3F000700 | |
| | | level3 | 0xFF558128 | [8, 13] | 15 | io -4 0xFF558128 0x3F000F00 | |
| | | level4 | 0xFF558128 | [8, 13] | 31 | io -4 0xFF558128 0x3F001F00 | |
| | ExtPort | level5 | 0xFF558128 | [8, 13] | 63 | io -4 0xFF558128 0x3F003F00 | |
| | | read_gpio_data_in_input_mode | 0xFF550070 | [21, 21] | | io -4 0xFF550070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO3C6 | IOMUX | VICAP_D10 | 0xFF558054 | [8, 10] | 1 | io -4 0xFF558054 0x07000100 | |
| | | PWM7_IR_M2 | 0xFF558054 | [8, 10] | 2 | io -4 0xFF558054 0x07000200 | |
| | | MIPI_REFCLK_OUT1 | 0xFF558054 | [8, 10] | 3 | io -4 0xFF558054 0x07000300 | |
| | | GPIO3_C6 | 0xFF558054 | [8, 10] | 0 | io -4 0xFF558054 0x07000000 | |
| | Pull | normal | 0xFF5581E8 | [12, 13] | 0 | io -4 0xFF5581E8 0x30000000 | |
| | | pullup | 0xFF5581E8 | [12, 13] | 1 | io -4 0xFF5581E8 0x30001000 | |
| | | pulldown | 0xFF5581E8 | [12, 13] | 2 | io -4 0xFF5581E8 0x30002000 | |
| | I/O Mode | input | 0xFF55000C | [6, 6] | 0 | io -4 0xFF55000C 0x00400000 | |
| | | output | 0xFF55000C | [6, 6] | 1 | io -4 0xFF55000C 0x00400040 | |
| | Output | low | 0xFF550004 | [6, 6] | 0 | io -4 0xFF550004 0x00400000 | |
| | | high | 0xFF550004 | [6, 6] | 1 | io -4 0xFF550004 0x00400040 | |
| | Input Ctrl | disable | 0xFF5581A8 | [6, 6] | 0 | io -4 0xFF5581A8 0x00400000 | |
| | | enable | 0xFF5581A8 | [6, 6] | 1 | io -4 0xFF5581A8 0x00400040 | |
| | Schmitt | disable | 0xFF5582A8 | [6, 6] | 0 | io -4 0xFF5582A8 0x00400000 | |
| | | enable | 0xFF5582A8 | [6, 6] | 1 | io -4 0xFF5582A8 0x00400040 | |
| | Drive Strength | level0 | 0xFF55812C | [0, 5] | 1 | io -4 0xFF55812C 0x003F0001 | |
| | | level1 | 0xFF55812C | [0, 5] | 3 | io -4 0xFF55812C 0x003F0003 | |
| | | level2 | 0xFF55812C | [0, 5] | 7 | io -4 0xFF55812C 0x003F0007 | |
| | | level3 | 0xFF55812C | [0, 5] | 15 | io -4 0xFF55812C 0x003F000F | |
| | | level4 | 0xFF55812C | [0, 5] | 31 | io -4 0xFF55812C 0x003F001F | |
| | | level5 | 0xFF55812C | [0, 5] | 63 | io -4 0xFF55812C 0x003F003F | |
| | ExtPort | read gpio data in input mode | 0xFF550070 | [22, 22] | | io -4 0xFF550070 | |
| GPIO3C7 | IOMUX | VICAP_D11 | 0xFF558054 | [12, 14] | 1 | io -4 0xFF558054 0x70001000 | |
| | | UART5_TX_M2 | 0xFF558054 | [12, 14] | 2 | io -4 0xFF558054 0x70002000 | |
| | | I2C4_SCL_M2 | 0xFF558054 | [12, 14] | 3 | io -4 0xFF558054 0x70003000 | |
| | | GPIO3_C7 | 0xFF558054 | [12, 14] | 0 | io -4 0xFF558054 0x70000000 | |
| | Pull | normal | 0xFF5581E8 | [14, 15] | 0 | io -4 0xFF5581E8 0xC0000000 | |
| | | pullup | 0xFF5581E8 | [14, 15] | 1 | io -4 0xFF5581E8 0xC0004000 | |
| | | pulldown | 0xFF5581E8 | [14, 15] | 2 | io -4 0xFF5581E8 0xC0008000 | |
| | I/O Mode | input | 0xFF55000C | [7, 7] | 0 | io -4 0xFF55000C 0x00800000 | |
| | | output | 0xFF55000C | [7, 7] | 1 | io -4 0xFF55000C 0x00800080 | |
| | Output | low | 0xFF550004 | [7, 7] | 0 | io -4 0xFF550004 0x00800000 | |
| | | high | 0xFF550004 | [7, 7] | 1 | io -4 0xFF550004 0x00800080 | |
| | Input Ctrl | disable | 0xFF5581A8 | [7, 7] | 0 | io -4 0xFF5581A8 0x00800000 | |
| | | enable | 0xFF5581A8 | [7, 7] | 1 | io -4 0xFF5581A8 0x00800080 | |
| | Schmitt | disable | 0xFF5582A8 | [7, 7] | 0 | io -4 0xFF5582A8 0x00800000 | |
| | | enable | 0xFF5582A8 | [7, 7] | 1 | io -4 0xFF5582A8 0x00800080 | |
| | Drive Strength | level0 | 0xFF55812C | [8, 13] | 1 | io -4 0xFF55812C 0x3F000100 | |
| | | level1 | 0xFF55812C | [8, 13] | 3 | io -4 0xFF55812C 0x3F000300 | |
| | | level2 | 0xFF55812C | [8, 13] | 7 | io -4 0xFF55812C 0x3F000700 | |
| | | level3 | 0xFF55812C | [8, 13] | 15 | io -4 0xFF55812C 0x3F000F00 | |
| | | level4 | 0xFF55812C | [8, 13] | 31 | io -4 0xFF55812C 0x3F001F00 | |
| | | level5 | 0xFF55812C | [8, 13] | 63 | io -4 0xFF55812C 0x3F003F00 | |
| | ExtPort | read gpio data in input mode | 0xFF550070 | [23, 23] | | io -4 0xFF550070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO3D0 | IOMUX | VICAP_D12 | 0xFF558058 | [0, 2] | 1 | io -4 0xFF558058 0x00070001 | |
| | | UART5_RX_M2 | 0xFF558058 | [0, 2] | 2 | io -4 0xFF558058 0x00070002 | |
| | | I2C4_SDA_M2 | 0xFF558058 | [0, 2] | 3 | io -4 0xFF558058 0x00070003 | |
| | | GPIO3_D0 | 0xFF558058 | [0, 2] | 0 | io -4 0xFF558058 0x00070000 | |
| | Pull | normal | 0xFF5581EC | [0, 1] | 0 | io -4 0xFF5581EC 0x00030000 | |
| | | pullup | 0xFF5581EC | [0, 1] | 1 | io -4 0xFF5581EC 0x00030001 | |
| | | pulldown | 0xFF5581EC | [0, 1] | 2 | io -4 0xFF5581EC 0x00030002 | |
| | I/O Mode | input | 0xFF55000C | [8, 8] | 0 | io -4 0xFF55000C 0x01000000 | |
| | | output | 0xFF55000C | [8, 8] | 1 | io -4 0xFF55000C 0x01000100 | |
| | Output | low | 0xFF550004 | [8, 8] | 0 | io -4 0xFF550004 0x01000000 | |
| | | high | 0xFF550004 | [8, 8] | 1 | io -4 0xFF550004 0x01000100 | |
| | Input Ctrl | disable | 0xFF5581AC | [0, 0] | 0 | io -4 0xFF5581AC 0x00010000 | |
| | | enable | 0xFF5581AC | [0, 0] | 1 | io -4 0xFF5581AC 0x00010001 | |
| | Schmitt | disable | 0xFF5582AC | [0, 0] | 0 | io -4 0xFF5582AC 0x00010000 | |
| | | enable | 0xFF5582AC | [0, 0] | 1 | io -4 0xFF5582AC 0x00010001 | |
| | Drive Strength | level0 | 0xFF558130 | [0, 5] | 1 | io -4 0xFF558130 0x003F0001 | |
| | | level1 | 0xFF558130 | [0, 5] | 3 | io -4 0xFF558130 0x003F0003 | |
| | | level2 | 0xFF558130 | [0, 5] | 7 | io -4 0xFF558130 0x003F0007 | |
| | | level3 | 0xFF558130 | [0, 5] | 15 | io -4 0xFF558130 0x003F000F | |
| | | level4 | 0xFF558130 | [0, 5] | 31 | io -4 0xFF558130 0x003F001F | |
| | | level5 | 0xFF558130 | [0, 5] | 63 | io -4 0xFF558130 0x003F003F | |
| | ExtPort | read gpio data in input mode | 0xFF550070 | [24, 24] | | io -4 0xFF550070 | |
| GPIO3D1 | IOMUX | VICAP_D13 | 0xFF558058 | [4, 6] | 1 | io -4 0xFF558058 0x00700010 | |
| | | UART5_RT5N_M2 | 0xFF558058 | [4, 6] | 2 | io -4 0xFF558058 0x00700020 | |
| | | I2C3_SCL_M2 | 0xFF558058 | [4, 6] | 3 | io -4 0xFF558058 0x00700030 | |
| | | GPIO3_D1 | 0xFF558058 | [4, 6] | 0 | io -4 0xFF558058 0x00700000 | |
| | Pull | normal | 0xFF5581EC | [2, 3] | 0 | io -4 0xFF5581EC 0x000C0000 | |
| | | pullup | 0xFF5581EC | [2, 3] | 1 | io -4 0xFF5581EC 0x000C0004 | |
| | | pulldown | 0xFF5581EC | [2, 3] | 2 | io -4 0xFF5581EC 0x000C0008 | |
| | I/O Mode | input | 0xFF55000C | [9, 9] | 0 | io -4 0xFF55000C 0x02000000 | |
| | | output | 0xFF55000C | [9, 9] | 1 | io -4 0xFF55000C 0x02000200 | |
| | Output | low | 0xFF550004 | [9, 9] | 0 | io -4 0xFF550004 0x02000000 | |
| | | high | 0xFF550004 | [9, 9] | 1 | io -4 0xFF550004 0x02000200 | |
| | Input Ctrl | disable | 0xFF5581AC | [1, 1] | 0 | io -4 0xFF5581AC 0x00020000 | |
| | | enable | 0xFF5581AC | [1, 1] | 1 | io -4 0xFF5581AC 0x00020002 | |
| | Schmitt | disable | 0xFF5582AC | [1, 1] | 0 | io -4 0xFF5582AC 0x00020000 | |
| | | enable | 0xFF5582AC | [1, 1] | 1 | io -4 0xFF5582AC 0x00020002 | |
| | Drive Strength | level0 | 0xFF558130 | [8, 13] | 1 | io -4 0xFF558130 0x3F000100 | |
| | | level1 | 0xFF558130 | [8, 13] | 3 | io -4 0xFF558130 0x3F000300 | |
| | | level2 | 0xFF558130 | [8, 13] | 7 | io -4 0xFF558130 0x3F000700 | |
| | | level3 | 0xFF558130 | [8, 13] | 15 | io -4 0xFF558130 0x3F000F00 | |
| | | level4 | 0xFF558130 | [8, 13] | 31 | io -4 0xFF558130 0x3F001F00 | |
| | | level5 | 0xFF558130 | [8, 13] | 63 | io -4 0xFF558130 0x3F003F00 | |
| | ExtPort | read gpio data in input mode | 0xFF550070 | [25, 25] | | io -4 0xFF550070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO3D2 | IOMux | VICAP D14 | 0xFF558058 | [8, 10] | 1 | io -4 0xFF558058 0x07000100 | |
| | | UART5 CTSN M2 | 0xFF558058 | [8, 10] | 2 | io -4 0xFF558058 0x07000200 | |
| | | I2C3 SDA M2 | 0xFF558058 | [8, 10] | 3 | io -4 0xFF558058 0x07000300 | |
| | | GPIO3 D2 | 0xFF558058 | [8, 10] | 0 | io -4 0xFF558058 0x07000000 | |
| | Pull | normal | 0xFF5581EC | [4, 5] | 0 | io -4 0xFF5581EC 0x00300000 | |
| | | pullup | 0xFF5581EC | [4, 5] | 1 | io -4 0xFF5581EC 0x00300010 | |
| | | pulldown | 0xFF5581EC | [4, 5] | 2 | io -4 0xFF5581EC 0x00300020 | |
| | I/O Mode | input | 0xFF55000C | [10, 10] | 0 | io -4 0xFF55000C 0x04000000 | |
| | | output | 0xFF55000C | [10, 10] | 1 | io -4 0xFF55000C 0x04000400 | |
| | Output | low | 0xFF550004 | [10, 10] | 0 | io -4 0xFF550004 0x04000000 | |
| | | high | 0xFF550004 | [10, 10] | 1 | io -4 0xFF550004 0x04000400 | |
| | Input Ctrl | disable | 0xFF5581AC | [2, 2] | 0 | io -4 0xFF5581AC 0x00040000 | |
| | | enable | 0xFF5581AC | [2, 2] | 1 | io -4 0xFF5581AC 0x00040004 | |
| | Schmitt | disable | 0xFF5582AC | [2, 2] | 0 | io -4 0xFF5582AC 0x00040000 | |
| | | enable | 0xFF5582AC | [2, 2] | 1 | io -4 0xFF5582AC 0x00040004 | |
| | Drive Strength | level0 | 0xFF558134 | [0, 5] | 1 | io -4 0xFF558134 0x003F0001 | |
| | | level1 | 0xFF558134 | [0, 5] | 3 | io -4 0xFF558134 0x003F0003 | |
| | | level2 | 0xFF558134 | [0, 5] | 7 | io -4 0xFF558134 0x003F0007 | |
| | | level3 | 0xFF558134 | [0, 5] | 15 | io -4 0xFF558134 0x003F000F | |
| | | level4 | 0xFF558134 | [0, 5] | 31 | io -4 0xFF558134 0x003F001F | |
| | | level5 | 0xFF558134 | [0, 5] | 63 | io -4 0xFF558134 0x003F003F | |
| | ExtPort | read gpio data in input mode | 0xFF550070 | [26, 26] | | io -4 0xFF550070 | |
| GPIO3D3 | IOMux | VICAP D15 | 0xFF558058 | [12, 14] | 1 | io -4 0xFF558058 0x70001000 | |
| | | PWM1 M2 | 0xFF558058 | [12, 14] | 2 | io -4 0xFF558058 0x70002000 | |
| | | GPIO3 D3 | 0xFF558058 | [12, 14] | 0 | io -4 0xFF558058 0x70000000 | |
| | Pull | normal | 0xFF5581EC | [6, 7] | 0 | io -4 0xFF5581EC 0x00C00000 | |
| | | pullup | 0xFF5581EC | [6, 7] | 1 | io -4 0xFF5581EC 0x00C00040 | |
| | | pulldown | 0xFF5581EC | [6, 7] | 2 | io -4 0xFF5581EC 0x00C00080 | |
| | I/O Mode | input | 0xFF55000C | [11, 11] | 0 | io -4 0xFF55000C 0x08000000 | |
| | | output | 0xFF55000C | [11, 11] | 1 | io -4 0xFF55000C 0x08000800 | |
| | Output | low | 0xFF550004 | [11, 11] | 0 | io -4 0xFF550004 0x08000000 | |
| | | high | 0xFF550004 | [11, 11] | 1 | io -4 0xFF550004 0x08000800 | |
| | Input Ctrl | disable | 0xFF5581AC | [3, 3] | 0 | io -4 0xFF5581AC 0x00080000 | |
| | | enable | 0xFF5581AC | [3, 3] | 1 | io -4 0xFF5581AC 0x00080008 | |
| | Schmitt | disable | 0xFF5582AC | [3, 3] | 0 | io -4 0xFF5582AC 0x00080000 | |
| | | enable | 0xFF5582AC | [3, 3] | 1 | io -4 0xFF5582AC 0x00080008 | |
| | Drive Strength | level0 | 0xFF558134 | [8, 13] | 1 | io -4 0xFF558134 0x3F000100 | |
| | | level1 | 0xFF558134 | [8, 13] | 3 | io -4 0xFF558134 0x3F000300 | |
| | | level2 | 0xFF558134 | [8, 13] | 7 | io -4 0xFF558134 0x3F000700 | |
| | | level3 | 0xFF558134 | [8, 13] | 15 | io -4 0xFF558134 0x3F000F00 | |
| | | level4 | 0xFF558134 | [8, 13] | 31 | io -4 0xFF558134 0x3F001F00 | |
| | | level5 | 0xFF558134 | [8, 13] | 63 | io -4 0xFF558134 0x3F003F00 | |
| | ExtPort | read gpio data in input mode | 0xFF550070 | [27, 27] | | io -4 0xFF550070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO4A0 | IOMUX | EMMC D7 | 0xFF568000 | [0, 2] | 1 | io -4 0xFF568000 0x00070001 | |
| | | SPI1 MISO M0 | 0xFF568000 | [0, 2] | 2 | io -4 0xFF568000 0x00070002 | |
| | | UART0 RX M2 | 0xFF568000 | [0, 2] | 3 | io -4 0xFF568000 0x00070003 | |
| | | I2C0 SDA M1 | 0xFF568000 | [0, 2] | 4 | io -4 0xFF568000 0x00070004 | |
| | | TEST CLK2 OUT | 0xFF568000 | [0, 2] | 5 | io -4 0xFF568000 0x00070005 | |
| | Pull | GPIO4 A0 | 0xFF568000 | [0, 2] | 0 | io -4 0xFF568000 0x00070000 | |
| | | normal | 0xFF568070 | [0, 1] | 0 | io -4 0xFF568070 0x00030000 | |
| | | pullup | 0xFF568070 | [0, 1] | 1 | io -4 0xFF568070 0x00030001 | |
| | | pulldown | 0xFF568070 | [0, 1] | 2 | io -4 0xFF568070 0x00030002 | |
| | I/O Mode | input | 0xFF560008 | [0, 0] | 0 | io -4 0xFF560008 0x00010000 | |
| | | output | 0xFF560008 | [0, 0] | 1 | io -4 0xFF560008 0x00010001 | |
| | Output | low | 0xFF560000 | [0, 0] | 0 | io -4 0xFF560000 0x00010000 | |
| | | high | 0xFF560000 | [0, 0] | 1 | io -4 0xFF560000 0x00010001 | |
| | Input Ctrl | disable | 0xFF568060 | [0, 0] | 0 | io -4 0xFF568060 0x00010000 | |
| | | enable | 0xFF568060 | [0, 0] | 1 | io -4 0xFF568060 0x00010001 | |
| | Schmitt | disable | 0xFF5680A0 | [0, 0] | 0 | io -4 0xFF5680A0 0x00010000 | |
| | | enable | 0xFF5680A0 | [0, 0] | 1 | io -4 0xFF5680A0 0x00010001 | |
| | Drive Strength | level0 | 0xFF568020 | [0, 5] | 1 | io -4 0xFF568020 0x003F0001 | |
| | | level1 | 0xFF568020 | [0, 5] | 3 | io -4 0xFF568020 0x003F0003 | |
| | | level2 | 0xFF568020 | [0, 5] | 7 | io -4 0xFF568020 0x003F0007 | |
| | | level3 | 0xFF568020 | [0, 5] | 15 | io -4 0xFF568020 0x003F000F | |
| | | level4 | 0xFF568020 | [0, 5] | 31 | io -4 0xFF568020 0x003F001F | |
| | | level5 | 0xFF568020 | [0, 5] | 63 | io -4 0xFF568020 0x003F003F | |
| | ExtPort | read gpio data in input mode | 0xFF560070 | [0, 0] | | io -4 0xFF560070 | |
| GPIO4A1 | IOMUX | EMMC D6 | 0xFF568000 | [4, 6] | 1 | io -4 0xFF568000 0x00700010 | |
| | | SPI1 MOSI M0 | 0xFF568000 | [4, 6] | 2 | io -4 0xFF568000 0x00700020 | |
| | | UART0 TX M2 | 0xFF568000 | [4, 6] | 3 | io -4 0xFF568000 0x00700030 | |
| | | I2C0 SCL M1 | 0xFF568000 | [4, 6] | 4 | io -4 0xFF568000 0x00700040 | |
| | | TEST CLK3 OUT | 0xFF568000 | [4, 6] | 5 | io -4 0xFF568000 0x00700050 | |
| | Pull | GPIO4 A1 | 0xFF568000 | [4, 6] | 0 | io -4 0xFF568000 0x00700000 | |
| | | normal | 0xFF568070 | [2, 3] | 0 | io -4 0xFF568070 0x000C0000 | |
| | | pullup | 0xFF568070 | [2, 3] | 1 | io -4 0xFF568070 0x000C0004 | |
| | | pulldown | 0xFF568070 | [2, 3] | 2 | io -4 0xFF568070 0x000C0008 | |
| | I/O Mode | input | 0xFF560008 | [1, 1] | 0 | io -4 0xFF560008 0x00020000 | |
| | | output | 0xFF560008 | [1, 1] | 1 | io -4 0xFF560008 0x00020002 | |
| | Output | low | 0xFF560000 | [1, 1] | 0 | io -4 0xFF560000 0x00020000 | |
| | | high | 0xFF560000 | [1, 1] | 1 | io -4 0xFF560000 0x00020002 | |
| | Input Ctrl | disable | 0xFF568060 | [1, 1] | 0 | io -4 0xFF568060 0x00020000 | |
| | | enable | 0xFF568060 | [1, 1] | 1 | io -4 0xFF568060 0x00020002 | |
| | Schmitt | disable | 0xFF5680A0 | [1, 1] | 0 | io -4 0xFF5680A0 0x00020000 | |
| | | enable | 0xFF5680A0 | [1, 1] | 1 | io -4 0xFF5680A0 0x00020002 | |
| | Drive Strength | level0 | 0xFF568020 | [8, 13] | 1 | io -4 0xFF568020 0x3F000100 | |
| | | level1 | 0xFF568020 | [8, 13] | 3 | io -4 0xFF568020 0x3F000300 | |
| | | level2 | 0xFF568020 | [8, 13] | 7 | io -4 0xFF568020 0x3F000700 | |
| | | level3 | 0xFF568020 | [8, 13] | 15 | io -4 0xFF568020 0x3F000F00 | |
| | | level4 | 0xFF568020 | [8, 13] | 31 | io -4 0xFF568020 0x3F001F00 | |
| | | level5 | 0xFF568020 | [8, 13] | 63 | io -4 0xFF568020 0x3F003F00 | |
| | ExtPort | read gpio data in input mode | 0xFF560070 | [1, 1] | | io -4 0xFF560070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO4A2 | IOMUX | EMMC D2 | 0xFF568000 | [8, 10] | 1 | io -4 0xFF568000 0x07000100 | |
| | | FSPI D2 | 0xFF568000 | [8, 10] | 2 | io -4 0xFF568000 0x07000200 | |
| | | GPIO4 A2 | 0xFF568000 | [8, 10] | 0 | io -4 0xFF568000 0x07000000 | |
| | Pull | normal | 0xFF568070 | [4, 5] | 0 | io -4 0xFF568070 0x00300000 | |
| | | pullup | 0xFF568070 | [4, 5] | 1 | io -4 0xFF568070 0x00300010 | |
| | | pulldown | 0xFF568070 | [4, 5] | 2 | io -4 0xFF568070 0x00300020 | |
| | I/O Mode | input | 0xFF560008 | [2, 2] | 0 | io -4 0xFF560008 0x00040000 | |
| | | output | 0xFF560008 | [2, 2] | 1 | io -4 0xFF560008 0x00040004 | |
| | Output | low | 0xFF560000 | [2, 2] | 0 | io -4 0xFF560000 0x00040000 | |
| | | high | 0xFF560000 | [2, 2] | 1 | io -4 0xFF560000 0x00040004 | |
| | Input Ctrl | disable | 0xFF568060 | [2, 2] | 0 | io -4 0xFF568060 0x00040000 | |
| | | enable | 0xFF568060 | [2, 2] | 1 | io -4 0xFF568060 0x00040004 | |
| | Schmitt | disable | 0xFF5680A0 | [2, 2] | 0 | io -4 0xFF5680A0 0x00040000 | |
| | | enable | 0xFF5680A0 | [2, 2] | 1 | io -4 0xFF5680A0 0x00040004 | |
| | Drive Strength | level0 | 0xFF568024 | [0, 5] | 1 | io -4 0xFF568024 0x003F0001 | |
| | | level1 | 0xFF568024 | [0, 5] | 3 | io -4 0xFF568024 0x003F0003 | |
| | | level2 | 0xFF568024 | [0, 5] | 7 | io -4 0xFF568024 0x003F0007 | |
| | | level3 | 0xFF568024 | [0, 5] | 15 | io -4 0xFF568024 0x003F000F | |
| | | level4 | 0xFF568024 | [0, 5] | 31 | io -4 0xFF568024 0x003F001F | |
| | ExtPort | level5 | 0xFF568024 | [0, 5] | 63 | io -4 0xFF568024 0x003F003F | |
| | | read gpio data in input mode | 0xFF560070 | [2, 2] | | io -4 0xFF560070 | |
| GPIO4A3 | IOMUX | EMMC D1 | 0xFF568000 | [12, 14] | 1 | io -4 0xFF568000 0x70001000 | |
| | | FSPI D1 | 0xFF568000 | [12, 14] | 2 | io -4 0xFF568000 0x70002000 | |
| | | GPIO4 A3 | 0xFF568000 | [12, 14] | 0 | io -4 0xFF568000 0x70000000 | |
| | Pull | normal | 0xFF568070 | [6, 7] | 0 | io -4 0xFF568070 0x00C00000 | |
| | | pullup | 0xFF568070 | [6, 7] | 1 | io -4 0xFF568070 0x00C00040 | |
| | | pulldown | 0xFF568070 | [6, 7] | 2 | io -4 0xFF568070 0x00C00080 | |
| | I/O Mode | input | 0xFF560008 | [3, 3] | 0 | io -4 0xFF560008 0x00080000 | |
| | | output | 0xFF560008 | [3, 3] | 1 | io -4 0xFF560008 0x00080008 | |
| | Output | low | 0xFF560000 | [3, 3] | 0 | io -4 0xFF560000 0x00080000 | |
| | | high | 0xFF560000 | [3, 3] | 1 | io -4 0xFF560000 0x00080008 | |
| | Input Ctrl | disable | 0xFF568060 | [3, 3] | 0 | io -4 0xFF568060 0x00080000 | |
| | | enable | 0xFF568060 | [3, 3] | 1 | io -4 0xFF568060 0x00080008 | |
| | Schmitt | disable | 0xFF5680A0 | [3, 3] | 0 | io -4 0xFF5680A0 0x00080000 | |
| | | enable | 0xFF5680A0 | [3, 3] | 1 | io -4 0xFF5680A0 0x00080008 | |
| | Drive Strength | level0 | 0xFF568024 | [8, 13] | 1 | io -4 0xFF568024 0x3F000100 | |
| | | level1 | 0xFF568024 | [8, 13] | 3 | io -4 0xFF568024 0x3F000300 | |
| | | level2 | 0xFF568024 | [8, 13] | 7 | io -4 0xFF568024 0x3F000700 | |
| | | level3 | 0xFF568024 | [8, 13] | 15 | io -4 0xFF568024 0x3F000F00 | |
| | | level4 | 0xFF568024 | [8, 13] | 31 | io -4 0xFF568024 0x3F001F00 | |
| | ExtPort | level5 | 0xFF568024 | [8, 13] | 63 | io -4 0xFF568024 0x3F003F00 | |
| | | read gpio data in input mode | 0xFF560070 | [3, 3] | | io -4 0xFF560070 | |

| Name | Config | Description | Register | Position | Value | IO Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO4A4 | IOMUX | EMMC D0 | 0xFF568004 | [0, 2] | 1 | io -4 0xFF568004 0x00070001 | |
| | | FSPI D0 | 0xFF568004 | [0, 2] | 2 | io -4 0xFF568004 0x00070002 | |
| | | GPIO4 A4 | 0xFF568004 | [0, 2] | 0 | io -4 0xFF568004 0x00070000 | |
| | Pull | normal | 0xFF568070 | [8, 9] | 0 | io -4 0xFF568070 0x03000000 | |
| | | pullup | 0xFF568070 | [8, 9] | 1 | io -4 0xFF568070 0x03000100 | |
| | | pulldown | 0xFF568070 | [8, 9] | 2 | io -4 0xFF568070 0x03000200 | |
| | I/O Mode | input | 0xFF560008 | [4, 4] | 0 | io -4 0xFF560008 0x00100000 | |
| | | output | 0xFF560008 | [4, 4] | 1 | io -4 0xFF560008 0x00100010 | |
| | Output | low | 0xFF560000 | [4, 4] | 0 | io -4 0xFF560000 0x00100000 | |
| | | high | 0xFF560000 | [4, 4] | 1 | io -4 0xFF560000 0x00100010 | |
| | Input Ctrl | disable | 0xFF568060 | [4, 4] | 0 | io -4 0xFF568060 0x00100000 | |
| | | enable | 0xFF568060 | [4, 4] | 1 | io -4 0xFF568060 0x00100010 | |
| | Schmitt | disable | 0xFF5680A0 | [4, 4] | 0 | io -4 0xFF5680A0 0x00100000 | |
| | | enable | 0xFF5680A0 | [4, 4] | 1 | io -4 0xFF5680A0 0x00100010 | |
| | Drive Strength | level0 | 0xFF568028 | [0, 5] | 1 | io -4 0xFF568028 0x003F0001 | |
| | | level1 | 0xFF568028 | [0, 5] | 3 | io -4 0xFF568028 0x003F0003 | |
| | | level2 | 0xFF568028 | [0, 5] | 7 | io -4 0xFF568028 0x003F0007 | |
| | | level3 | 0xFF568028 | [0, 5] | 15 | io -4 0xFF568028 0x003F000F | |
| | | level4 | 0xFF568028 | [0, 5] | 31 | io -4 0xFF568028 0x003F001F | |
| | ExtPort | level5 | 0xFF568028 | [0, 5] | 63 | io -4 0xFF568028 0x003F003F | |
| | | read gpio data in input mode | 0xFF560070 | [4, 4] | | io -4 0xFF560070 | |
| GPIO4A5 | IOMUX | EMMC D4 | 0xFF568004 | [4, 6] | 1 | io -4 0xFF568004 0x00700010 | |
| | | SPI1 CS0N M0 | 0xFF568004 | [4, 6] | 2 | io -4 0xFF568004 0x00700020 | |
| | | UART1 TX M2 | 0xFF568004 | [4, 6] | 3 | io -4 0xFF568004 0x00700030 | |
| | | I2C2 SDA M1 | 0xFF568004 | [4, 6] | 4 | io -4 0xFF568004 0x00700040 | |
| | | GPIO4 A5 | 0xFF568004 | [4, 6] | 0 | io -4 0xFF568004 0x00700000 | |
| | Pull | normal | 0xFF568070 | [10, 11] | 0 | io -4 0xFF568070 0x0C000000 | |
| | | pullup | 0xFF568070 | [10, 11] | 1 | io -4 0xFF568070 0x0C000400 | |
| | | pulldown | 0xFF568070 | [10, 11] | 2 | io -4 0xFF568070 0x0C000800 | |
| | I/O Mode | input | 0xFF560008 | [5, 5] | 0 | io -4 0xFF560008 0x00200000 | |
| | | output | 0xFF560008 | [5, 5] | 1 | io -4 0xFF560008 0x00200020 | |
| | Output | low | 0xFF560000 | [5, 5] | 0 | io -4 0xFF560000 0x00200000 | |
| | | high | 0xFF560000 | [5, 5] | 1 | io -4 0xFF560000 0x00200020 | |
| | Input Ctrl | disable | 0xFF568060 | [5, 5] | 0 | io -4 0xFF568060 0x00200000 | |
| | | enable | 0xFF568060 | [5, 5] | 1 | io -4 0xFF568060 0x00200020 | |
| | Schmitt | disable | 0xFF5680A0 | [5, 5] | 0 | io -4 0xFF5680A0 0x00200000 | |
| | | enable | 0xFF5680A0 | [5, 5] | 1 | io -4 0xFF5680A0 0x00200020 | |
| | Drive Strength | level0 | 0xFF568028 | [8, 13] | 1 | io -4 0xFF568028 0x3F000100 | |
| | | level1 | 0xFF568028 | [8, 13] | 3 | io -4 0xFF568028 0x3F000300 | |
| | | level2 | 0xFF568028 | [8, 13] | 7 | io -4 0xFF568028 0x3F000700 | |
| | | level3 | 0xFF568028 | [8, 13] | 15 | io -4 0xFF568028 0x3F000F00 | |
| | | level4 | 0xFF568028 | [8, 13] | 31 | io -4 0xFF568028 0x3F001F00 | |
| | ExtPort | level5 | 0xFF568028 | [8, 13] | 63 | io -4 0xFF568028 0x3F003F00 | |
| | | read gpio data in input mode | 0xFF560070 | [5, 5] | | io -4 0xFF560070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO4A6 | IOMUX | EMMC D3 | 0xFF568004 | [8, 10] | 1 | io -4 0xFF568004 0x07000100 | |
| | | FSPI D3 | 0xFF568004 | [8, 10] | 2 | io -4 0xFF568004 0x07000200 | |
| | | GPIO4 A6 | 0xFF568004 | [8, 10] | 0 | io -4 0xFF568004 0x07000000 | |
| | Pull | normal | 0xFF568070 | [12, 13] | 0 | io -4 0xFF568070 0x30000000 | |
| | | pullup | 0xFF568070 | [12, 13] | 1 | io -4 0xFF568070 0x30001000 | |
| | | pulldown | 0xFF568070 | [12, 13] | 2 | io -4 0xFF568070 0x30002000 | |
| | I/O Mode | input | 0xFF560008 | [6, 6] | 0 | io -4 0xFF560008 0x00400000 | |
| | | output | 0xFF560008 | [6, 6] | 1 | io -4 0xFF560008 0x00400040 | |
| | Output | low | 0xFF560000 | [6, 6] | 0 | io -4 0xFF560000 0x00400000 | |
| | | high | 0xFF560000 | [6, 6] | 1 | io -4 0xFF560000 0x00400040 | |
| | Input Ctrl | disable | 0xFF568060 | [6, 6] | 0 | io -4 0xFF568060 0x00400000 | |
| | | enable | 0xFF568060 | [6, 6] | 1 | io -4 0xFF568060 0x00400040 | |
| | Schmitt | disable | 0xFF5680A0 | [6, 6] | 0 | io -4 0xFF5680A0 0x00400000 | |
| | | enable | 0xFF5680A0 | [6, 6] | 1 | io -4 0xFF5680A0 0x00400040 | |
| | Drive Strength | level0 | 0xFF56802C | [0, 5] | 1 | io -4 0xFF56802C 0x003F0001 | |
| | | level1 | 0xFF56802C | [0, 5] | 3 | io -4 0xFF56802C 0x003F0003 | |
| | | level2 | 0xFF56802C | [0, 5] | 7 | io -4 0xFF56802C 0x003F0007 | |
| | | level3 | 0xFF56802C | [0, 5] | 15 | io -4 0xFF56802C 0x003F000F | |
| | | level4 | 0xFF56802C | [0, 5] | 31 | io -4 0xFF56802C 0x003F001F | |
| | ExtPort | level5 | 0xFF56802C | [0, 5] | 63 | io -4 0xFF56802C 0x003F003F | |
| | | read gpio data in input mode | 0xFF560070 | [6, 6] | | io -4 0xFF560070 | |
| GPIO4A7 | IOMUX | EMMC D5 | 0xFF568004 | [12, 14] | 1 | io -4 0xFF568004 0x70001000 | |
| | | SPI1 CLK M0 | 0xFF568004 | [12, 14] | 2 | io -4 0xFF568004 0x70002000 | |
| | | UART1 RX M2 | 0xFF568004 | [12, 14] | 3 | io -4 0xFF568004 0x70003000 | |
| | | I2C2_SCL M1 | 0xFF568004 | [12, 14] | 4 | io -4 0xFF568004 0x70004000 | |
| | | GPIO4 A7 | 0xFF568004 | [12, 14] | 0 | io -4 0xFF568004 0x70000000 | |
| | Pull | normal | 0xFF568070 | [14, 15] | 0 | io -4 0xFF568070 0xC0000000 | |
| | | pullup | 0xFF568070 | [14, 15] | 1 | io -4 0xFF568070 0xC0004000 | |
| | | pulldown | 0xFF568070 | [14, 15] | 2 | io -4 0xFF568070 0xC0008000 | |
| | I/O Mode | input | 0xFF560008 | [7, 7] | 0 | io -4 0xFF560008 0x00800000 | |
| | | output | 0xFF560008 | [7, 7] | 1 | io -4 0xFF560008 0x00800080 | |
| | Output | low | 0xFF560000 | [7, 7] | 0 | io -4 0xFF560000 0x00800000 | |
| | | high | 0xFF560000 | [7, 7] | 1 | io -4 0xFF560000 0x00800080 | |
| | Input Ctrl | disable | 0xFF568060 | [7, 7] | 0 | io -4 0xFF568060 0x00800000 | |
| | | enable | 0xFF568060 | [7, 7] | 1 | io -4 0xFF568060 0x00800080 | |
| | Schmitt | disable | 0xFF5680A0 | [7, 7] | 0 | io -4 0xFF5680A0 0x00800000 | |
| | | enable | 0xFF5680A0 | [7, 7] | 1 | io -4 0xFF5680A0 0x00800080 | |
| | Drive Strength | level0 | 0xFF56802C | [8, 13] | 1 | io -4 0xFF56802C 0x3F000100 | |
| | | level1 | 0xFF56802C | [8, 13] | 3 | io -4 0xFF56802C 0x3F000300 | |
| | | level2 | 0xFF56802C | [8, 13] | 7 | io -4 0xFF56802C 0x3F000700 | |
| | | level3 | 0xFF56802C | [8, 13] | 15 | io -4 0xFF56802C 0x3F000F00 | |
| | | level4 | 0xFF56802C | [8, 13] | 31 | io -4 0xFF56802C 0x3F001F00 | |
| | ExtPort | level5 | 0xFF56802C | [8, 13] | 63 | io -4 0xFF56802C 0x3F003F00 | |
| | | read gpio data in input mode | 0xFF560070 | [7, 7] | | io -4 0xFF560070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|-----------------------------|--|
| GPIO4B0 | IOMUX | EMMC_CMD | 0xFF568008 | [0, 2] | 1 | io -4 0xFF568008 0x00070001 | |
| | | FSPI_CSON | 0xFF568008 | [0, 2] | 2 | io -4 0xFF568008 0x00070002 | |
| | | GPIO4_B0 | 0xFF568008 | [0, 2] | 0 | io -4 0xFF568008 0x00070000 | |
| | Pull | normal | 0xFF568074 | [0, 1] | 0 | io -4 0xFF568074 0x00030000 | |
| | | pullup | 0xFF568074 | [0, 1] | 1 | io -4 0xFF568074 0x00030001 | |
| | | pulldown | 0xFF568074 | [0, 1] | 2 | io -4 0xFF568074 0x00030002 | |
| | I/O Mode | input | 0xFF560008 | [8, 8] | 0 | io -4 0xFF560008 0x01000000 | |
| | | output | 0xFF560008 | [8, 8] | 1 | io -4 0xFF560008 0x01000100 | |
| | Output | low | 0xFF560000 | [8, 8] | 0 | io -4 0xFF560000 0x01000000 | |
| | | high | 0xFF560000 | [8, 8] | 1 | io -4 0xFF560000 0x01000100 | |
| | Input Ctrl | disable | 0xFF568064 | [0, 0] | 0 | io -4 0xFF568064 0x00010000 | |
| | | enable | 0xFF568064 | [0, 0] | 1 | io -4 0xFF568064 0x00010001 | |
| | Schmitt | disable | 0xFF5680A4 | [0, 0] | 0 | io -4 0xFF5680A4 0x00010000 | |
| | | enable | 0xFF5680A4 | [0, 0] | 1 | io -4 0xFF5680A4 0x00010001 | |
| | Drive Strength | level0 | 0xFF568030 | [0, 5] | 1 | io -4 0xFF568030 0x003F0001 | |
| | | level1 | 0xFF568030 | [0, 5] | 3 | io -4 0xFF568030 0x003F0003 | |
| | | level2 | 0xFF568030 | [0, 5] | 7 | io -4 0xFF568030 0x003F0007 | |
| | | level3 | 0xFF568030 | [0, 5] | 15 | io -4 0xFF568030 0x003F000F | |
| | | level4 | 0xFF568030 | [0, 5] | 31 | io -4 0xFF568030 0x003F001F | |
| | ExtPort | level5 | 0xFF568030 | [0, 5] | 63 | io -4 0xFF568030 0x003F003F | |
| | | read gpio data in input mode | 0xFF560070 | [8, 8] | | io -4 0xFF560070 | |
| GPIO4B1 | IOMUX | EMMC_CLK | 0xFF568008 | [4, 6] | 1 | io -4 0xFF568008 0x00700010 | |
| | | FSPI_CLK | 0xFF568008 | [4, 6] | 2 | io -4 0xFF568008 0x00700020 | |
| | | GPIO4_B1 | 0xFF568008 | [4, 6] | 0 | io -4 0xFF568008 0x00700000 | |
| | Pull | normal | 0xFF568074 | [2, 3] | 0 | io -4 0xFF568074 0x000C0000 | |
| | | pullup | 0xFF568074 | [2, 3] | 1 | io -4 0xFF568074 0x000C0004 | |
| | | pulldown | 0xFF568074 | [2, 3] | 2 | io -4 0xFF568074 0x000C0008 | |
| | I/O Mode | input | 0xFF560008 | [9, 9] | 0 | io -4 0xFF560008 0x02000000 | |
| | | output | 0xFF560008 | [9, 9] | 1 | io -4 0xFF560008 0x02000200 | |
| | Output | low | 0xFF560000 | [9, 9] | 0 | io -4 0xFF560000 0x02000000 | |
| | | high | 0xFF560000 | [9, 9] | 1 | io -4 0xFF560000 0x02000200 | |
| | Input Ctrl | disable | 0xFF568064 | [1, 1] | 0 | io -4 0xFF568064 0x00020000 | |
| | | enable | 0xFF568064 | [1, 1] | 1 | io -4 0xFF568064 0x00020002 | |
| | Schmitt | disable | 0xFF5680A4 | [1, 1] | 0 | io -4 0xFF5680A4 0x00020000 | |
| | | enable | 0xFF5680A4 | [1, 1] | 1 | io -4 0xFF5680A4 0x00020002 | |
| | Drive Strength | level0 | 0xFF568030 | [8, 13] | 1 | io -4 0xFF568030 0x3F000100 | |
| | | level1 | 0xFF568030 | [8, 13] | 3 | io -4 0xFF568030 0x3F000300 | |
| | | level2 | 0xFF568030 | [8, 13] | 7 | io -4 0xFF568030 0x3F000700 | |
| | | level3 | 0xFF568030 | [8, 13] | 15 | io -4 0xFF568030 0x3F000F00 | |
| | | level4 | 0xFF568030 | [8, 13] | 31 | io -4 0xFF568030 0x3F001F00 | |
| | ExtPort | level5 | 0xFF568030 | [8, 13] | 63 | io -4 0xFF568030 0x3F003F00 | |
| | | read gpio data in input mode | 0xFF560070 | [9, 9] | | io -4 0xFF560070 | |

| Name | Config | Description | Register | Position | Value | I/O Command | |
|---------|----------------|------------------------------|------------|----------|-------|--|------------|
| GPIO4C0 | IOMUX | ADC_IN0 | 0xFF568010 | [0, 2] | 1 | io -4 0xFF568010 0x00070001 | |
| | | GPIO4_C0 | 0xFF568010 | [0, 2] | 0 | io -4 0xFF568010 0x00070000 io -4 0xFF5680C0 0x00040004 | input only |
| | Pull | normal | 0xFF5680C0 | [13, 14] | 0 | io -4 0xFF5680C0 0x60000000 | |
| | | pullup | 0xFF5680C0 | [13, 14] | 3 | io -4 0xFF5680C0 0x60006000 | |
| | | pulldown | 0xFF5680C0 | [13, 14] | 1 | io -4 0xFF5680C0 0x60002000 | |
| | I/O Mode | input | 0xFF56000C | [0, 0] | 0 | io -4 0xFF56000C 0x00010000 | |
| | | | | | | | |
| | Output | | | | | | |
| | Input Ctrl | disable | 0xFF5680C0 | [2, 2] | 0 | io -4 0xFF5680C0 0x00040000 | |
| | | enable | 0xFF5680C0 | [2, 2] | 1 | io -4 0xFF5680C0 0x00040004 | |
| | Schmitt | | | | | | |
| | Drive Strength | level0 | 0xFF5680C0 | [10, 11] | 0 | io -4 0xFF5680C0 0x0C000000 | |
| | | level1 | 0xFF5680C0 | [10, 11] | 1 | io -4 0xFF5680C0 0x0C000400 | |
| | | level2 | 0xFF5680C0 | [10, 11] | 2 | io -4 0xFF5680C0 0x0C000800 | |
| | | level3 | 0xFF5680C0 | [10, 11] | 3 | io -4 0xFF5680C0 0x0C000C00 | |
| | ExtPort | read gpio data in input mode | 0xFF560070 | [16, 16] | | io -4 0xFF560070 | |
| GPIO4C1 | IOMUX | ADC_IN1 | 0xFF568010 | [4, 6] | 1 | io -4 0xFF568010 0x00700010 | |
| | | PWM1_M1 | 0xFF568010 | [4, 6] | 2 | io -4 0xFF568010 0x00700020 | |
| | | GPIO4_C1 | 0xFF568010 | [4, 6] | 0 | io -4 0xFF568010 0x00700000 io -4 0xFF5680C0 0x00080008 | input only |
| | Pull | normal | 0xFF5680C0 | [13, 14] | 0 | io -4 0xFF5680C0 0x60000000 | |
| | | pullup | 0xFF5680C0 | [13, 14] | 3 | io -4 0xFF5680C0 0x60006000 | |
| | | pulldown | 0xFF5680C0 | [13, 14] | 1 | io -4 0xFF5680C0 0x60002000 | |
| | I/O Mode | input | 0xFF56000C | [1, 1] | 0 | io -4 0xFF56000C 0x00020000 | |
| | | | | | | | |
| | Output | | | | | | |
| | Input Ctrl | disable | 0xFF5680C0 | [3, 3] | 0 | io -4 0xFF5680C0 0x00080000 | |
| | | enable | 0xFF5680C0 | [3, 3] | 1 | io -4 0xFF5680C0 0x00080008 | |
| | Schmitt | | | | | | |
| | Drive Strength | level0 | 0xFF5680C0 | [10, 11] | 0 | io -4 0xFF5680C0 0x0C000000 | |
| | | level1 | 0xFF5680C0 | [10, 11] | 1 | io -4 0xFF5680C0 0x0C000400 | |
| | | level2 | 0xFF5680C0 | [10, 11] | 2 | io -4 0xFF5680C0 0x0C000800 | |
| | | level3 | 0xFF5680C0 | [10, 11] | 3 | io -4 0xFF5680C0 0x0C000C00 | |
| | ExtPort | read gpio data in input mode | 0xFF560070 | [17, 17] | | io -4 0xFF560070 | |