

# Product Backlog Sprint 1

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## Backlog

Frequency Identification, Due TBD, 20 hours, Milan Haruyama

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Audio Input, Due TBD, 2 hours, David Serfaty, Milan Haruyama, Maxwell Moolchan, Tahmina Tisha

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Data Storage, Due TBD, 20 hours, Tahmina Tisha, Maxwell Moolchan

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Scrum 1 Demo, Due Oct 10, 2 - 3 hours, All members

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Add Backlog for for sprint 2, Due Oct 9, 1 hour, All members

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Software Requirement Documentation, Due TBD 60 hours, Tabitha Hudson, Milan Haruyama, David Serfaty, Maxwell Moolchan, Tahmina Tisha

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# To Do

SRS V1, Due Sep 28, 10 hours, Tabitha Hudson, Milan Haruyama, David Serfaty, Maxwell Moolchan, Tahmina Tisha

- Introduction
  - Purpose
  - Document Conventions
  - Intended Audience
  - Product Scope
  - References
- Overall Description
  - Product Perspective
  - Product Functions
  - User Classes
  - Operating Environment
  - Design and Implementation Constraints
  - User Documentation
  - Assumptions and Dependencies
- External Interface Requirements
  - User Interfaces
  - Hardware Interfaces
  - Software Interfaces
  - Communications Interfaces
- System Features
  - Systems Features
- Other Nonfunctional Requirements
  - Performance Requirement
  - Safety Requirement
  - Security Requirement
  - Software Quality Attributes
  - Business Rules
- Other Requirements
  - Other Requirements
  - Appendix A
  - Appendix B
  - Appendix C

SDS V1, Due Sept 28, 10 hours, Tabitha Hudson, Milan Haruyama, David Serfaty, Maxwell Moolchan, Tahmina Tisha

- Introduction
  - Purpose and Scope
  - Project Executive Summary
  - System overview
  - Design Constraints

- Future Contingencies
- Document Organization
- Project References
- Glossary
- System Architecture
  - System Hardware Architecture
  - System Software Architecture
  - Internal Communications Architecture
- Human-Machine Interface
  - Inputs
  - Outputs
- Detailed Design
  - Hardware Detailed Design
  - Software Detailed Design
  - Internal Communication Detailed Design
- External Interfaces
  - Interface Architecture
  - Interface Detailed Design
- System Integrity Controls
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Programming Language Familiarization, Due TBD, 10 hours, Tabitha Hudson, Milan Haruyama, David Serfaty, Maxwell Moolchan, Tahmina Tisha

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Callsign Library, Due TBD, 20 hours, Milan Haruyama, Adam Gullub

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Model Understanding, Due TBD, 30 hours, All members

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In Progress

# Done

Vision Statement, Due Sep 19, 2 hours, All members

Start Backlog, Due Sep 19, 2 hours, All members

Connection between Kaldi and Nemo, Due TBD, 1 hour, Tabitha Hudson