

Difference. The speedup of 915 mV compared to 880 mV is $\frac{0.0072}{0.0043} \approx 1.74$ times faster.

4(c) How much slower will it run at 2 GHz at 915 mV?

Step 1) Find new dynamic power.

$$P = K \cdot (915 \text{ mV})^2 \cdot 2 \approx 25 \text{ W}$$

Step 2) solve for seconds in the energy calculation

$$0.128x = 25 \cdot S$$

$$\frac{0.128x}{25} = S$$

$$0.00512 = S = \text{time}_{\text{new}} \quad (2.4 \text{ GHz at } 915 \text{ mV version})$$

and from previous problem $\text{time}_{\text{old}} \approx 0.0043$

So the 2 GHz is $\approx 0.00512 - 0.0043 = 0.00082$ seconds slower.

4(d) For the new 2 GHz, 915 mV configuration, power consumption = 25 W. (see above calculation)
So it uses 5 W less than the 2.4 GHz version.

1A) A processor's clock runs at $2 \cdot 10^9$ (2,000,000,000) cycles per second. What is speed in GHz and MHz?

$$1 \text{ GHz} = 10^9 \text{ /note}$$

So the processor runs at 2 GHz.

$$1 \text{ GHz} = 1000 \text{ MHz} \text{ /note}$$

$$\text{Processor Speed in MHz} = 2 \cdot 1000 = 2000 \text{ MHz}$$

1B) For the same clock what is the period (aka cycle time) in P.S., ns., μs. ?

Note: 1 second = 10^{-9} ps

$$\frac{1 \text{ Second}}{2 \cdot 10^9} = \frac{10^{-9} \text{ ps}}{2 \cdot 10^9} = 500 \text{ ps}$$

$$\log_2(256) = x$$

$$2^x = 256$$

/note: 1 second = 10^{-9} ns

$$\frac{1 \text{ second}}{2 \cdot 10^9} = \frac{10^{-9} \text{ ns}}{2 \cdot 10^9} = 0.5 \text{ ns}$$

$$x = 8$$

/note: 1 second = 10^{-9} μs

$$\frac{1 \text{ second}}{2 \cdot 10^9} = \frac{10^{-9} \mu\text{s}}{2 \cdot 10^9} = 0.0005 \mu\text{s}$$

$$1C) 0xf = 15 \quad 1D) 2^7 = 128 \quad \log_2 256 = 8 :$$

1E) 1111 1110 to Decimal:

8-bit two's complement signed number = flip bits and add 1...

$$0000 \ 0001 \text{ (add 1)} \quad 0000 \ 0010 \text{ so } = \underline{-2}$$

$$8\text{-bit unsigned number: } 2^{1+2^2+2^3+2^4+2^5+2^6+2^7} = 2^{54}$$

1111 | 0x5

4 2 5

1f) Convert -13 to an 8-bit two's complement number.

Step 1: write 13 in binary 0000 1101

Step 2: invert bits: 1111 0010

Step 3: add 1: 1111 0011

-13 in an 8-bit two's complement binary: 1111 0011

16) Both &*a and *a are invalid syntax in C++ since a is an integer value and the first * attempts to dereference it, which is impossible. (run code)

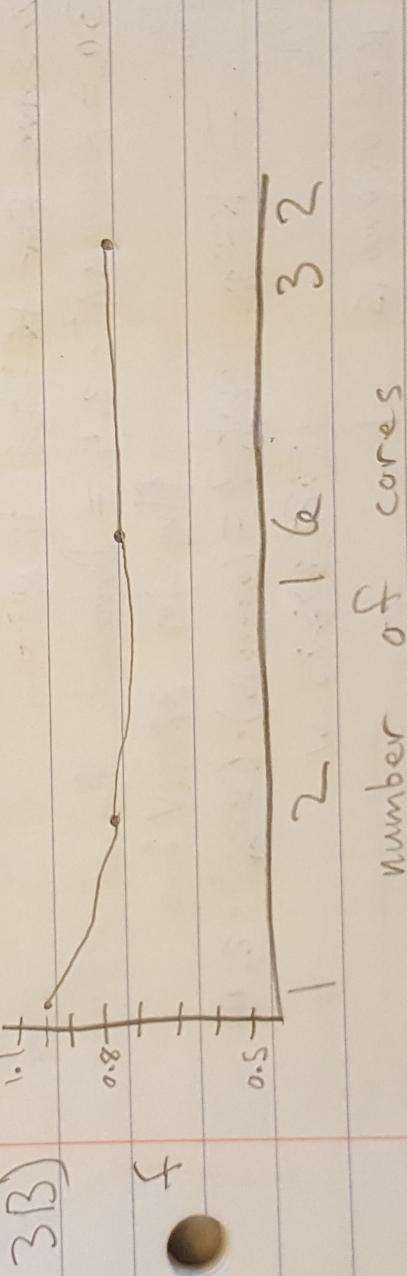
2A) For sale Computer: Newegg.com/Product/Product.aspx?Item=9SIA24G5PT6622

NewEgg		Server in Paper
Processor	Intel core i7-7500	Intel Xeon E5-2697 v2
Model/Manufacture	i7-7500	E5-2697 v2
# of Processors	1	1
Processor Clock Frequency	3.4 GHz, 4 cores	2.7 GHz, 24 cores
Size of Processor Cache	6 MB L3 cache	32 MB L3 cache
Amount and type (ddr4)	8 GB DDR4 2400	256 GB D
of ram		

2B) It has a mechanical hard disk.
1TB 7200 RPM

2C) Intel HD Graphics 630

3A) The Karp - Flatt metric is similar to Andahl's law, except that it solves for the fraction of the program we are speeding up since that is typically unknown.



$$\begin{aligned}2 \text{ cores} & \text{ is } \frac{8}{5} \text{ times faster compared to 1} \\16 \text{ cores} & \text{ is } \frac{80}{25} \text{ times faster compared to 1} \\32 \text{ cores} & \text{ is } \frac{800}{225} \text{ times faster compared to 1} \\F_{2 \text{ cores}} & = \left(1 - \left(\frac{1}{8/5}\right)\right) \left(1 - \frac{1}{2}\right)^{-1} = 0.75 \\F_{16 \text{ cores}} & = \left(1 - \left(\frac{1}{80/25}\right)\right) \left(1 - \frac{1}{16}\right)^{-1} \approx 0.73 \\F_{32 \text{ cores}} & = \left(1 - \left(\frac{1}{800/225}\right)\right) \left(1 - \frac{1}{32}\right)^{-1} \approx 0.74\end{aligned}$$

$$3C) p = \frac{16}{16}, s = \frac{80}{25} = \frac{16}{5} \quad \left| \frac{1}{(1-1) + \frac{16}{5}} = \frac{1}{\left(\frac{1}{1445}\right)} = 3.2\right.$$

76

3D) Processor cycles used in the single-core

version of Mr. Fable Bench Mark is

$\frac{800 \text{ ms}}{1 \text{ second}} \times 2.7 \cdot 10^9 \text{ cycles} = 2.7 \cdot 10^9 \text{ cycles}$, Also note $800 \text{ ms} = \frac{4}{5} \text{ second}$

So to get cycles: $(2.7 \cdot 10^9 \text{ cycles}) \cdot \frac{4}{5} = 2.16 \cdot 10^9 \text{ cycles}$

4A)

Power consumption at $915 \text{ mV} = 30 \text{ W}$ dynamic power

Power consumption at $880 \text{ mV} = 27.75 \text{ W}$

/ solve for K

$$30 = K \cdot (915 \text{ mV})^2 \cdot 2.4 = \text{Dynamic Power}$$

$$\left(\frac{30}{2.4}\right) = K \quad \text{Plug in } 880 \text{ mV:}$$

$$K = (0.000015) \cdot (880)^2 \cdot 2.14$$

$$K \approx 27.75 \text{ W}$$

$$K \approx 0.00015 \text{ W}$$

4B)

Energy = $W \cdot S$

Energy Savings: 12.8% (at the 915 mV)

Energy Savings: 21.94% (at the 880 mV)

$0.128X = 30 \text{ W} \cdot S$	$0.194X = 27.7 \text{ W} \cdot S$	<u>$X = \text{the energy}$</u>
$\frac{0.128X}{30} = S$	$\frac{0.194X}{27.7} = S$	<u>$\text{at the higher voltage}$</u>
$S \approx 0.0043$	$S \approx 0.0075$	<u>$\text{than } 915 \text{ mV, unknown?}$</u>

As you can see above, there is a performance