For each of the following addressing modes, show how the operand is derived and a short snippet of C code that would result in the use of each addressing mode.

	Oprnd	C snippet
Immediate	OprndSpec	a = 5;
Direct		
Indirect		
Stack-relative		
Stack-relative deferred		
Indexed		
Stack-indexed		
Stack-deferred indexed		

Consider the following assembly listing:

```
--------------------------------------------------------------------------------
      Object
Addr  code    Symbol  Mnemon  Operand     Comment
--------------------------------------------------------------------------------
0000  120009          BR      main
              x:      .EQUATE 0x0003
0003  0000    x0:     .WORD   0x0000
0005  0001    x1:     .WORD   0x0001
0007  0002    x2:     .WORD   0x0002
              ;
              ;******* main()
              y:      .EQUATE 0
              y0:     .EQUATE 0
              y1:     .EQUATE 2
              y2:     .EQUATE 4
              z:      .EQUATE 6
0009  580008 main:    SUBSP   8,i
000C  03              MOVSPA
000D  600000          ADDA    y,i
0010  E30006          STWA    z,s
0013  C10003          LDWA    x0,d
0016  E30000          STWA    y0,s
0019  C10005          LDWA    x1,d
001C  E30002          STWA    y1,s
001F  C10007          LDWA    x2,d
0022  E30004          STWA    y2,s
              ; insert instructions here
--------------------------------------------------------------------------------


Symbol table
---------------------------------------
Symbol    Value       Symbol    Value
---------------------------------------
main      0009        x         0003
x0        0003        x1        0005
x2        0007        y         0000
y0        0000        y1        0002
y2        0004        z         0006
---------------------------------------
```

What will be the values printed should the following instructions replace the comment above:

   i. DECO x,i   ii. DECO x,d   iii. LDWX 0,i
                                                             ASLX
                                                            DECO x,x

   iv. DECO y,i   v. DECO y,s   vi. LDWX 1,i
                                                          ASLX
                                                           DECO y,sx

  vii. DECO z,i   viii. DECO z,s   ix. DECO z,sf

   x. LDWX 2,i
      ASLX
      DECO z,sfx