Ayman Hajja. Fri Jul 14 2017 13:51:50 GMT-0400 (EDT)

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1) Translate the following C code to MIPS assembly code.

```
void main()
{
int s0;
 int s1;
 for(s0 = 0; s0 < 5; s0++)
   for (s1 = 0; s1 < 4; s1++)
   {
     arr[counter] = s0 + s1;
   }
}
```

No Answer

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2) Provide a set of pure MIPS instructions that may be used to implement the following pseudo-instruction:

```
not $t1, $t2 # bit-wise invert
```

No Answer

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3) Translate the following C code to MIPS assembly code.

```
void main()
{
int counter = 20;
int loc_arr[16];

 while(counter >= 5)
 {
   loc_arr[counter - 5] = 50;
   counter = counter - 1;
 }
}
```

No Answer

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4) Assume $t0 holds the value 0x00101000. What is the value of $t2 after the following instructions?

```
slt $t2, $0, $t0
bne $t2, $0, ELSE
j DONE
ELSE:
addi $t2, $t2, 2
```

DONE:
No Answer

****

5) Translate the following C code to MIPS assembly code.

```
int counter = 0;

void change_global(int value)
{
counter = counter + value;
}

void main()
{
change_global(5);
change_global(10);
}
```

De hello

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