

Acknowledgment

I, Aditya Kiran Pal 20UCS119, would like to express my special thanks of gratitude to my teacher (Sir Ardhendu Gupta) who gave me the golden opportunity to do this wonderful experiments of DCLD and his detailed explanation, which also helped me in doing a lot of Research and also came to know about the LOGIC CIRCUIT SIMULATOR PRO APP, which helps us understand the practical application of the components and also,so many new things.

Secondly i would also like to thank my parents and friends who helped me a lot in finishing this project within the limited time.

I am making this project not only for marks but to also increase my knowledge.

THANKS AGAIN TO ALL WHO HELPED ME.