

## **EXPT NO.1 :**

### **STUDY OF LOGIC GATES**

#### **Objective :**

To study about the logic gates and verify their truth tables.

#### **Equipments :**

Logic Circuit Simulator Pro.

#### **Theory :**

Circuit that takes the logical decision and the process are called logic gates. Each gate has one or more input and only one output. OR, AND and NOT are basic gates. NAND, NOR and X-OR are known as universal gates. Basic gates form these gates.

#### **AND GATE :**

The AND gate performs a logical multiplication commonly known as AND function. The output is high when both inputs are high. The output is low when any one of the inputs is low.

#### **OR GATE:**

The OR gate performs a logical addition commonly known as OR function. The output is high when any one of the inputs is high. The output is low level when both inputs are low.

#### **NOT GATE:**

The NOT gate is called inverter. The output is high when input is low. The output is low when input is high.

#### **NAND GATE:**

The NAND gate is a contraction of AND-NOT. The output is high when both inputs are low and any one of the inputs is high. The output is low when both inputs are high.

#### **NOR GATE:**

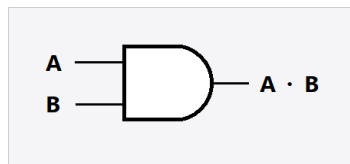
The NOR gate is a contraction of OR-NOT. The output is high when both inputs are low. The output is low when any one or both inputs are high.

#### **X-OR GATE:**

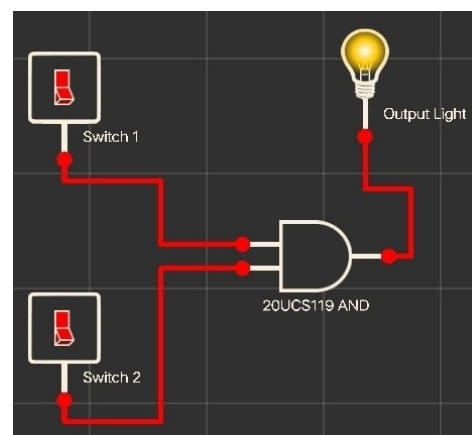
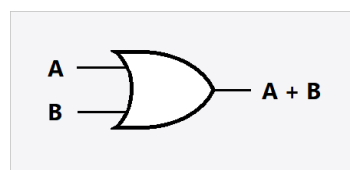
The output is high when any one of the inputs is high. The output is low when both inputs are high or low.

**Procedure :**

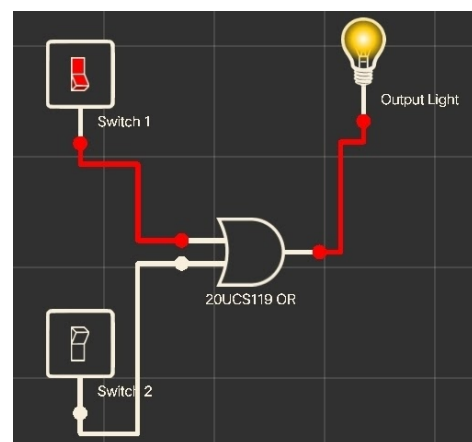
1. Firstly we have to install the LOGIC CIRCUIT SIMULATOR PRO APP and then after opening it, we have to make a new project.
2. Then we have to add the required elements to make the circuits i.e LOGIC gates, inputs, outputs etc.
3. Then connect the elements together with the help of a path to form a circuit and through the given gates we have to connect the input and output.
4. Lastly, we have to apply the TOUCH button to turn on/off the inputs i.e the switch by clicking on it, then we have to observe the output from the output section and verify the given truth tables.

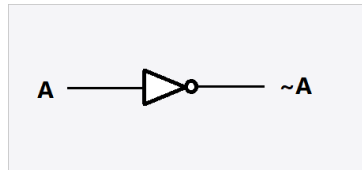
**AND GATE :**

Truth Table		
A	B	A.B
0	0	0
0	1	0
1	0	0
1	1	1

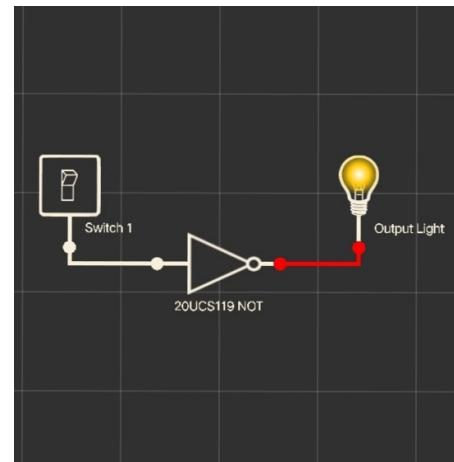
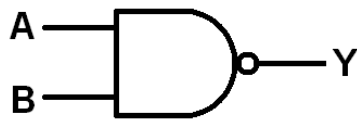
**OR GATE:**

Truth Table		
A	B	A+B
0	0	0
0	1	1
1	0	1
1	1	1

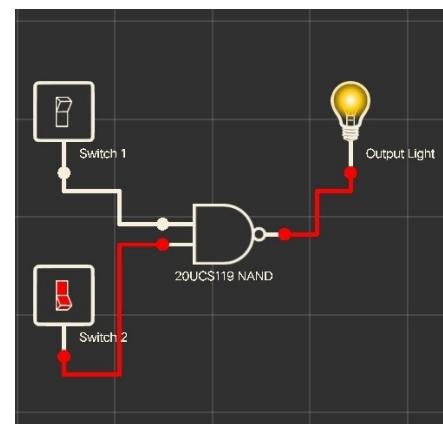
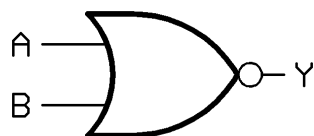


**NOT GATE:**

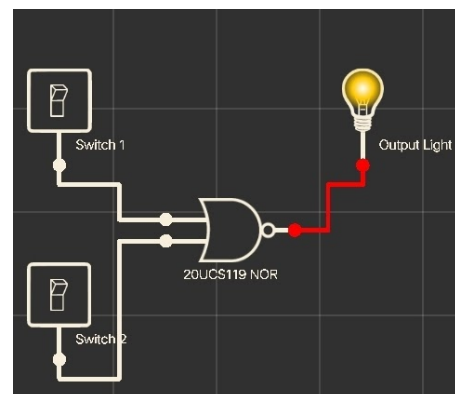
Truth Table	
A	A'
0	1
1	0

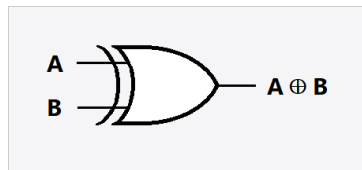
**NAND GATE:**

Truth Table		
A	B	$\overline{A \cdot B}$
0	0	1
0	1	1
1	0	1
1	1	0

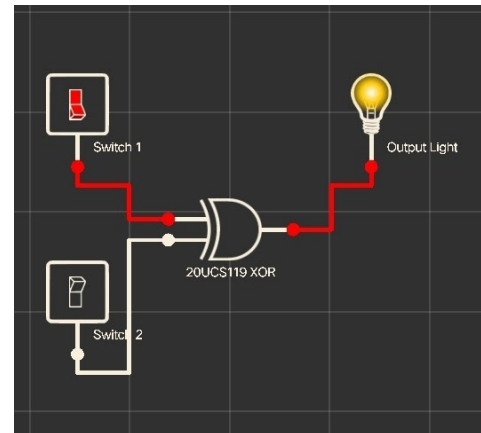
**NOR GATE:**

Truth Table		
A	B	$\overline{A + B}$
0	0	1
0	1	0
1	0	0
1	1	0



**X-OR GATE:**

Truth Table		
A	B	$\bar{A}B + A\bar{B}$
0	0	0
0	1	1
1	0	1
1	1	0

**Conclusion :**

The truth tables for various logic gates like AND, OR, NAND, NOT, X-OR, NOR are verified.