

**DIGITAL LOGIC AND CIRCUIT DESIGN LAB**  
**LABORATORY ASSIGNMENTS**  
**3<sup>rd</sup> SEMESTER COMPUTER SCIENCE & ENGINEERING**



**SUBMITTED BY**

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**SECTION : A**

**ENROLLMENT NO : 20UCS119**

**REGISTRATION NO : 2012709**

**SUBJECT : DCLD LAB**

**SEMESTER & YEAR : 3<sup>rd</sup> SEM , 2<sup>nd</sup> YEAR B. Tech.**

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