

Fig. 1
-Which Circuit Design Rule Can We Apply?

Fig. 2
-What is the type of the diode(D1)?

-If we want UREF of 3.3V, what should be our Zener Voltage should be(Ignore voltage drop through R5)?

Fig. 3
-Which Circuit Design Rule Does the Figure Violate?

Fig. 4
-Which Circuit Design Rule Does the Figure Violate?

Fig. 5
-Which Circuit Design Rule Does the Figure Violate?

-List Any Common Sub-Circuit

Fig. 6
- Which Circuit Design Rule Does the Figure Violate?

-What type of diode is D5?

-What type of switch is S1?

Fig. 7
-Which Circuit Design Rule Does the Figure Violate?

Fig. 8
-What is Missing in The Figure?

Fig. 9
-What Common Sub-Circuit is Used in The Figure?

Fig. 10
-What Type of Diode is Used?

Fig. 11 and 12
-<True or False> Figure 11 and 12 has full connection for all isolated nets

Fig. 13
-List Diode and Their Type(s)

-What Type of Fuse is F1?

-If We Want Short-Circuit Protection for 12V Output, Which Component Should We Use?

-Where Would We Place that Component at?

Fig. 14
-What Common Sub-Circuit is Used in The Figure?

-For Q1~Q4, Define Type of Transistor and Their Type
(That is, if BJT, is it NPN/PNP, or if it is FET, is it P-Channel/N-Channel?)

Extra Points
-On the Schematic, Label Anything That is Missing.
(Resistance, Capacitance, Missing Net Label, etc.)
Write them on the Schematic Sheet.

TITLE: Example_B

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