# STP Constraint Solver

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#### What is STP?

STP is a constraint solver (also referred to as a decision procedure or automated prover) aimed at solving constraints generated by program analysis tools, theorem provers, automated bug finders, biology, cryptography, intelligent fuzzers and model checkers.

STP has been used in many research projects at Stanford, Berkeley, MIT, CMU and other universities. It is also being used at many companies such as NVIDIA, some startup companies, and by certain government agencies.

The input to STP are formulas over the theory of bit-vectors and arrays (This theory captures most expressions from languages like C,C++,Java, Verilog etc.), and the output of STP is a single bit of information that indicates whether the formula is satisfiable or not. If the input is satisfiable, then it also generates a variable assignment to satisfy the

input formula.

#### Download

# STP source github

- STP benchmarks at sourceforge.net
  - Download using SVN (subversion) by typing the following command on your xterm:

svn co https://stp-fast-prover.svn.sourceforge.net /svnroot/stp-fast-prover/trunk/stp stp

WARNING: PLEASE DO `ulimit -s unlimited` BEFORE RUNNING STP. OTHERWISE, THE YACC PARSER WILL SEGFAULT ON BIG EXAMPLES

# Download STP wrappers and Ports

- <u>Python Wrapper</u> for STP written by <u>Roberto Paleari</u> from University of Milan, Italy. These bindings are released under GNU General Public License
- Ocaml Wrapper for STP written by <u>Dawn Song</u>'s group at CMU and Berkeley. These bindings are released under the Stanford License
- <u>Java Bindings</u> for STP written by <u>Adam Kiezun</u> from MIT. These bindings

are released under the MIT license

- STP has been ported to <u>OpenBSD</u> by Kevin Lo, and can be found at <a href="http://www.openbsd.org/cgi-bin/cvsweb/ports/devel/stp/">http://www.openbsd.org/cgi-bin/cvsweb/ports/devel/stp/</a> (STP can be installed via the ports tree)
- STP ships native with Fedora

#### STP Paper

 A Decision Procedure for Bit-vectors and Arrays by Vijay Ganesh and David L. Dill. In Proceedings of the International Conference in Computer Aided Verification (CAV 2007), Berlin, Germany, July 2007

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