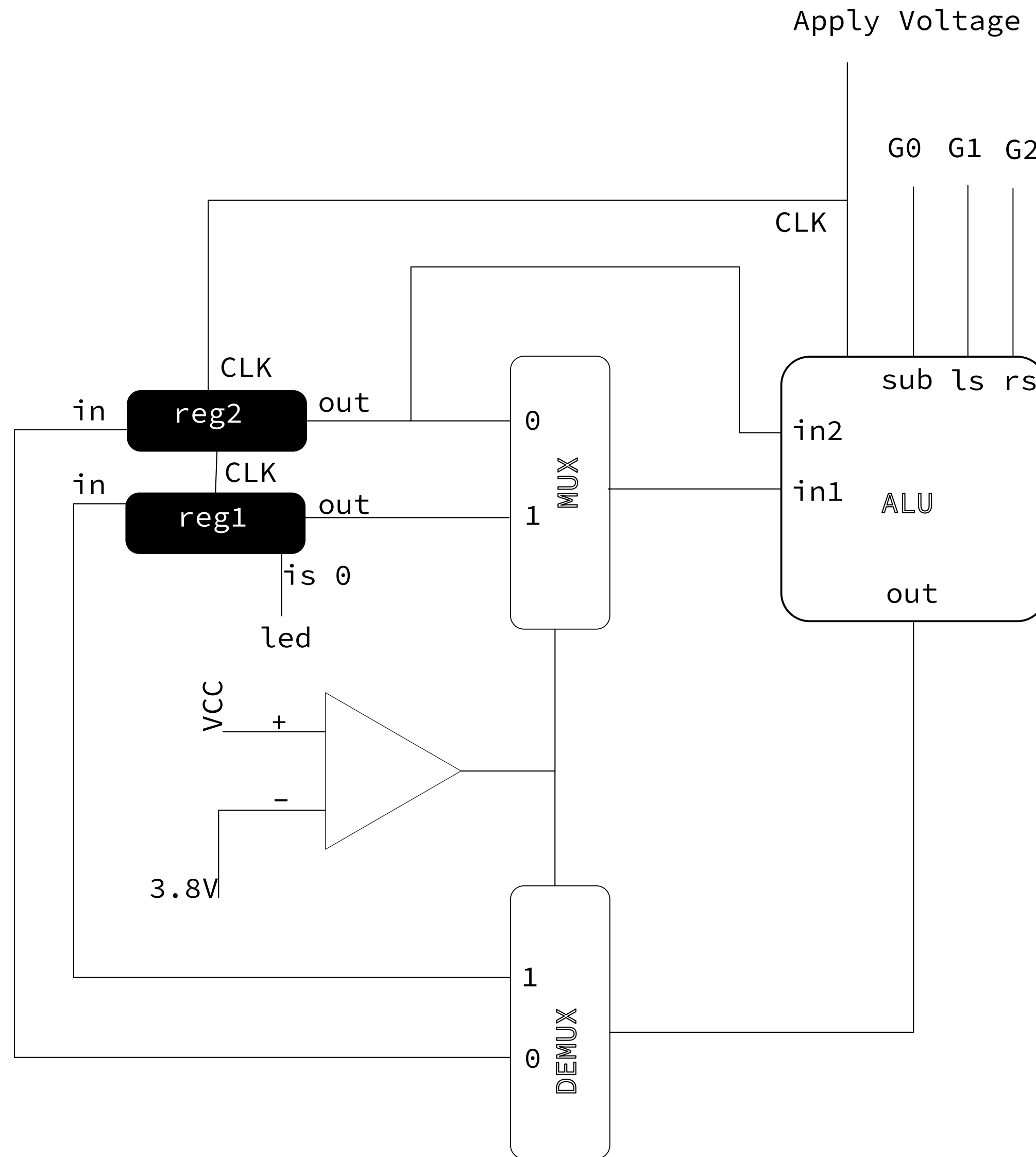


reg2 and reg1 are
128 bit shift
registers

All components
Grounded and
connected to source
voltage

is 0 pin is high
when value of that
particular register
is 0



ALU

sub on clk gives $in1 - in2$
ls on clk gives $in1 \ll 1$
rs on clk gives $in1 \gg 1$