

1. Description

1.1. Project

| Project Name | template |
|-----------------|--------------------|
| Board Name | custom |
| Generated with: | STM32CubeMX 6.12.0 |
| Date | 08/20/2024 |

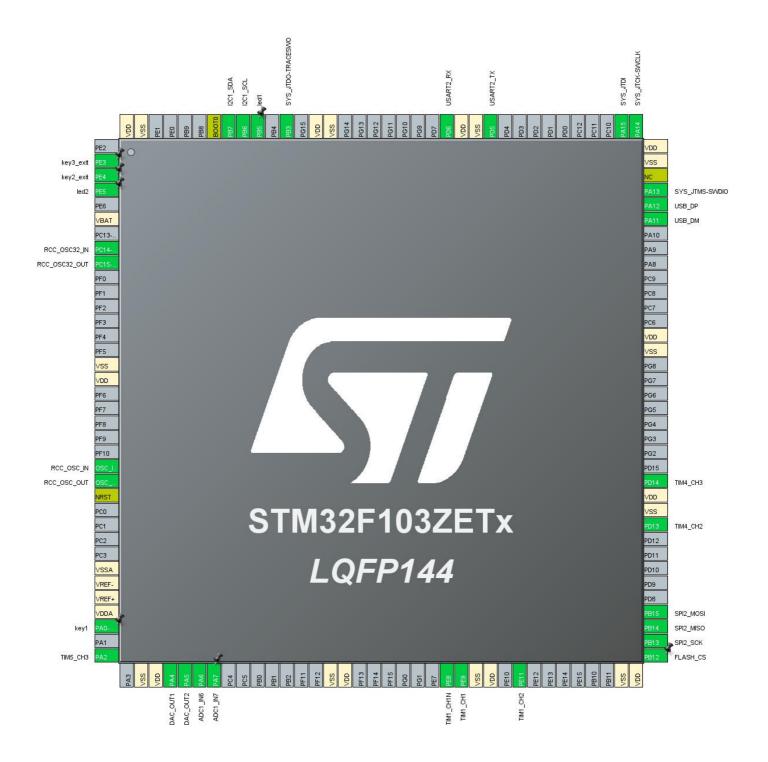
1.2. MCU

| MCU Series | STM32F1 |
|----------------|---------------|
| MCU Line | STM32F103 |
| MCU name | STM32F103ZETx |
| MCU Package | LQFP144 |
| MCU Pin number | 144 |

1.3. Core(s) information

| Core(s) | Arm Cortex-M3 |
|---------|---------------|

2. Pinout Configuration



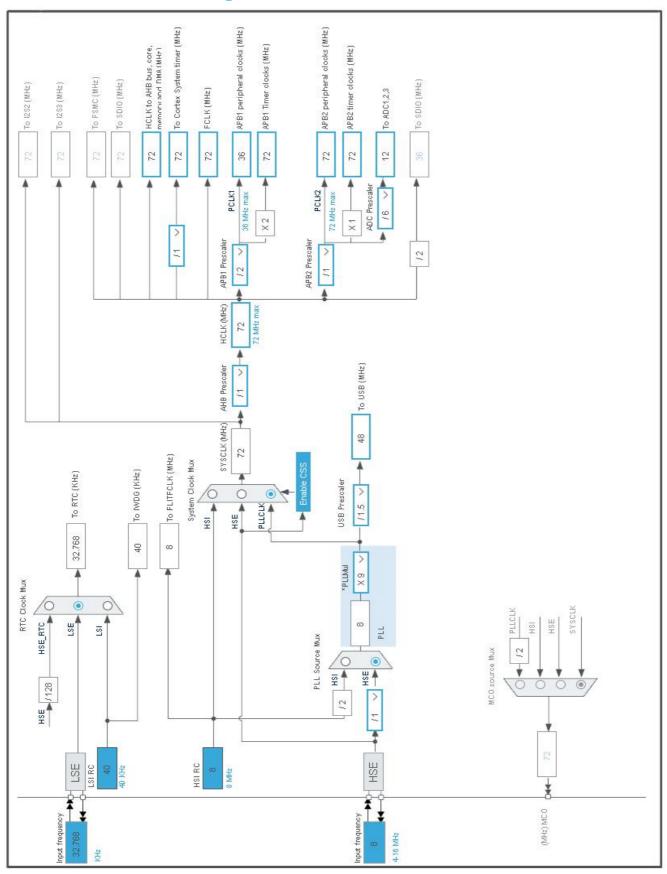
3. Pins Configuration

| Pin Number | Pin Name | Pin Type | Alternate | Label |
|------------|-----------------|----------|---------------|-----------|
| LQFP144 | (function after | | Function(s) | |
| | reset) | | | |
| 2 | PE3 | I/O | GPIO_EXTI3 | key3_exit |
| 3 | PE4 | I/O | GPIO_EXTI4 | key2_exit |
| 4 | PE5 * | I/O | GPIO_Output | led2 |
| 6 | VBAT | Power | | |
| 8 | PC14-OSC32_IN | I/O | RCC_OSC32_IN | |
| 9 | PC15-OSC32_OUT | I/O | RCC_OSC32_OUT | |
| 16 | VSS | Power | | |
| 17 | VDD | Power | | |
| 23 | OSC_IN | MonolO | RCC_OSC_IN | |
| 24 | OSC_OUT | MonolO | RCC_OSC_OUT | |
| 25 | NRST | Reset | | |
| 30 | VSSA | Power | | |
| 31 | VREF- | Power | | |
| 32 | VREF+ | Power | | |
| 33 | VDDA | Power | | |
| 34 | PA0-WKUP * | I/O | GPIO_Input | key1 |
| 36 | PA2 | I/O | TIM5_CH3 | |
| 38 | VSS | Power | | |
| 39 | VDD | Power | | |
| 40 | PA4 | I/O | DAC_OUT1 | |
| 41 | PA5 | I/O | DAC_OUT2 | |
| 42 | PA6 | I/O | ADC1_IN6 | |
| 43 | PA7 | I/O | ADC1_IN7 | |
| 51 | VSS | Power | | |
| 52 | VDD | Power | | |
| 59 | PE8 | I/O | TIM1_CH1N | |
| 60 | PE9 | I/O | TIM1_CH1 | |
| 61 | VSS | Power | | |
| 62 | VDD | Power | | |
| 64 | PE11 | I/O | TIM1_CH2 | |
| 71 | VSS | Power | | |
| 72 | VDD | Power | | |
| 73 | PB12 * | I/O | GPIO_Output | FLASH_CS |
| 74 | PB13 | I/O | SPI2_SCK | |
| 75 | PB14 | I/O | SPI2_MISO | |
| 76 | PB15 | I/O | SPI2_MOSI | |

| Pin Number LQFP144 | Pin Name (function after reset) | Pin Type | Alternate Function(s) | Label |
|-----------------------|---------------------------------------|----------|--------------------------|-------|
| 82 | PD13 | I/O | TIM4_CH2 | |
| 83 | VSS | Power | | |
| 84 | VDD | Power | | |
| 85 | PD14 | I/O | TIM4_CH3 | |
| 94 | VSS | Power | | |
| 95 | VDD | Power | | |
| 103 | PA11 | I/O | USB_DM | |
| 104 | PA12 | I/O | USB_DP | |
| 105 | PA13 | I/O | SYS_JTMS-SWDIO | |
| 106 | NC | NC | | |
| 107 | VSS | Power | | |
| 108 | VDD | Power | | |
| 109 | PA14 | I/O | SYS_JTCK-SWCLK | |
| 110 | PA15 | I/O | SYS_JTDI | |
| 119 | PD5 | I/O | USART2_TX | |
| 120 | VSS | Power | | |
| 121 | VDD | Power | | |
| 122 | PD6 | I/O | USART2_RX | |
| 130 | VSS | Power | | |
| 131 | VDD | Power | | |
| 133 | PB3 | I/O | SYS_JTDO-TRACESWO | |
| 135 | PB5 * | I/O | GPIO_Output | led1 |
| 136 | PB6 | I/O | I2C1_SCL | |
| 137 | PB7 | I/O | I2C1_SDA | |
| 138 | BOOT0 | Boot | | |
| 143 | VSS | Power | | |
| 144 | VDD | Power | | |

^{*} The pin is affected with an I/O function

4. Clock Tree Configuration



1. Power Consumption Calculator report

1.1. Microcontroller Selection

| Series | STM32F1 |
|-----------|---------------|
| Line | STM32F103 |
| MCU | STM32F103ZETx |
| Datasheet | DS5792_Rev12 |

1.2. Parameter Selection

| Temperature | 25 |
|-------------|-----|
| Vdd | 3.3 |

1.3. Battery Selection

| Battery | Li-SOCL2(A3400) |
|-------------------|-----------------|
| Capacity | 3400.0 mAh |
| Self Discharge | 0.08 %/month |
| Nominal Voltage | 3.6 V |
| Max Cont Current | 100.0 mA |
| Max Pulse Current | 200.0 mA |
| Cells in series | 1 |
| Cells in parallel | 1 |

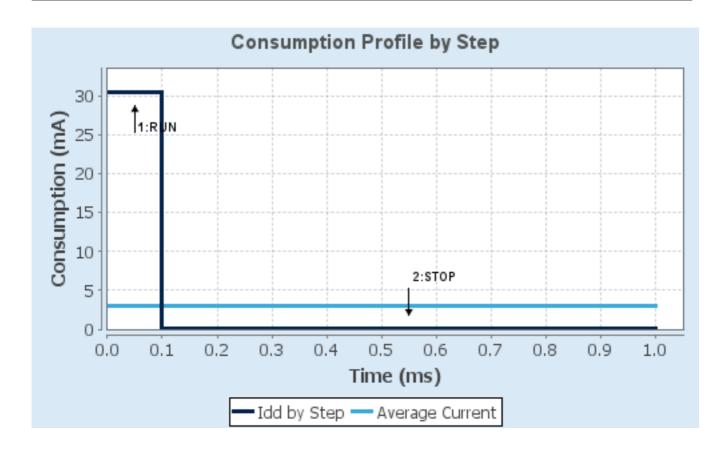
1.4. Sequence

| _ | _ | _ |
|------------------------|-------------|--------------|
| Step | Step1 | Step2 |
| Mode | RUN | STOP |
| Vdd | 3.3 | 3.3 |
| Voltage Source | Battery | Battery |
| Range | No Scale | No Scale |
| Fetch Type | FLASH | n/a |
| CPU Frequency | 72 MHz | 0 Hz |
| Clock Configuration | HSE PLL | Regulator LP |
| Clock Source Frequency | 8 MHz | 0 Hz |
| Peripherals | | |
| Additional Cons. | 0 mA | 0 mA |
| Average Current | 30.5 mA | 25 μΑ |
| Duration | 0.1 ms | 0.9 ms |
| DMIPS | 90.0 | 0.0 |
| Ta Max | 101.98 | 105 |
| Category | In DS Table | In DS Table |

1.5. Results

| Sequence Time | 1 ms | Average Current | 3.07 mA |
|---------------|-------------------|-----------------|------------|
| Battery Life | 1 month, 15 days, | Average DMIPS | 61.0 DMIPS |
| | 15 hours | | |

1.6. Chart



2. Software Project

2.1. Project Settings

| Name | Value |
|-----------------------------------|--|
| Project Name | template |
| Project Folder | C:\Users\79371\Desktop\WorkSpace\workspace_demo\STM32_Template |
| Toolchain / IDE | CMake |
| Firmware Package Name and Version | STM32Cube FW_F1 V1.8.5 |
| Application Structure | Advanced |
| Generate Under Root | No |
| Do not generate the main() | No |
| Minimum Heap Size | 0x200 |
| Minimum Stack Size | 0x400 |

2.2. Code Generation Settings

| Name | Value |
|---|---------------------------------------|
| STM32Cube MCU packages and embedded software | Copy only the necessary library files |
| Generate peripheral initialization as a pair of '.c/.h' files | Yes |
| Backup previously generated files when re-generating | No |
| Keep User Code when re-generating | Yes |
| Delete previously generated files when not re-generated | Yes |
| Set all free pins as analog (to optimize the power | No |
| consumption) | |
| Enable Full Assert | No |

2.3. Advanced Settings - Generated Function Calls

| Rank | Function Name | Peripheral Instance Name |
|------|---------------------|--------------------------|
| 1 | SystemClock_Config | RCC |
| 2 | MX_GPIO_Init | GPIO |
| 3 | MX_DMA_Init | DMA |
| 4 | MX_USART2_UART_Init | USART2 |
| 5 | MX_USB_DEVICE_Init | USB_DEVICE |
| 6 | MX_I2C1_Init | I2C1 |
| 7 | MX_CRC_Init | CRC |
| 8 | MX_SPI2_Init | SPI2 |
| 9 | MX_RTC_Init | RTC |
| 10 | MX_DAC_Init | DAC |
| 11 | MX_TIM2_Init | TIM2 |

| Rank | Function Name | Peripheral Instance Name |
|------|---------------|--------------------------|
| 12 | MX_TIM4_Init | TIM4 |
| 13 | MX_ADC1_Init | ADC1 |
| 14 | MX_TIM3_Init | TIM3 |
| 15 | MX_TIM1_Init | TIM1 |
| 16 | MX_IWDG_Init | IWDG |
| 17 | MX TIM5 Init | TIM5 |

3. Peripherals and Middlewares Configuration

3.1. ADC1 mode: IN6 mode: IN7

3.1.1. Parameter Settings:

ADCs_Common_Settings:

Mode Independent mode

ADC_Settings:

Data Alignment Right alignment

Scan Conversion Mode Enabled
Continuous Conversion Mode Disabled
Discontinuous Conversion Mode Disabled

ADC_Regular_ConversionMode:

Enable Regular Conversions Enable

Number Of Conversion 2 *

External Trigger Conversion Source Timer 3 Trigger Out event *

Rank 1

Channel Channel 6

Sampling Time 71.5 Cycles *

<u>Rank</u> 2 *

Channel 7 *
Sampling Time 71.5 Cycles *

ADC_Injected_ConversionMode:

Enable Injected Conversions Disable

WatchDog:

Enable Analog WatchDog Mode false

3.2. CRC

mode: Activated

3.3. DAC

mode: OUT1 Configuration mode: OUT2 Configuration 3.3.1. Parameter Settings:

DAC Out1 Settings:

Output Buffer Enable

Trigger Out event *

Wave generation mode Disabled

DAC Out2 Settings:

Output Buffer Enable

Trigger Out event *

Wave generation mode Disabled

3.4. I2C1 I2C: I2C

3.4.1. Parameter Settings:

Master Features:

I2C Speed Mode Standard Mode

I2C Clock Speed (Hz) 100000

Slave Features:

Clock No Stretch Mode Disabled

Primary Address Length selection 7-bit

Dual Address Acknowledged Disabled

Primary slave address 0

General Call address detection Disabled

3.5. IWDG

mode: Activated

3.5.1. Parameter Settings:

Clocking:

IWDG counter clock prescaler 64 *

IWDG down-counter reload value 1250 *

3.6. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator

Low Speed Clock (LSE): Crystal/Ceramic Resonator

3.6.1. Parameter Settings:

System Parameters:

VDD voltage (V) 3.3
Prefetch Buffer Enabled

Flash Latency(WS) 2 WS (3 CPU cycle)

RCC Parameters:

HSI Calibration Value 16
HSE Startup Timout Value (ms) 100
LSE Startup Timout Value (ms) 5000

3.7. RTC

mode: Activate Clock Source mode: Activate Calendar RTC OUT: No RTC Output 3.7.1. Parameter Settings:

Calendar Time:

Data Format BCD data format

Hours 22 *
Minutes 10 *
Seconds 0

General:

Auto Predivider Calculation Enabled

Asynchronous Predivider value Automatic Predivider Calculation Enabled

Output No output on the TAMPER pin

Calendar Date:

Week Day Friday *

Month August *

Date 16 *

Year 24 *

3.8. SPI2

Mode: Full-Duplex Master

3.8.1. Parameter Settings:

Basic Parameters:

Frame Format Motorola

Data Size 8 Bits

First Bit MSB First

Clock Parameters:

Prescaler (for Baud Rate) 2

Baud Rate 18.0 MBits/s *

Clock Polarity (CPOL) Low
Clock Phase (CPHA) 1 Edge

Advanced Parameters:

CRC Calculation Disabled
NSS Signal Type Software

3.9. SYS

Debug: JTAG (4 pins)

Timebase Source: SysTick

3.10. TIM1

Clock Source: Internal Clock

Channel1: PWM Generation CH1 CH1N

Channel2: PWM Generation CH2

3.10.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value) 71 *

Counter Mode Up

Counter Period (AutoReload Register - 16 bits value) 999 *

Internal Clock Division (CKD) No Division

Repetition Counter (RCR - 8 bits value) 0

auto-reload preload Enable *

Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit)

Disable (Trigger input effect not delayed)

Trigger Event Selection Reset (UG bit from TIMx_EGR)

Break And Dead Time management - BRK Configuration:

BRK State Enable *

BRK Polarity Low *

Break And Dead Time management - Output Configuration:

Automatic Output State

Off State Selection for Run Mode (OSSR)

Enable *

Off State Selection for Idle Mode (OSSI)

Enable *

Lock Configuration Off

Dead Time Oxac *

PWM Generation Channel 1 and 1N:

Mode PWM mode 1

Pulse (16 bits value) 0

Output compare preload Enable

Fast Mode Disable

CH Polarity High

CHN Polarity High

CH Idle State Reset

CHN Idle State Reset

PWM Generation Channel 2:

Mode PWM mode 1

Pulse (16 bits value) 0

Output compare preload Enable

Fast Mode Disable

CH Polarity High

CH Idle State Reset

3.11. TIM2

Clock Source : Internal Clock

3.11.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value) 0
Counter Mode Up
Counter Period (AutoReload Register - 16 bits value) 65535
Internal Clock Division (CKD) No Division
auto-reload preload Disable

Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit)

Disable (Trigger input effect not delayed)

Trigger Event Selection Update Event *

3.12. TIM3

Clock Source : Internal Clock

3.12.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value)

Counter Mode

Counter Period (AutoReload Register - 16 bits value)

Internal Clock Division (CKD)

auto-reload preload

September 1

By 1

By 2

By 4

By 3

By 4

By 5

By 5

By 6

By 7

By

Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit)

Disable (Trigger input effect not delayed)

Trigger Event Selection Update Event *

3.13. TIM4

Clock Source: Internal Clock

Channel2: Input Capture direct mode Channel3: Input Capture direct mode

3.13.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value)

Counter Mode

Counter Period (AutoReload Register - 16 bits value)

Internal Clock Division (CKD)

Auto-reload preload

Tal.*

Up

65535

No Division

auto-reload preload

Enable *

Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit) Disable (Trigger input effect not delayed)

Trigger Event Selection Update Event *

Input Capture Channel 2:

Polarity Selection Rising Edge
IC Selection Direct
Prescaler Division Ratio No division
Input Filter (4 bits value) 8 *

Input Capture Channel 3:

Polarity Selection Falling Edge *

IC Selection Direct
Prescaler Division Ratio No division
Input Filter (4 bits value) 8 *

3.14. TIM5

mode: Clock Source

Channel3: PWM Generation CH3

3.14.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value) 71 *

Counter Mode Up

Counter Period (AutoReload Register - 16 bits value) 65535

Internal Clock Division (CKD) No Division auto-reload preload Disable

Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit) Disable (Trigger input effect not delayed)

Trigger Event Selection Reset (UG bit from TIMx_EGR)

PWM Generation Channel 3:

Mode PWM mode 1

Pulse (16 bits value) 0

Output compare preload Enable

Fast Mode Disable

CH Polarity High

3.15. USART2

Mode: Asynchronous

3.15.1. Parameter Settings:

Basic Parameters:

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

Advanced Parameters:

Data Direction Receive and Transmit

Over Sampling 16 Samples

3.16. USB

mode: Device (FS)

3.16.1. Parameter Settings:

Basic Parameters:

Speed Full Speed 12MBit/s

Power Parameters:

Low PowerDisabledLink Power ManagementDisabledBattery ChargingDisabled

3.17. USB DEVICE

Class For FS IP: Communication Device Class (Virtual Port Com)

3.17.1. Parameter Settings:

Basic Parameters:

USBD_MAX_NUM_INTERFACES (Maximum number of supported interfaces)

1
USBD_MAX_NUM_CONFIGURATION (Maximum number of supported configuration)

1
USBD_MAX_STR_DESC_SIZ (Maximum size for the string descriptors)

512
USBD_SELF_POWERED (Enabled self power)

Enabled

USBD_DEBUG_LEVEL (USBD Debug Level) 0: No debug message

Class Parameters:

USB CDC Rx Buffer Size 1024
USB CDC Tx Buffer Size 1024

3.17.2. Device Descriptor:

Device Descriptor:

VID (Vendor IDentifier) 1155

LANGID_STRING (Language Identifier) English(United States)

MANUFACTURER_STRING (Manufacturer Identifier) STMicroelectronics

Device Descriptor FS:

PID (Product IDentifier) 22336

PRODUCT_STRING (Product Identifier) STM32 Virtual ComPort

| CONFIGURATION_STRING (Configuration Identifier) |
|---|
| INTERFACE_STRING (Interface Identifier) |

CDC Config CDC Interface

* User modified value

4. System Configuration

4.1. GPIO configuration

| IP | Pin | Signal | GPIO mode | GPIO pull/up pull down | Max Speed | User Label |
|--------|------------------------|-----------------------|----------------------------------|-----------------------------|--------------|------------|
| ADC1 | PA6 | ADC1_IN6 | Analog mode | n/a | n/a | |
| | PA7 | ADC1_IN7 | Analog mode | n/a | n/a | |
| DAC | PA4 | DAC_OUT1 | Analog mode | n/a | n/a | |
| | PA5 | DAC_OUT2 | Analog mode | n/a | n/a | |
| I2C1 | PB6 | I2C1_SCL | Alternate Function Open Drain | n/a | High * | |
| | PB7 | I2C1_SDA | Alternate Function Open Drain | n/a | High * | |
| RCC | PC14- OSC32_IN | RCC_OSC32_IN | n/a | n/a | n/a | |
| | PC15- OSC32_OU T | RCC_OSC32_O UT | n/a | n/a | n/a | |
| | OSC_IN | RCC_OSC_IN | n/a | n/a | n/a | |
| | OSC_OUT | RCC_OSC_OUT | n/a | n/a | n/a | |
| SPI2 | PB13 | SPI2_SCK | Alternate Function Push Pull | n/a | High * | |
| | PB14 | SPI2_MISO | Input mode | No pull-up and no pull-down | n/a | |
| | PB15 | SPI2_MOSI | Alternate Function Push Pull | n/a | High * | |
| SYS | PA13 | SYS_JTMS- SWDIO | n/a | n/a | n/a | |
| | PA14 | SYS_JTCK- SWCLK | n/a | n/a | n/a | |
| | PA15 | SYS_JTDI | n/a | n/a | n/a | |
| | PB3 | SYS_JTDO- TRACESWO | n/a | n/a | n/a | |
| TIM1 | PE8 | TIM1_CH1N | Alternate Function Push Pull | n/a | Low | |
| | PE9 | TIM1_CH1 | Alternate Function Push Pull | n/a | Low | |
| | PE11 | TIM1_CH2 | Alternate Function Push Pull | n/a | Low | |
| TIM4 | PD13 | TIM4_CH2 | Input mode | No pull-up and no pull-down | n/a | |
| | PD14 | TIM4_CH3 | Input mode | No pull-up and no pull-down | n/a | |
| TIM5 | PA2 | TIM5_CH3 | Alternate Function Push Pull | n/a | Low | |
| USART2 | PD5 | USART2_TX | Alternate Function Push Pull | n/a | High * | |
| | PD6 | USART2_RX | Input mode | No pull-up and no pull-down | n/a | |
| USB | PA11 | USB_DM | n/a | n/a | n/a | |
| | PA12 | USB_DP | n/a | n/a | n/a | |
| GPIO | PE3 | GPIO_EXTI3 | External Event Mode | No pull-up and no pull-down | n/a | key3_exit |
| | | | with Rising/Falling | | | |

| IP | Pin | Signal | GPIO mode | GPIO pull/up pull | Max | User Label |
|----|----------|-------------|--|-----------------------------|-------|------------|
| | | | | down | Speed | |
| | | | edge trigger detection | | | |
| | PE4 | GPIO_EXTI4 | External Interrupt Mode with Rising edge trigger detection | No pull-up and no pull-down | n/a | key2_exit |
| | PE5 | GPIO_Output | Output Push Pull | No pull-up and no pull-down | Low | led2 |
| | PA0-WKUP | GPIO_Input | Input mode | Pull-down * | n/a | key1 |
| | PB12 | GPIO_Output | Output Push Pull | No pull-up and no pull-down | Low | FLASH_CS |
| | PB5 | GPIO_Output | Output Push Pull | Pull-up * | Low | led1 |

4.2. DMA configuration

| DMA request | Stream | Direction | Priority |
|-------------|---------------|----------------------|----------|
| DAC_CH1 | DMA2_Channel3 | Memory To Peripheral | Low |
| DAC_CH2 | DMA2_Channel4 | Memory To Peripheral | Low |
| ADC1 | DMA1_Channel1 | Peripheral To Memory | Low |
| TIM4_CH2 | DMA1_Channel4 | Peripheral To Memory | Low |
| TIM4_CH3 | DMA1_Channel5 | Peripheral To Memory | Low |
| TIM1_CH2 | DMA1_Channel3 | Memory To Peripheral | Low |

DAC_CH1: DMA2_Channel3 DMA request Settings:

Mode: Circular *
Peripheral Increment: Disable
Memory Increment: Enable *
Peripheral Data Width: Half Word
Memory Data Width: Half Word

DAC_CH2: DMA2_Channel4 DMA request Settings:

Mode: Circular *

Peripheral Increment: Disable

Memory Increment: Enable *

Peripheral Data Width: Half Word

Memory Data Width: Half Word

ADC1: DMA1_Channel1 DMA request Settings:

Mode: Circular *
Peripheral Increment: Disable
Memory Increment: Enable *
Peripheral Data Width: Half Word
Memory Data Width: Half Word

TIM4_CH2: DMA1_Channel4 DMA request Settings:

Mode: Circular *
Peripheral Increment: Disable

Memory Increment: Enable *

Peripheral Data Width: Half Word

Memory Data Width: Half Word

TIM4_CH3: DMA1_Channel5 DMA request Settings:

Mode: Normal
Peripheral Increment: Disable
Memory Increment: Enable *
Peripheral Data Width: Half Word
Memory Data Width: Half Word

TIM1_CH2: DMA1_Channel3 DMA request Settings:

Mode: Normal
Peripheral Increment: Disable
Memory Increment: Enable *
Peripheral Data Width: Half Word
Memory Data Width: Half Word

4.3. NVIC configuration

4.3.1. NVIC

| Interrupt Table | Enable | Preenmption Priority | SubPriority | |
|--|--------|----------------------|-------------|--|
| Non maskable interrupt | true | 0 | 0 | |
| Hard fault interrupt | true | 0 | 0 | |
| Memory management fault | true | 0 | 0 | |
| Prefetch fault, memory access fault | true | 0 | 0 | |
| Undefined instruction or illegal state | true | 0 | 0 | |
| System service call via SWI instruction | true | 0 | 0 | |
| Debug monitor | true | 0 | 0 | |
| Pendable request for system service | true | 1 | 0 | |
| System tick timer | true | 0 | 0 | |
| RTC global interrupt | true | 0 | 0 | |
| RCC global interrupt | true | 0 | 0 | |
| EXTI line4 interrupt | true | 4 | 0 | |
| DMA1 channel1 global interrupt | true | 0 | 0 | |
| DMA1 channel3 global interrupt | true | 0 | 0 | |
| DMA1 channel4 global interrupt | true | 0 | 0 | |
| DMA1 channel5 global interrupt | true | 0 | 0 | |
| USB low priority or CAN RX0 interrupts | true | 0 | 0 | |
| TIM1 update interrupt | true | 0 | 0 | |
| TIM4 global interrupt | true | 0 | 0 | |
| USART2 global interrupt | true | 0 | 0 | |
| TIM5 global interrupt | true | 0 | 0 | |
| DMA2 channel3 global interrupt | true | 0 | 0 | |
| DMA2 channel4 and channel5 global interrupts | true | 0 | 0 | |
| PVD interrupt through EXTI line 16 | unused | | | |
| Flash global interrupt | unused | | | |
| ADC1 and ADC2 global interrupts | | unused | | |
| USB high priority or CAN TX interrupts | | unused | | |
| TIM1 break interrupt | unused | | | |
| TIM1 trigger and commutation interrupts | unused | | | |
| TIM1 capture compare interrupt | unused | | | |
| TIM2 global interrupt | unused | | | |
| TIM3 global interrupt | unused | | | |
| I2C1 event interrupt | unused | | | |
| I2C1 error interrupt | unused | | | |
| SPI2 global interrupt | | unused | | |

4.3.2. NVIC Code generation

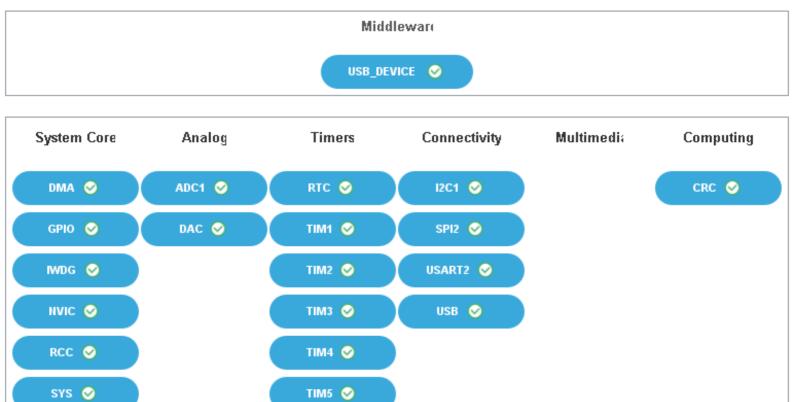
| Enabled interrupt Table | Select for init sequence ordering | Generate IRQ handler | Call HAL handler |
|--|-----------------------------------|-------------------------|------------------|
| Non maskable interrupt | false | true | false |
| Hard fault interrupt | false | true | false |
| Memory management fault | false | true | false |
| Prefetch fault, memory access fault | false | true | false |
| Undefined instruction or illegal state | false | true | false |
| System service call via SWI instruction | false | true | false |
| Debug monitor | false | true | false |
| Pendable request for system service | false | true | false |
| System tick timer | false | true | true |
| RTC global interrupt | false | true | true |
| RCC global interrupt | false | true | false |
| EXTI line4 interrupt | false | true | true |
| DMA1 channel1 global interrupt | false | true | true |
| DMA1 channel3 global interrupt | false | true | true |
| DMA1 channel4 global interrupt | false | true | true |
| DMA1 channel5 global interrupt | false | true | true |
| USB low priority or CAN RX0 interrupts | false | true | true |
| TIM1 update interrupt | false | true | true |
| TIM4 global interrupt | false | true | true |
| USART2 global interrupt | false | true | true |
| TIM5 global interrupt | false | true | true |
| DMA2 channel3 global interrupt | false | true | true |
| DMA2 channel4 and channel5 global interrupts | false | true | true |

^{*} User modified value

5. System Views

5.1. Category view

5.1.1. Current



6. Docs & Resources

Type Link

BSDL files https://www.st.com/resource/en/bsdl_model/stm32f1_bsdl.zip

IBIS models https://www.st.com/resource/en/ibis_model/stm32ibis.zip

System View https://www.st.com/resource/en/svd/stm32f1_svd.zip

Description

Presentations https://www.st.com/resource/en/product_presentation/stm32-

stm8_embedded_software_solutions.pdf

Presentations https://www.st.com/resource/en/product_presentation/stm32_eval-

tools_portfolio.pdf

Presentations https://www.st.com/resource/en/product_presentation/stm32_stm8_functi

onal-safety-packages.pdf

Presentations https://www.st.com/resource/en/product_presentation/stm32-

stm8_software_development_tools.pdf

Presentations https://www.st.com/resource/en/product_presentation/microcontrollers-

stm32-family-overview.pdf

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