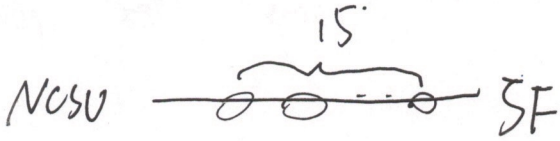
T2.




~~$t_{switching} = 50\mu s$~~

$v = 2\times10^8 m/s$

$d_{prop} = \frac{5\times10^6 m}{2\times10^8 m/s} = 25 ms.$

$d_{trans} = 50\mu s.$

$d = 50\mu s + 15\times50\mu s + 25ms = 800\mu s + 25ms$

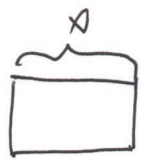
$= \underline{25.8 ms}.$

~~$L = n\times$~~

$p \times n = L.$

---

T3.




x bits long

p bits. b bps.

k-hop.

见二张背面

R < setup time: s

prop - - : d.

# ~~$n = \frac{x}{p}$~~ # $n = \frac{x}{p}$.

~~pkt-switching: $T = \frac{p}{b}\times\frac{x}{p} + (n-1)\times\frac{p}{b} + n\times d$~~

Circuit-switching: $T = s + \frac{p}{b}\times n + k\cdot d.$

~~$= s + \frac{p}{b}\times\frac{x}{p} + \frac{x}{p}\times d$~~ ①

~~$= s + \frac{x}{b} + \frac{x}{p}\cdot d.$~~