

REV.		ZONE		REVISIONS	DATE	DRAWN
A				ADDED ECN6583	12/02	JRF
B				ADDED ECN6770,6919,6937,7005,7139,7220,7303,7375A,7487	01/04	JRF
C				ADDED ECN6755,7916,8096,8200,8280,8408	1/05	VG
D				ADDED ECN8732,8773,8808,88138,8990,9016,9192	3/06	SMN
E				ADDED ECN9715	11/06	JRF
F				ADDED ECN9812,9881,10086A,10215,10238	7/07	JRF
G				ADDED ECN10332,10474,10480	10/07	JRF
H				ADDED ECN10565	10/07	VG
J				ADDED ECN10588,11421,11942	9/09	JRF/VG
K				ADDED ECN12060,12220,12589,12626	4/10	JRF
L				ADDED ECN12705,12766,12860	12/10	VG

NOTES: 1 SHEET LIST: SHEET 1 & 2 ARE THE SCHEMATIC. SHEET 3 IS THE ASSEMBLY. SHEET 4 IS THE DEFINITION OF LAYERS FOR THE SCHEMATIC (SHT 1 & 2). SHEET 7 IS THE EMI SHIELDING SHEET.

2 PLACE SYSTEM SERIAL LABEL (EIA01252) ON REAR OF CHASSIS AS SHOWN ON ASSY DRAWING

COMPLETE AC POWER INPUT LABEL (EIA01200) AS FOLLOWS: -A 115VAC 10A 50/60HZ SEE DETAIL 'EIA01200' PLACE LABEL ON REAR OF CHASSIS AS SHOWN ON ASSEMBLY DRAWING.

3 VOLTAGE SELECTOR SWITCH CONFIGURATION.

A6 SWITCHES		A10 SWITCHES	
A6-S1	A6-S2	A6-S3	A6-S4
A10-S5	A10-S6		
UP	UP	UP	UP
UP	UP	UP	UP

4 NPAQ DRIVE INTERFACE PCB (A1)

AXIS	1	2	3	4	5	6
AMPLIFIER	DP32020E	DP32020E	DP32020E	DP32020E	DP32020E	BLANK
BUS 1 (80B) CONFIGURATION CHART:						
T1	C1	C2	BW1			
(EAX01006)	16000F (ECC01002)	16000F (ECC01002)	BW1 DUT			
BUS 2 (80B) CONFIGURATION CHART:						
T2	C3	C4	BW2			
(EAX01006)	16000F (ECC01002)	16000F (ECC01002)	BW2 DUT			

BUS SEPARATION: NO SPLIT (STD). IN BW10, BW11, BW12, BW20, BW21, BW22, IN BW30, BW31, BW32, BW40, BW41, BW42, IN BW50, BW51, BW52

AUTOMATIC BRAKE 'DE-ENERGIZED' JP1 A) +3.3V MONITORED SET JP1 1-2 IN (STD) BRAKE OPTION: INSERT M1 ODC5A (ECS000394).

5 NPAQ DSP CONTROL BD. (A2)

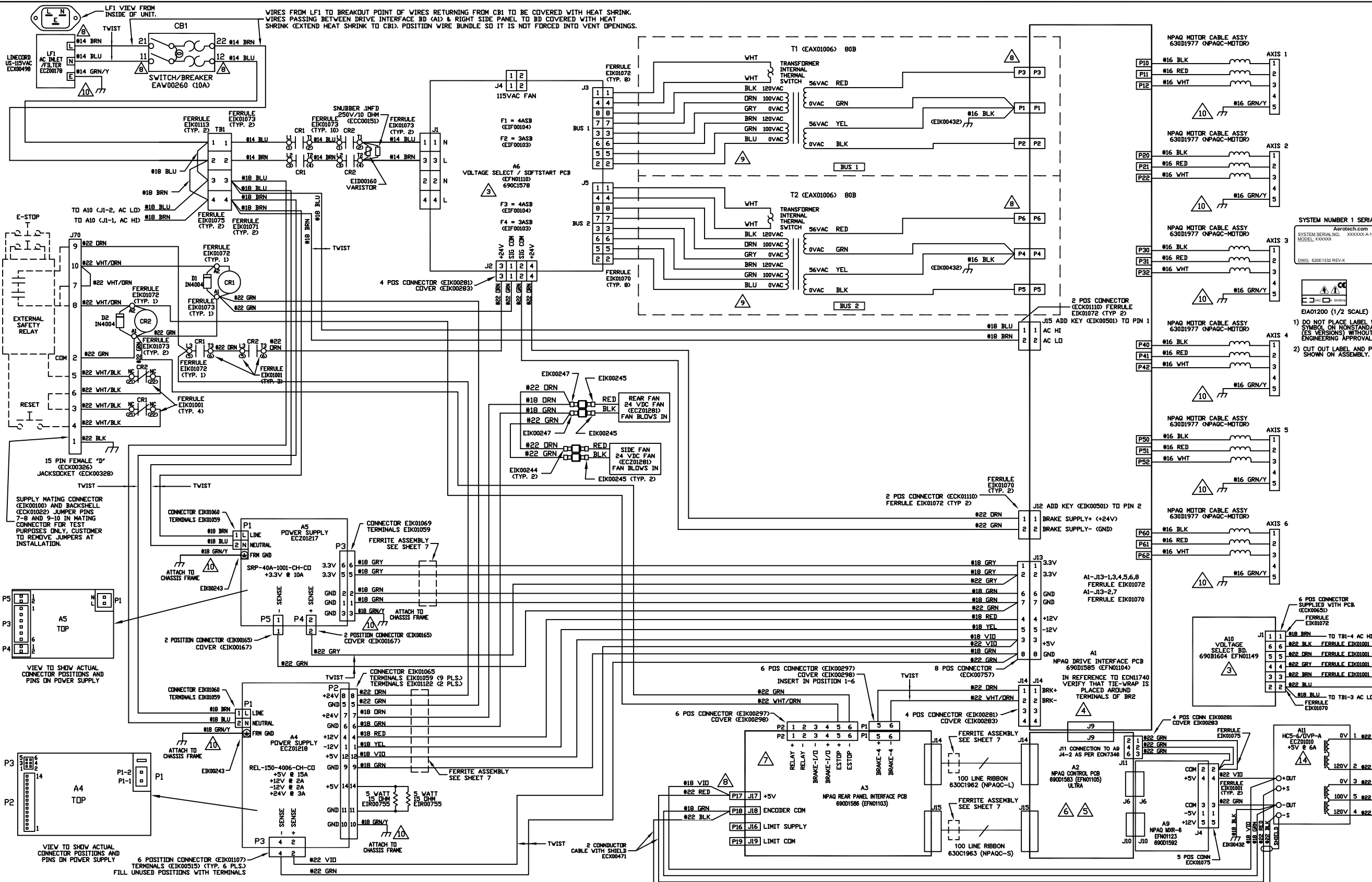
VERIFY JP1 IS SET TO 1-2M M28 - HCLP-2601 (ECS00119) IS TO BE INSTALLED AT ALL TIMES.

RS232 / RS422 SELECT (SEE ALSO NOTE 7)	JP2 1-2M
RESET TIMEOUT (JP3 MUST BE SET1-2)	JP3 1-2M
RESET TIMEOUT ENABLED	JP3 1-2M

AUX FIREWIRE BUS POWER SOURCE (NORMALLY NOT USED) +12 VOLT POWER SOURCE (A4) (DEFAULT) JP4 2-3M, JP5 2-3M

S1 SWITCH SETTINGS	
SI-1 (RESERVED)	SI-1 CLOSED (CND)
SI-2 (RESERVED)	SI-2 CLOSED (CND)
SI-3 (RESERVED)	SI-3 CLOSED (CND)
SI-4 (RESERVED)	SI-4 CLOSED (CND)
SI-5 (RESERVED)	SI-5 CLOSED (CND)
SI-6 (RESERVED)	SI-6 CLOSED (CND)
SI-7 (RESERVED)	SI-7 CLOSED (CND)
SI-8 (RESERVED)	SI-8 CLOSED (CND)
SI-9 (RESERVED)	SI-9 CLOSED (CND)

SI-1 SWITCH IS NEAREST TO THE 'SI' MARKING ON PCB.



21 PLACE WARNING LABEL (EIA01135) ON TOP COVER OF CHASSIS. SEE ASSEMBLY DWG (SHT 3) FOR LOCATION.

22 USE 16-14 AWG FASTON CONNECTORS EIK00432. LABEL WITH PERMANENT MARKER.

23 SPLICE AND INSULATE WIRING TO/FROM TRANSFORMERS AS REQUIRED TO OBTAIN PROPER LENGTH.

24 ALL GRN/YEL GRD WIRES TO BE CONNECTED TO SEPARATE CHASSIS MOUNTING POINTS. USE EIK00432. ADD PROTECTIVE GROUND LABEL (EIA00435) AT CONNECTION POINT IN CHASSIS.

25 ALL CRIMPED CONNECTIONS MUST ALSO BE SECURED WITH WIRE TIES OR HEATSHRINK. SOLDER CONNECTIONS SHOULD BE "J" HOOKED.

26 VOID

27 VOID

28 POWER SUPPLY (A1) CUT OUT ANY WIRE JUMPERS BETWEEN TRANSFORMER PINS 1 THRU 5 (PINS MUST BE ISOLATED). SOLDER WIRES FROM A10 TO TRANSFORMER PINS 1 THRU 5.

6 NPAQ REAR PANEL INTERFACE PCB (A3)

LIMIT POWER SUPPLY					
AXIS	1	2	3	4	5
JP1	JP2	JP3	JP4	JP5	JP6
1-2M	1-2M	1-2M	1-2M	1-2M	1-2M

RS232 / RS422 SETTINGS (SEE ALSO NOTE 5)

JP8	JP9	JP10	JP11	JP12	JP13
1-2M	1-2M	1-2M	1-2M	1-2M	1-2M

5 NPAQ DSP CONTROL BD. (A2)

FIREWIRE DEVICE # (S2 0-4, DN CLOSED) = 0, OFF (OPEN) = 1					
NPAG # (BASE +5)	S2-1	S2-2	S2-3	S2-4	S2-5
1 THRU 6	DN	DN	DN	DN	DN

S2-5 THRU S2-9 SWITCH SETTINGS (DN = CLOSED, OFF = OPEN)

S2-5 (UNASSIGNED)	S2-5 CLOSED
S2-6 (UNASSIGNED)	S2-6 CLOSED
S2-7 (UNASSIGNED)	S2-7 CLOSED
S2-8 (UNASSIGNED)	S2-8 CLOSED
S2-9 (UNASSIGNED)	S2-9 CLOSED

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TOLERANCES ON FRACTIONS DECIMALS ANGLES 0 TO 6 ±1/32 .XX ±.01 ±0-30' OVER 6 ±1/16 .XXX ±.005

RMS FINISH EXCEPT AS NOTED

DRAWN	JRF	11/02
CHECKED	GLK	11/02
APPD.	ARW	11/02

SCALE NTS FILE NAME 620D1332L1.dwg SHEET 1 OF 7

620D1332

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