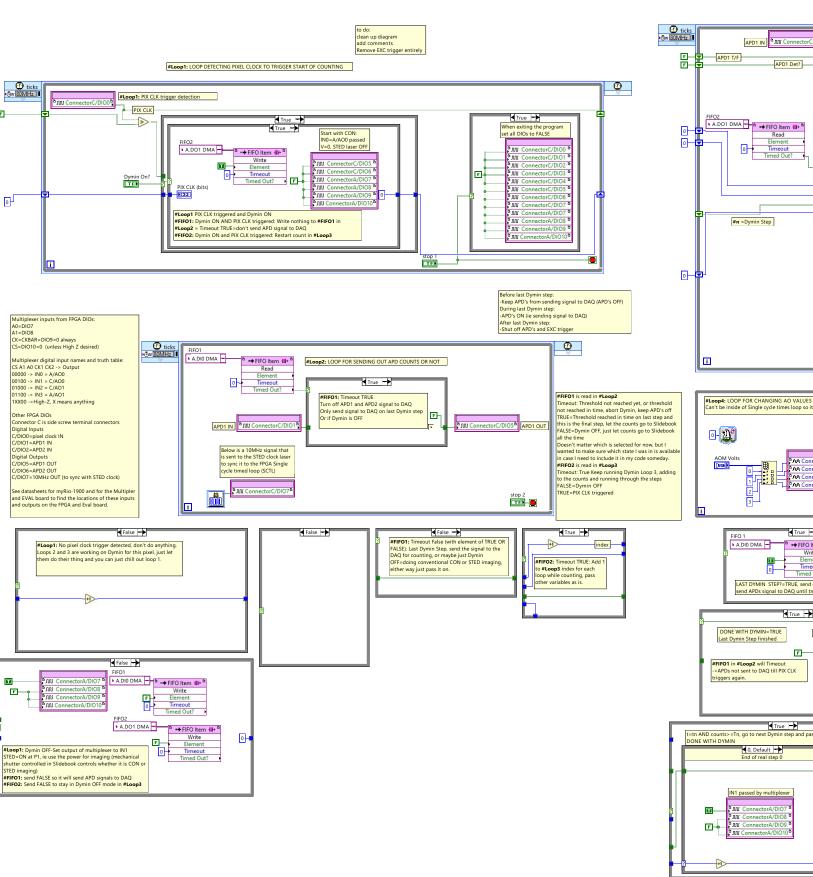
1 📵

Dymin_FPGA_Main.vi Q:\GitHub\DyminSTED4all\LabviewCode\Dymin_FPGA_Main.vi Last modified on 7/24/2023 at 12:39 PM

Printed on 12/8/2023 at 12:11 PM



Dymin_FPGA_Main.vi

#FIFO1 in #Loop2 to

Below: Pass INO to multiplexer

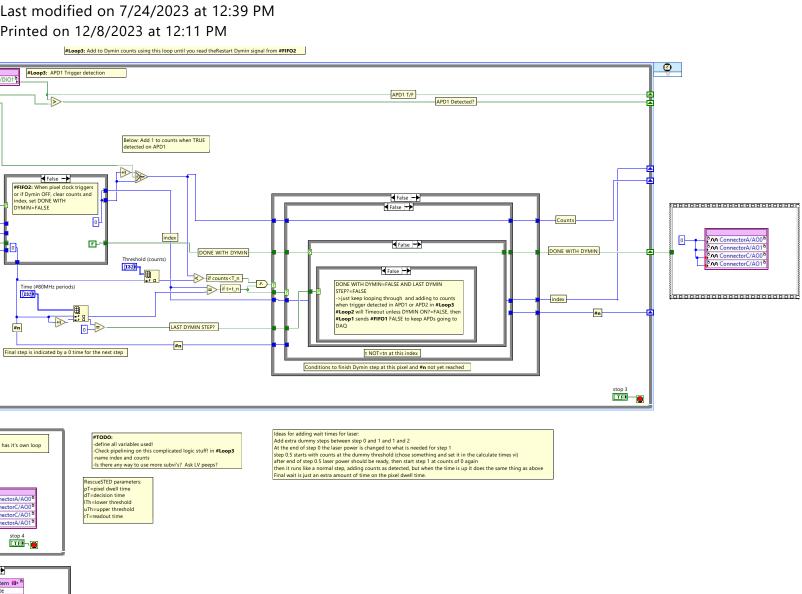
s LAST DYMIN STEP? to

110

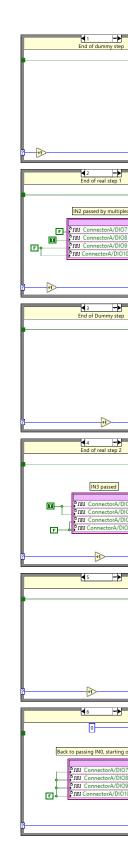
DINI ConnectorA/DIO7 DINI ConnectorA/DIO8 DINI ConnectorA/DIO9 DINI ConnectorA/DIO10 DI

0

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