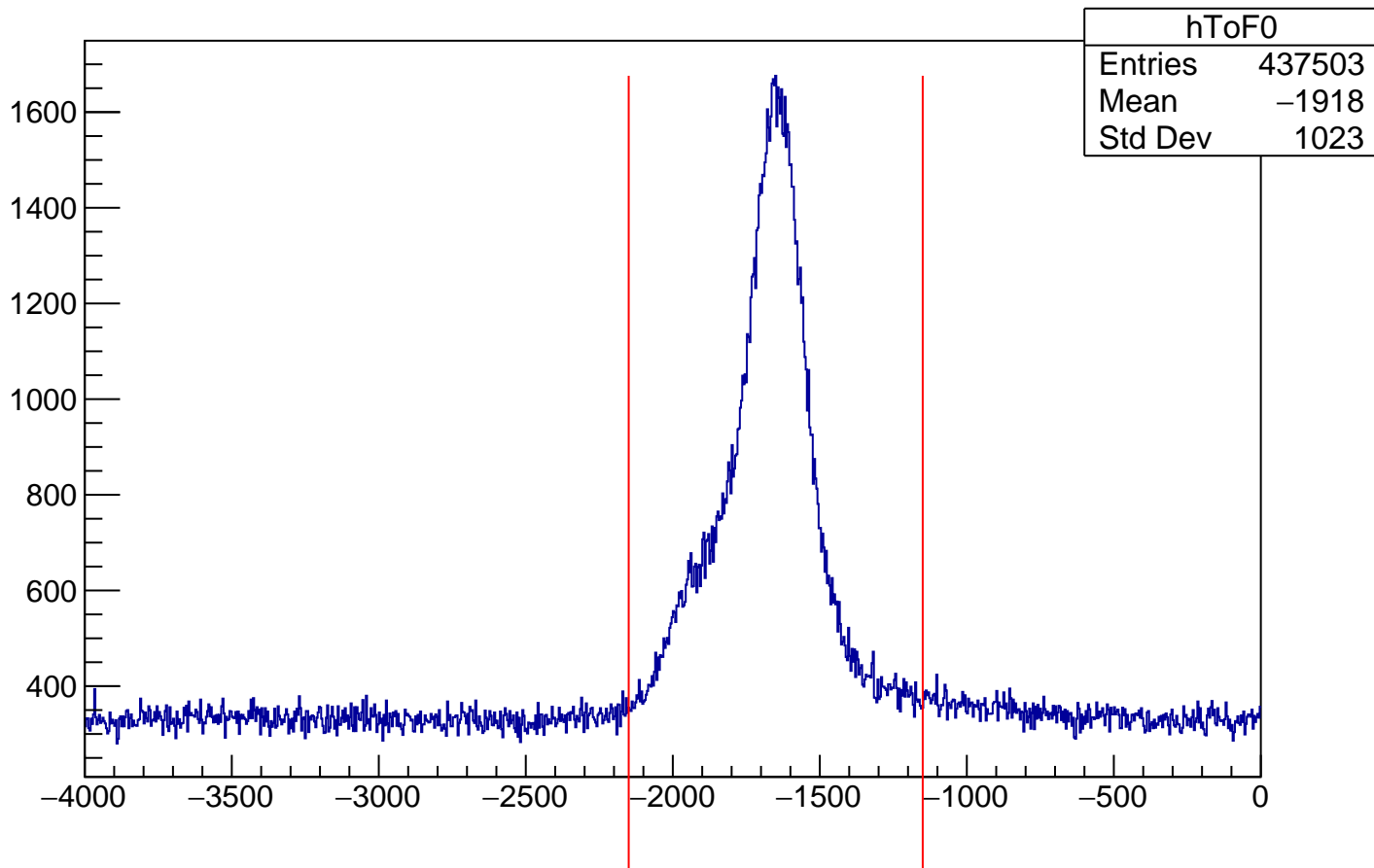
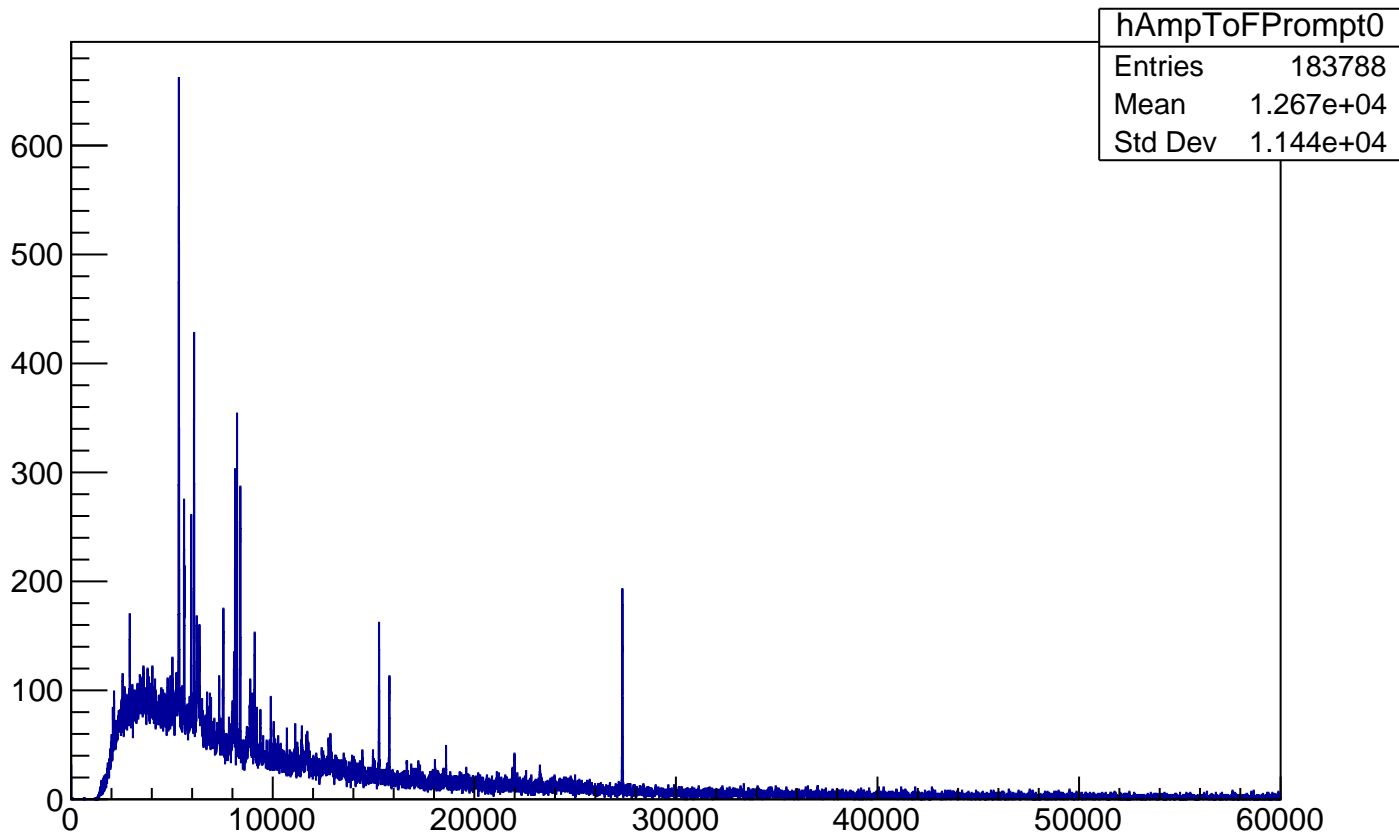




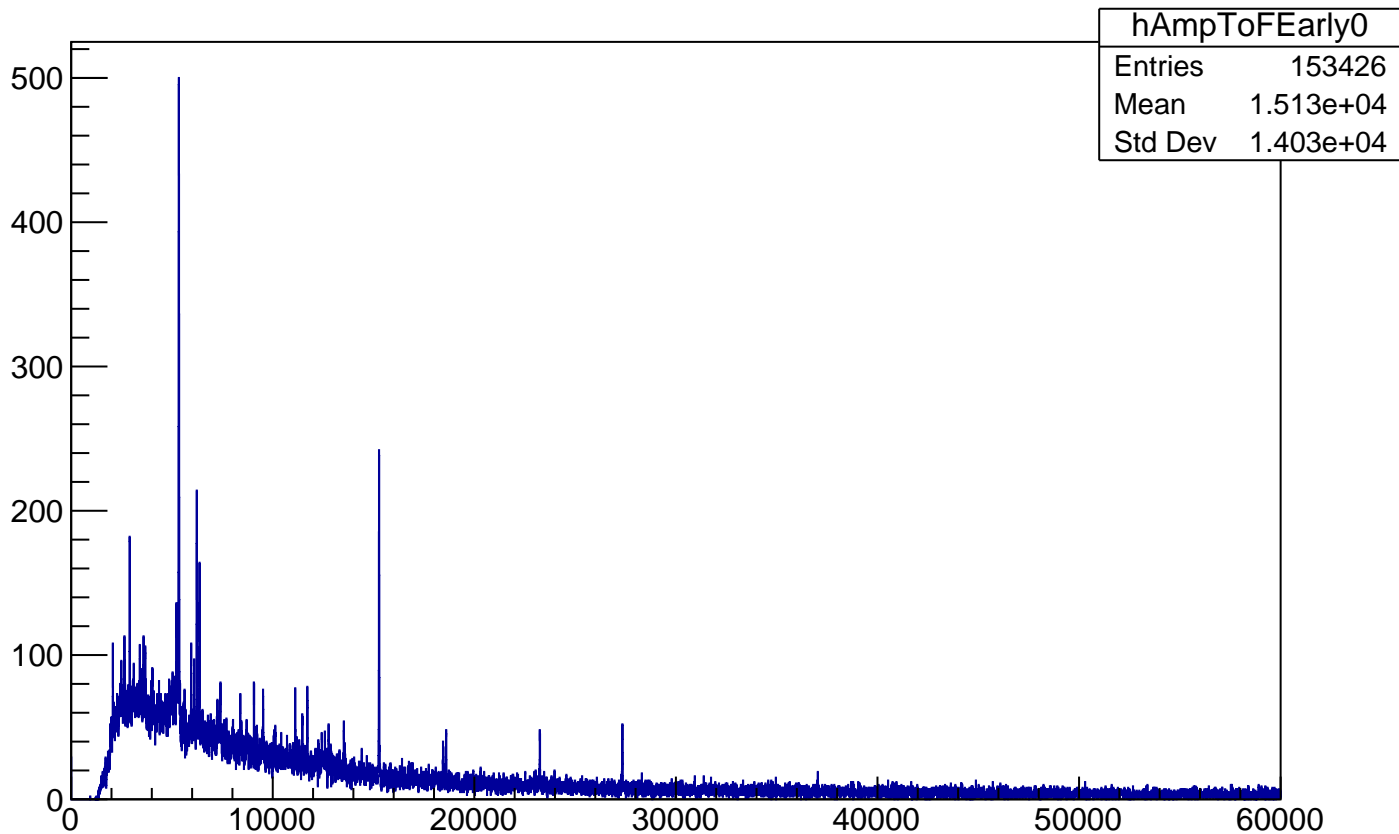
# hToF0



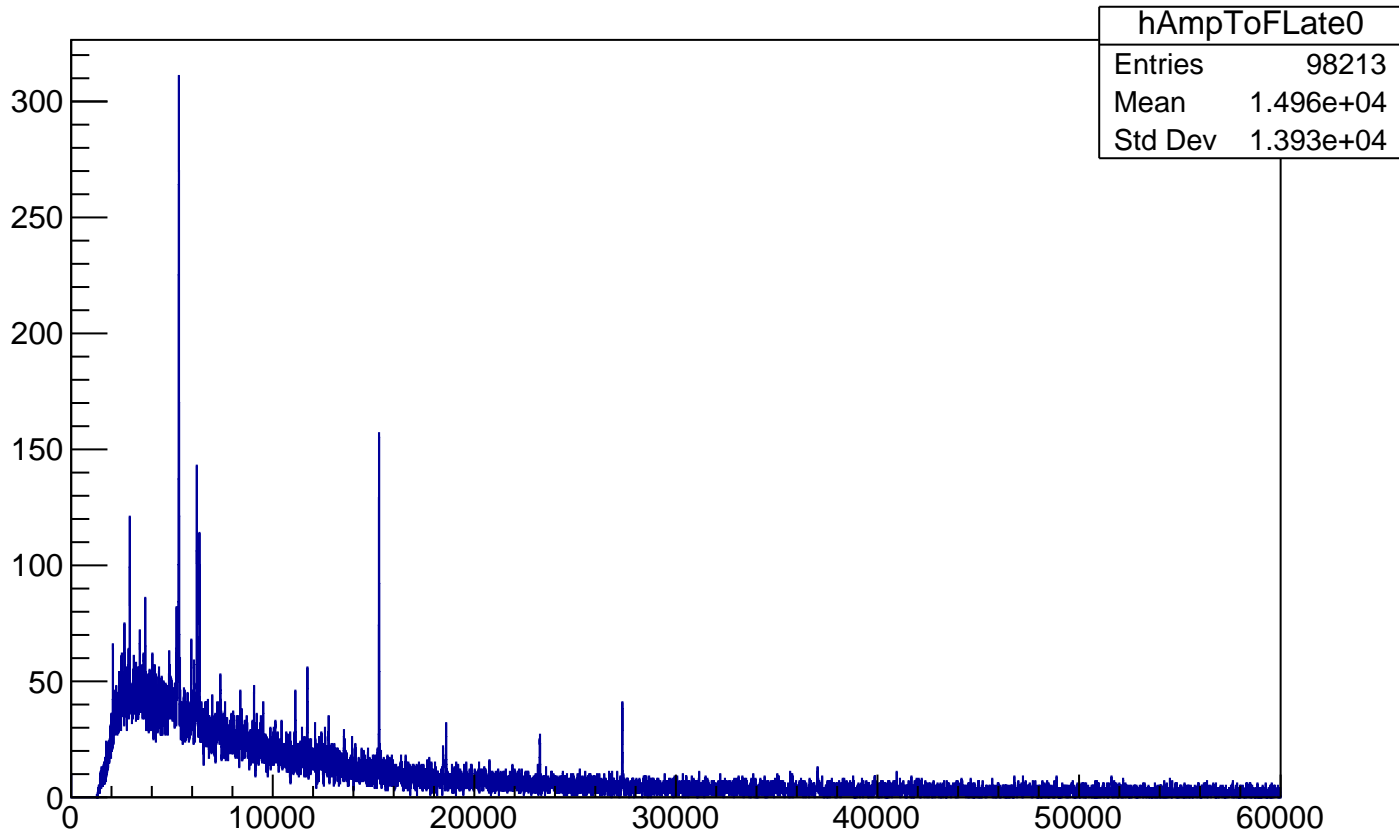
# hAmpToFPrompt0



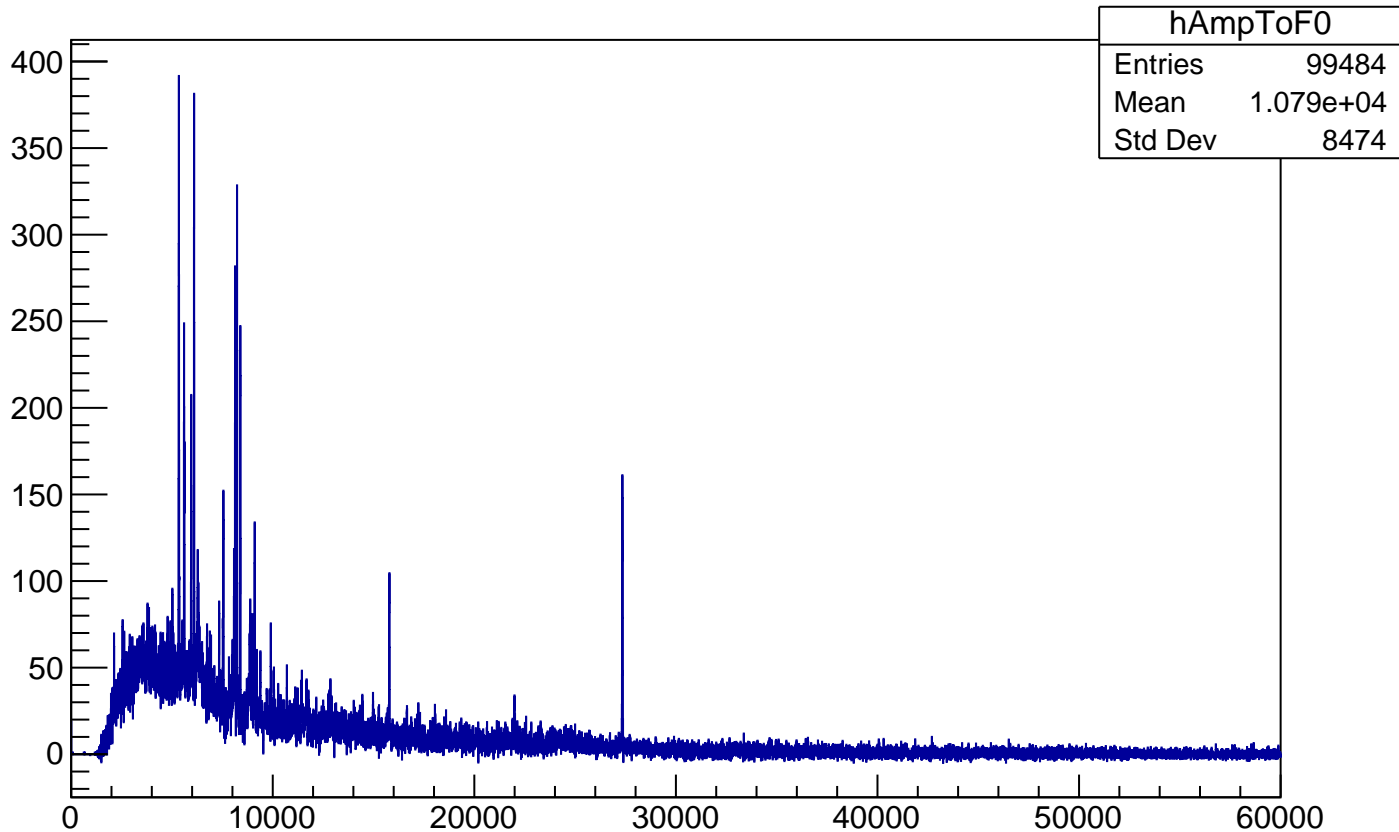
# hAmpToFEarly0



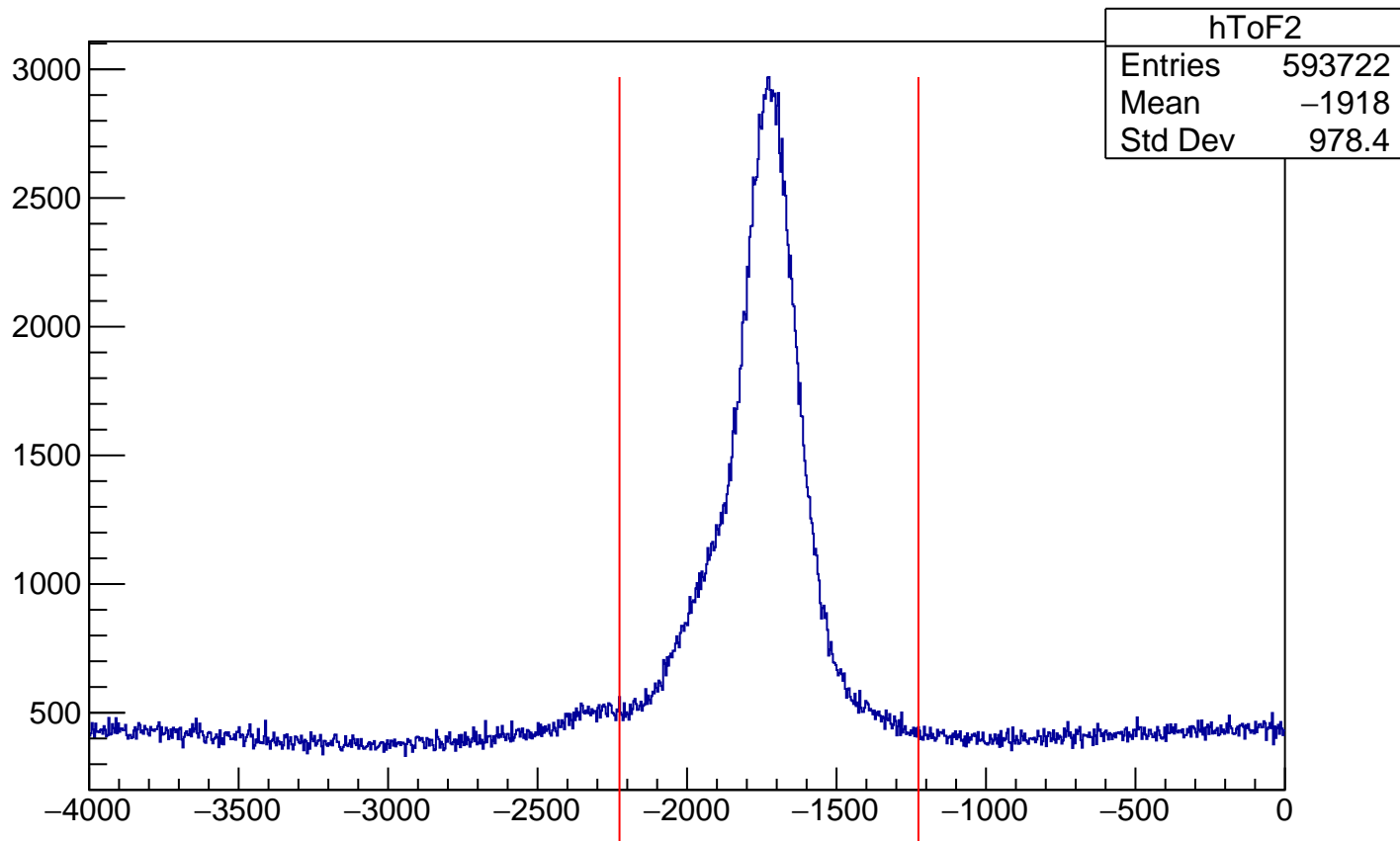
# hAmpToFLate0



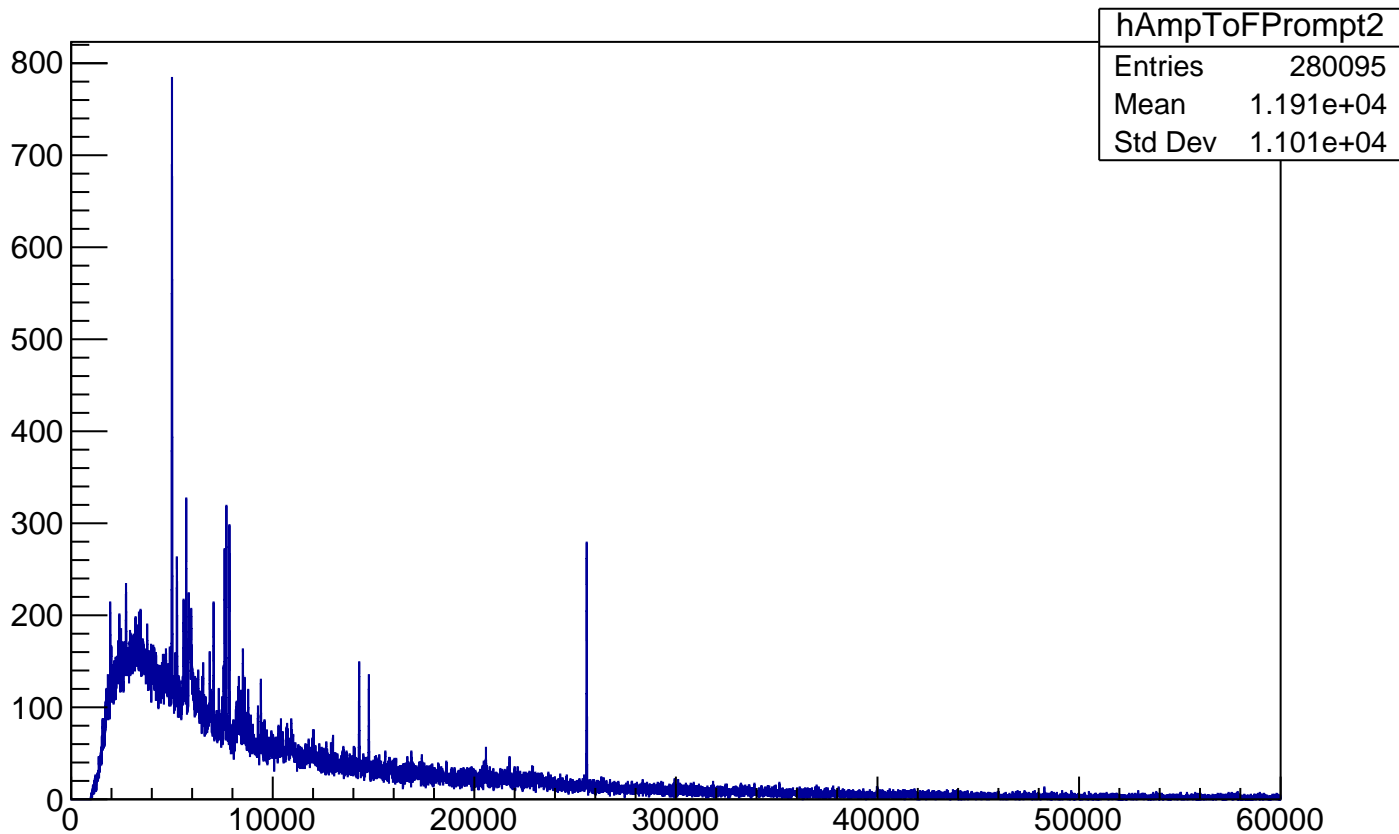
# hAmpToF0



# hToF2

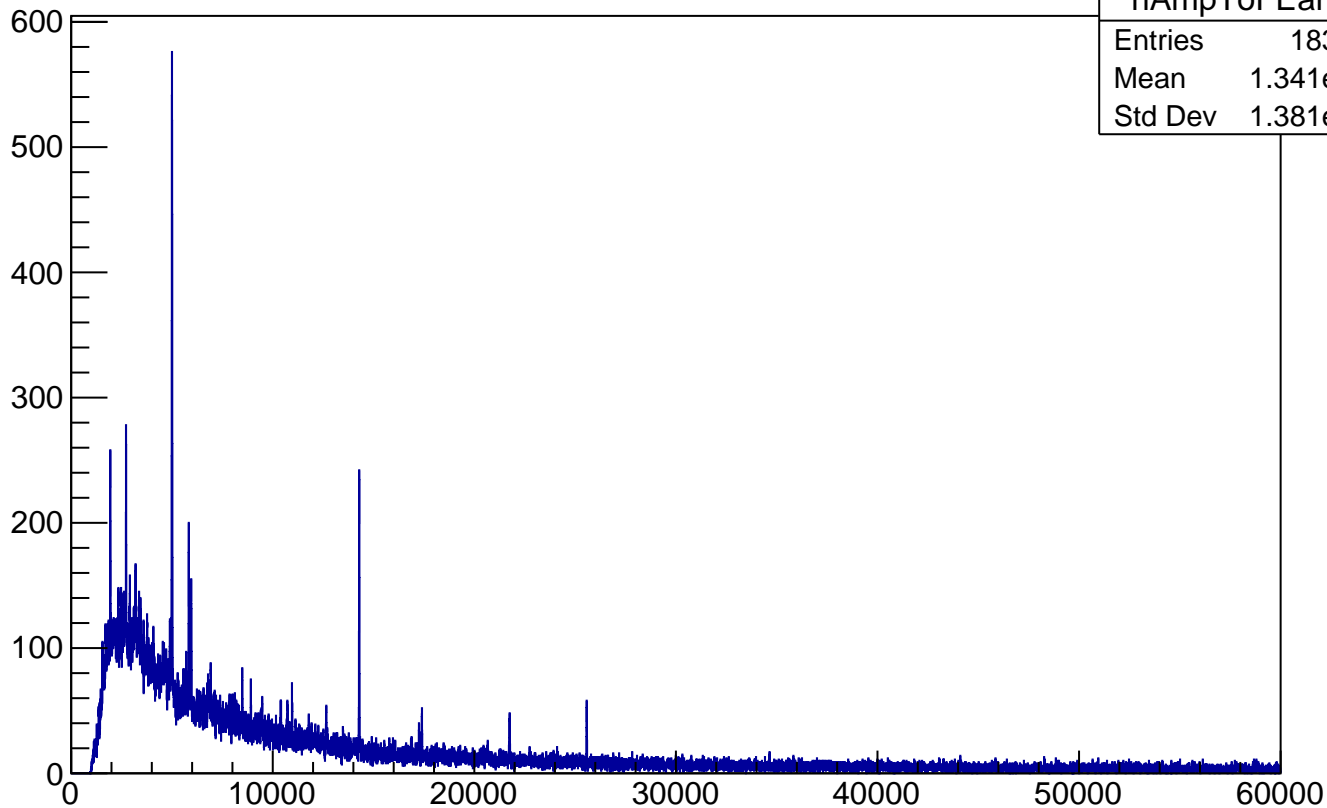


# hAmpToFPrompt2

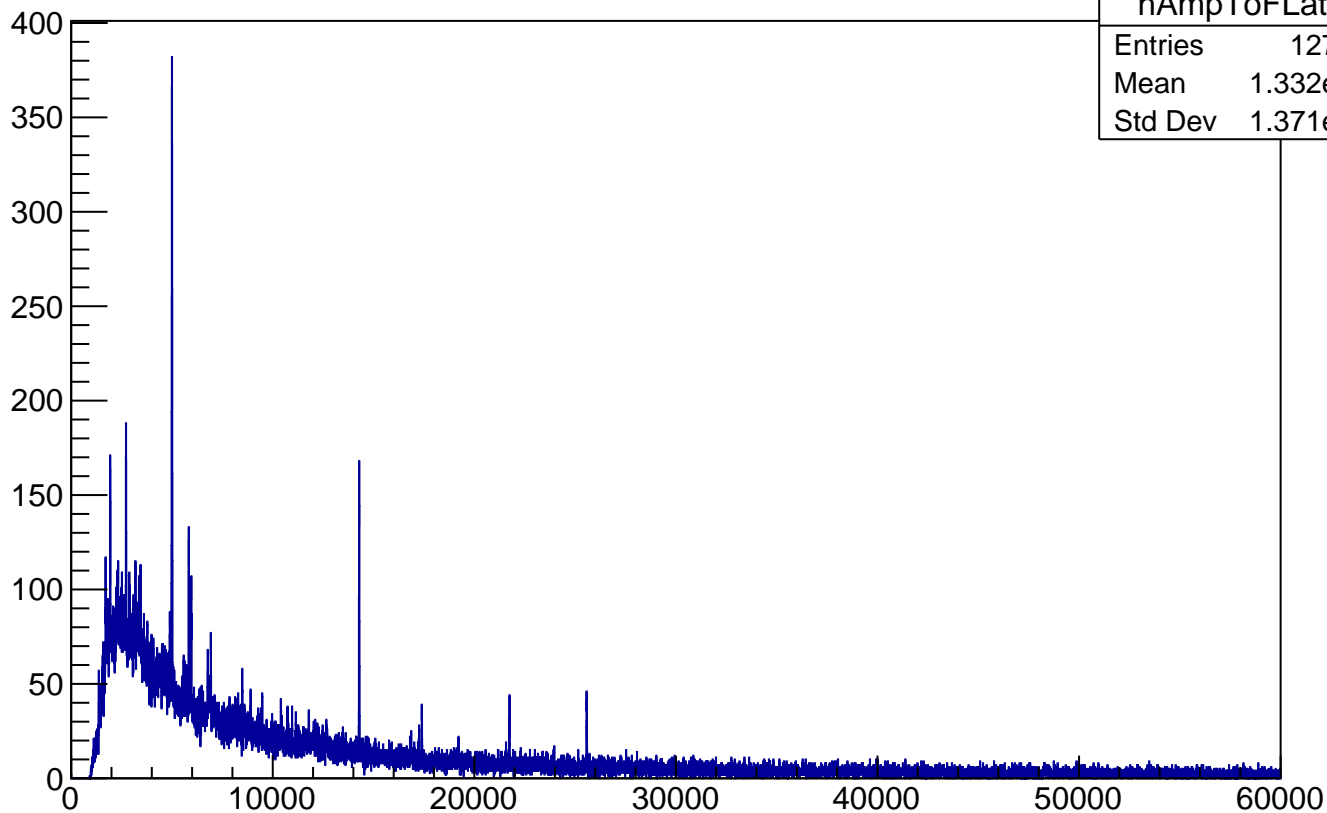




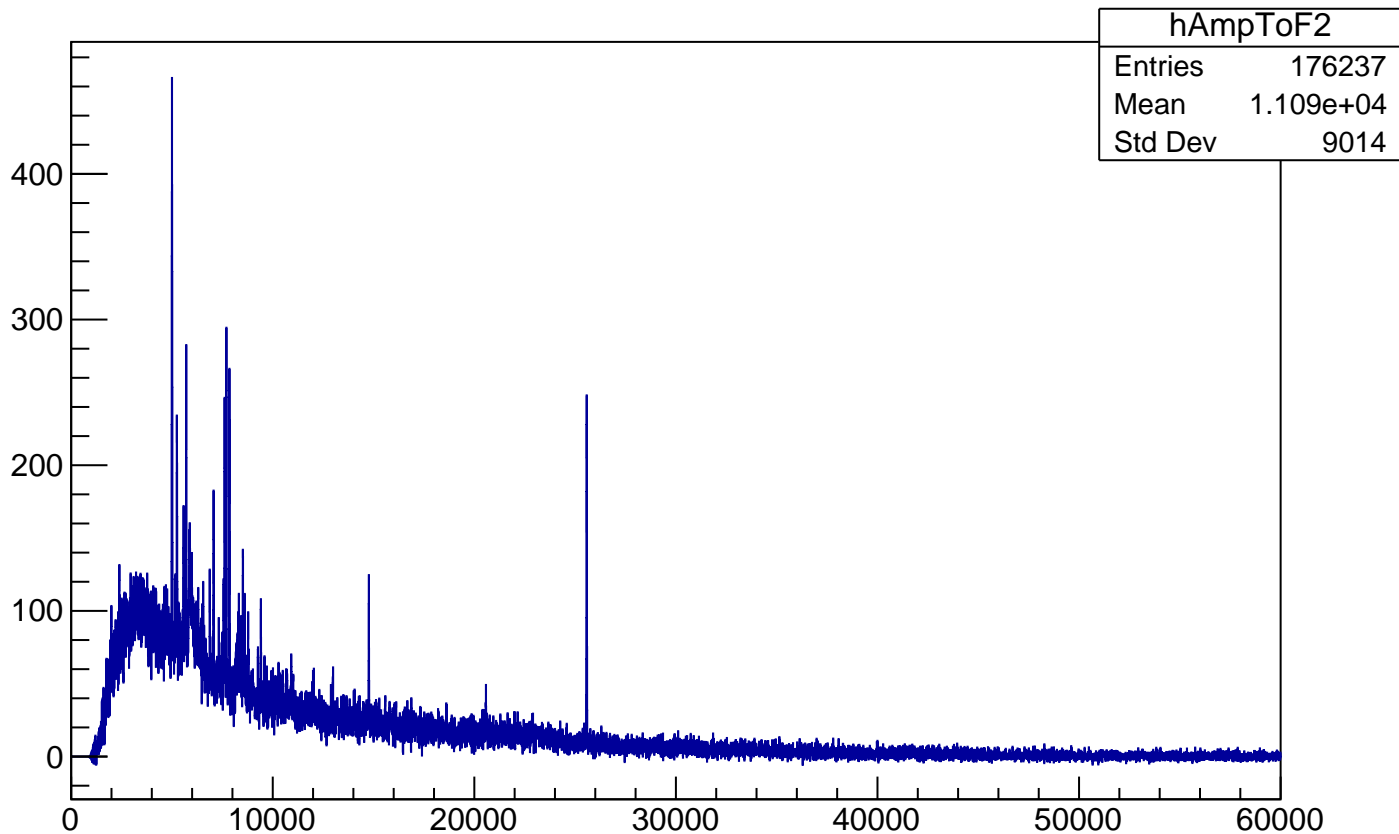
# hAmpToFEarly2



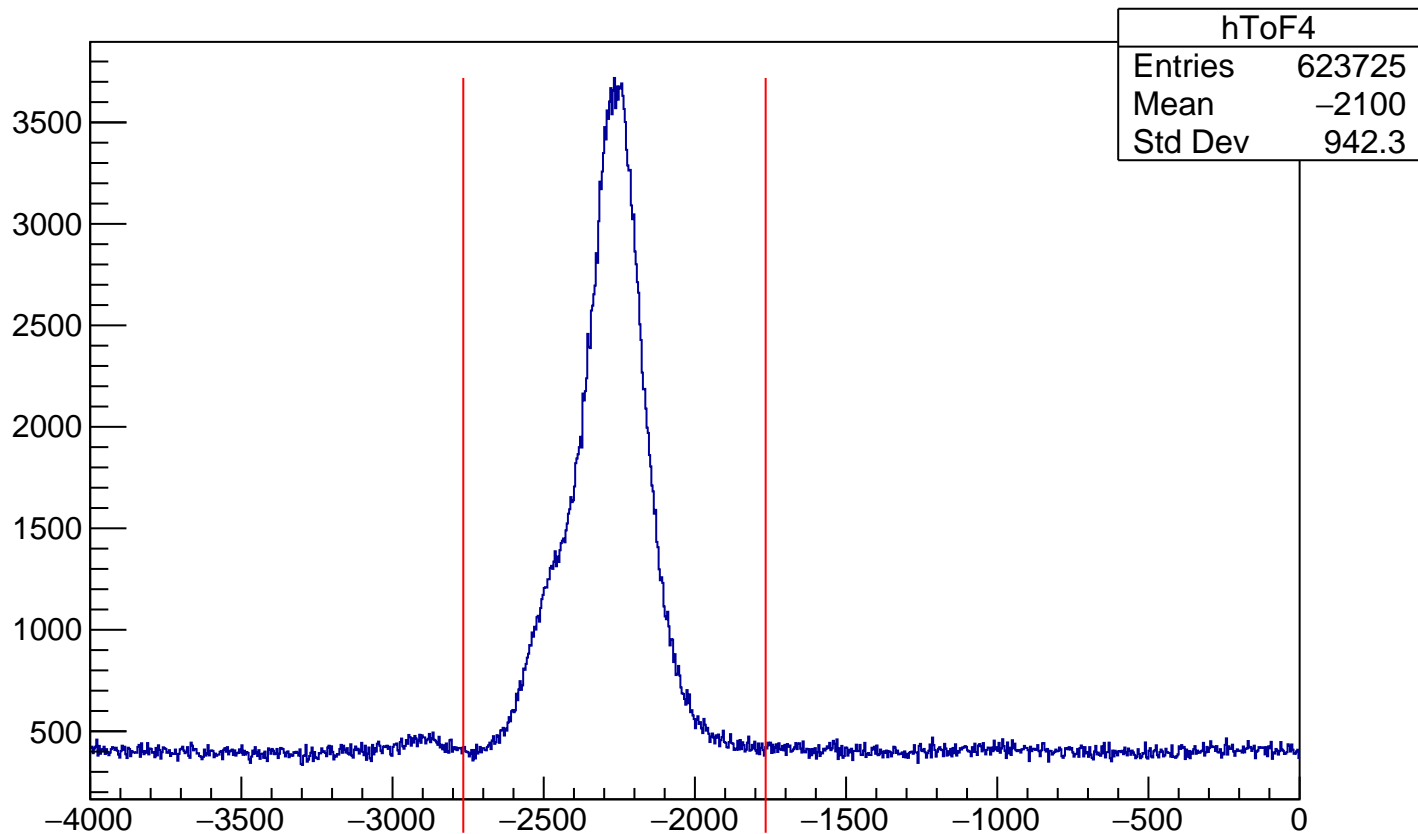
# hAmpToFLate2



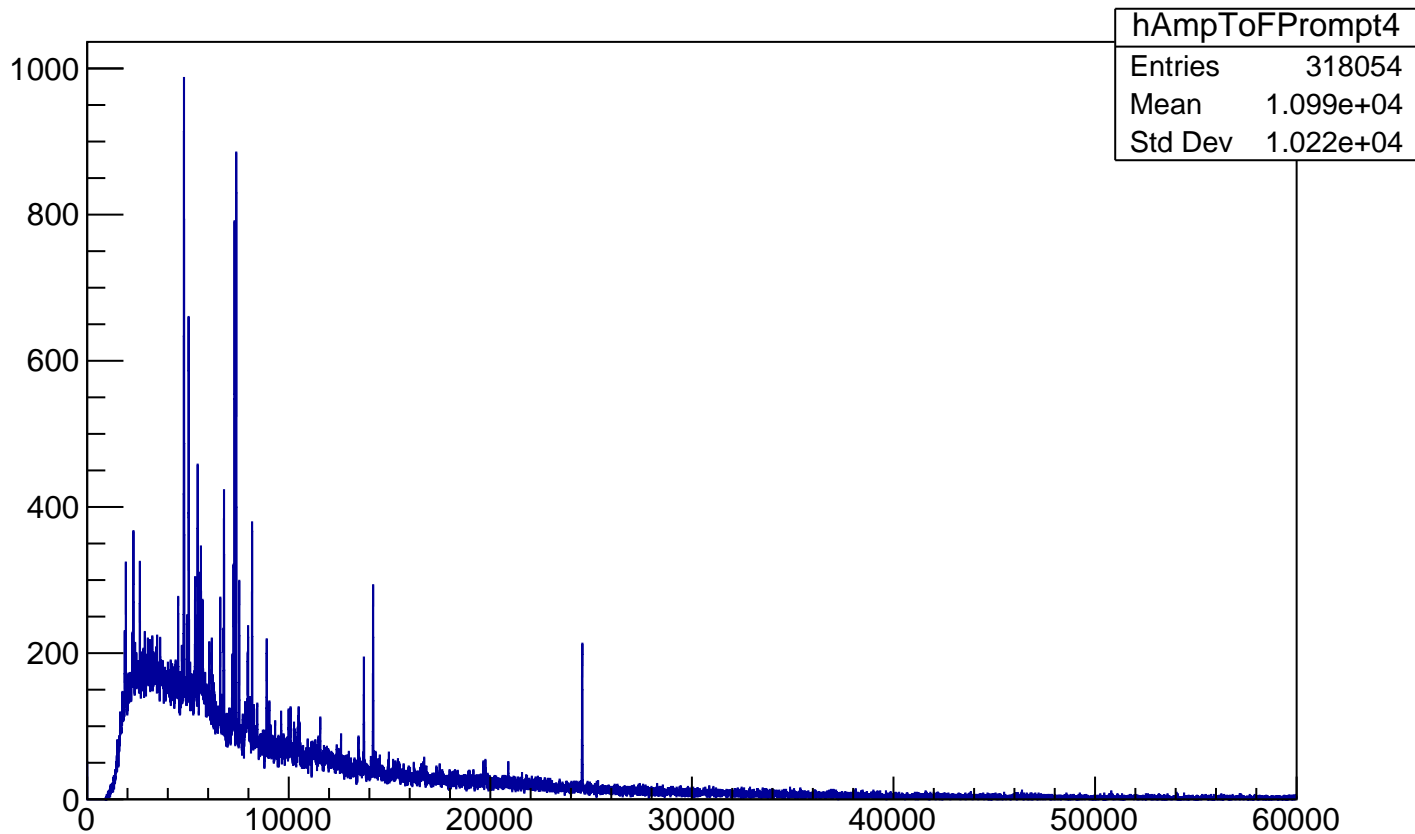
# hAmpToF2



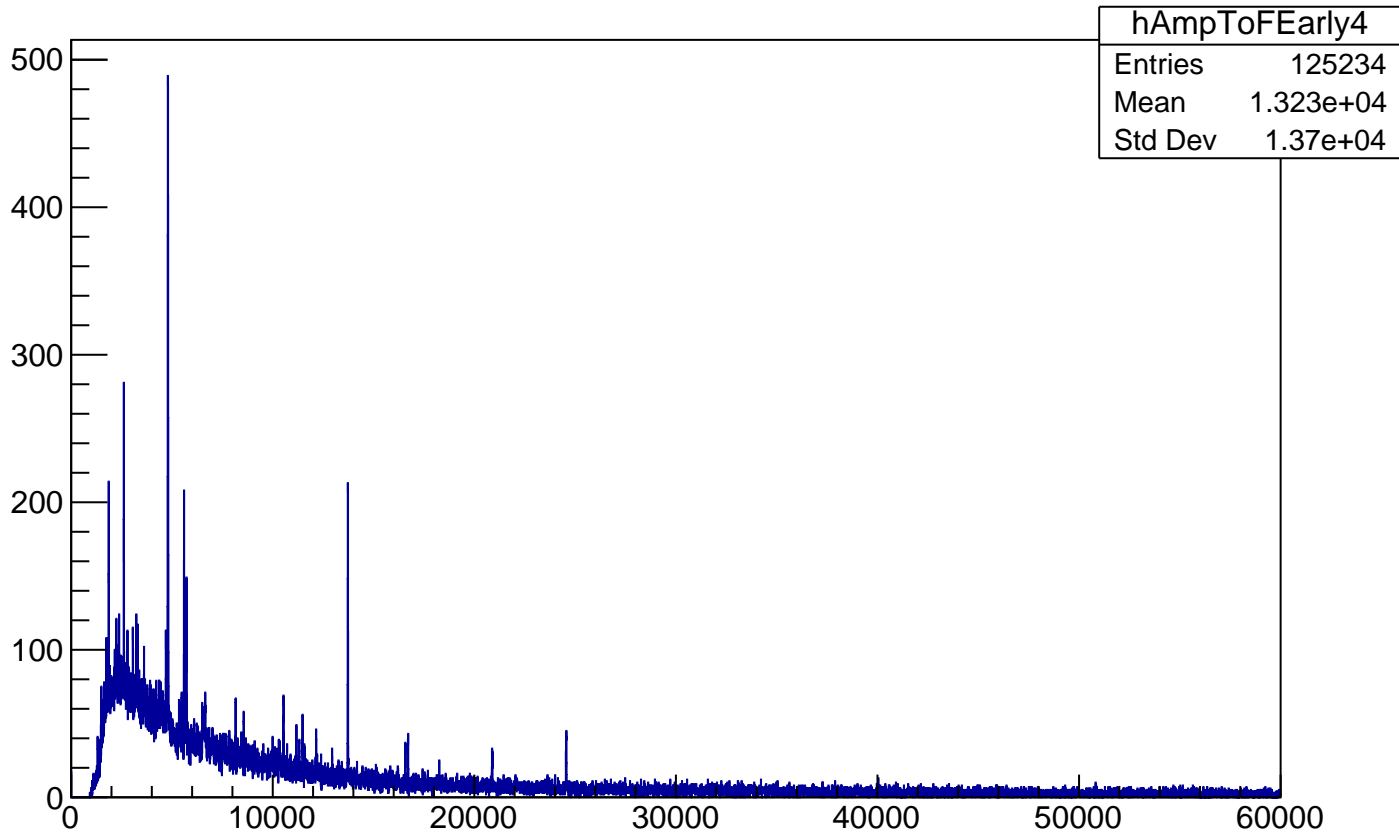
# hToF4



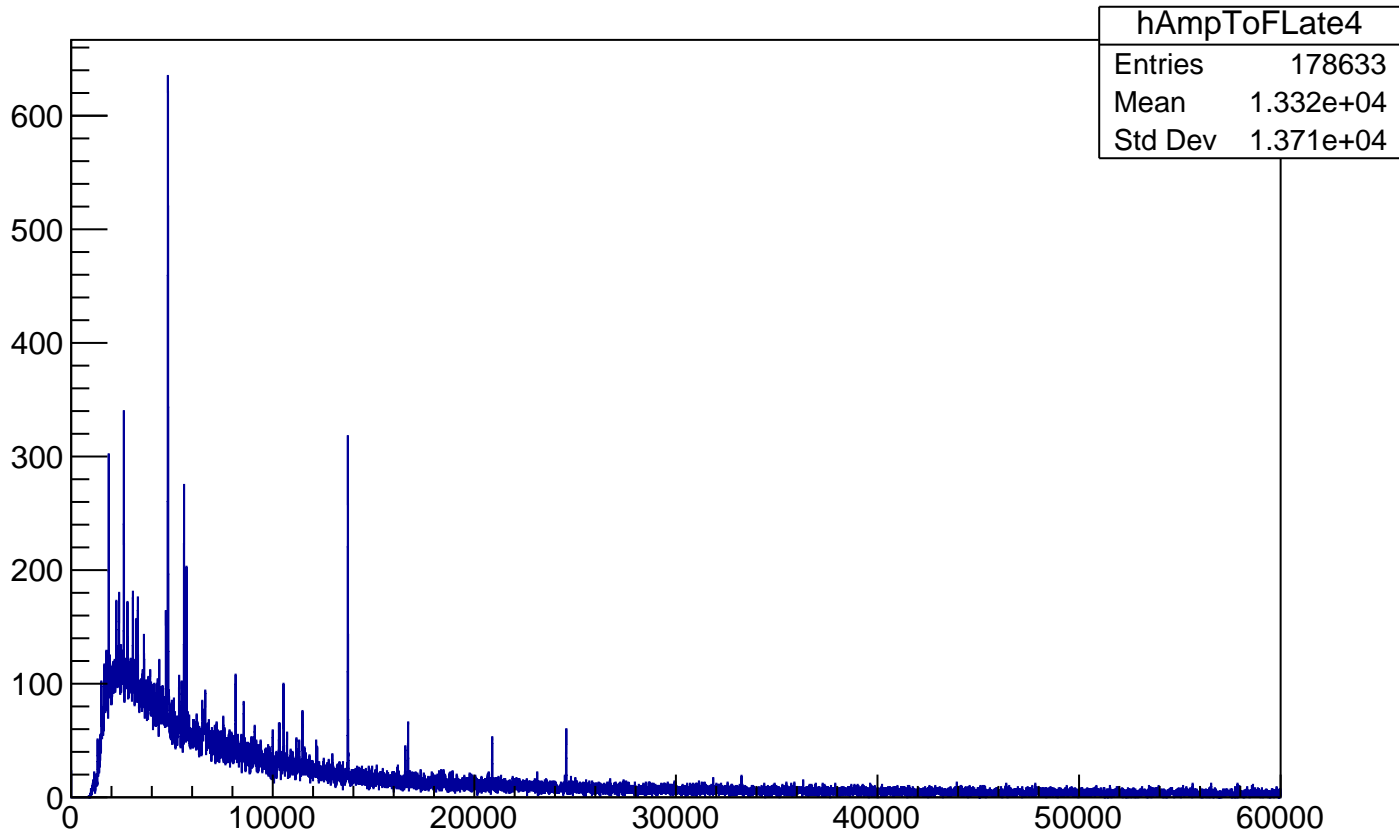
# hAmpToFPrompt4



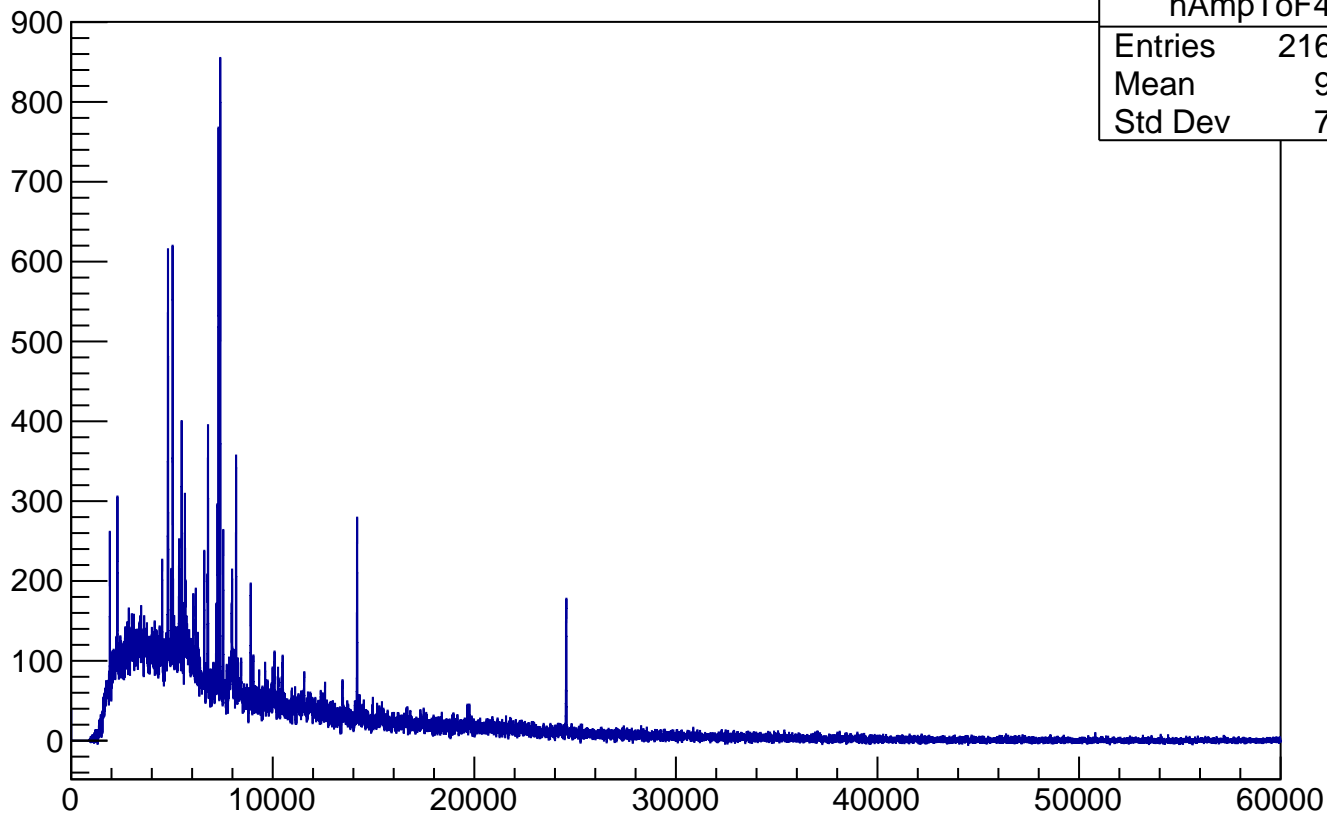
# hAmpToFEarly4



# hAmpToFLate4

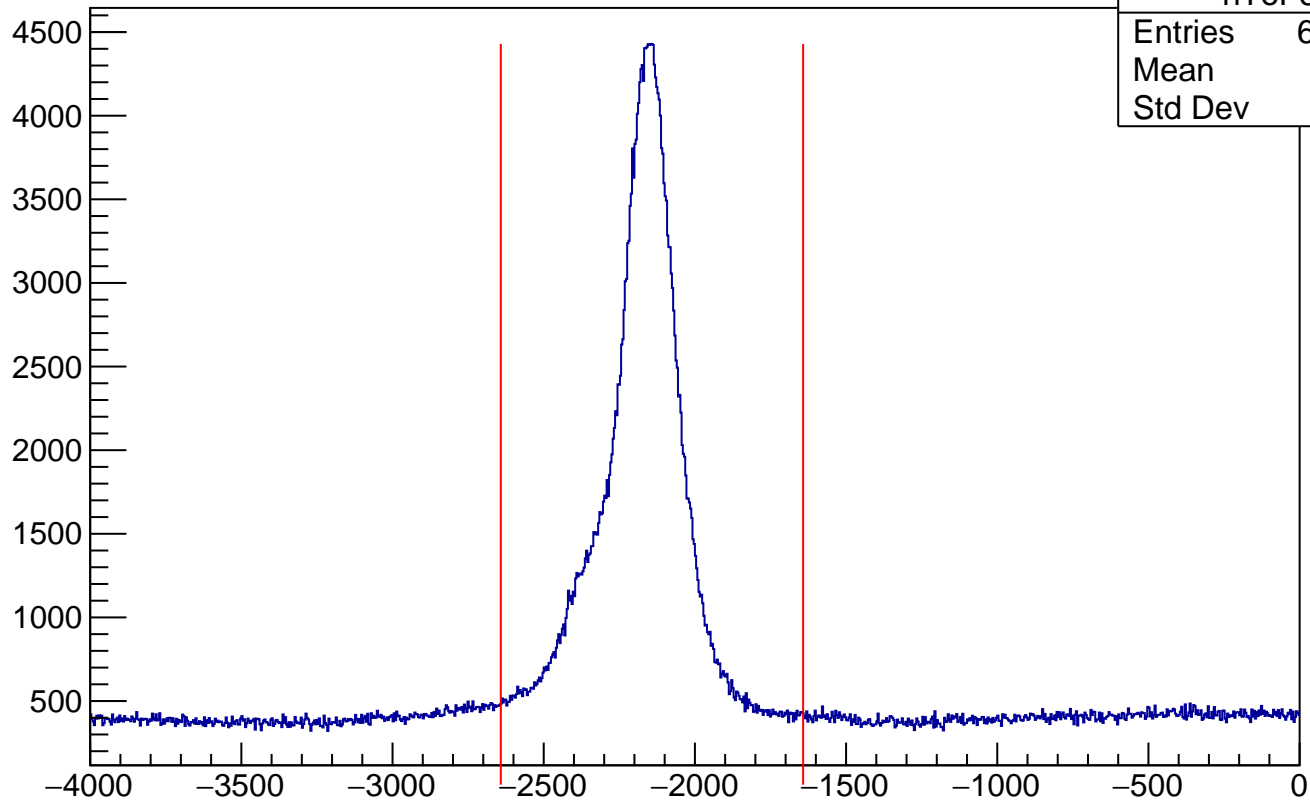


# hAmpToF4



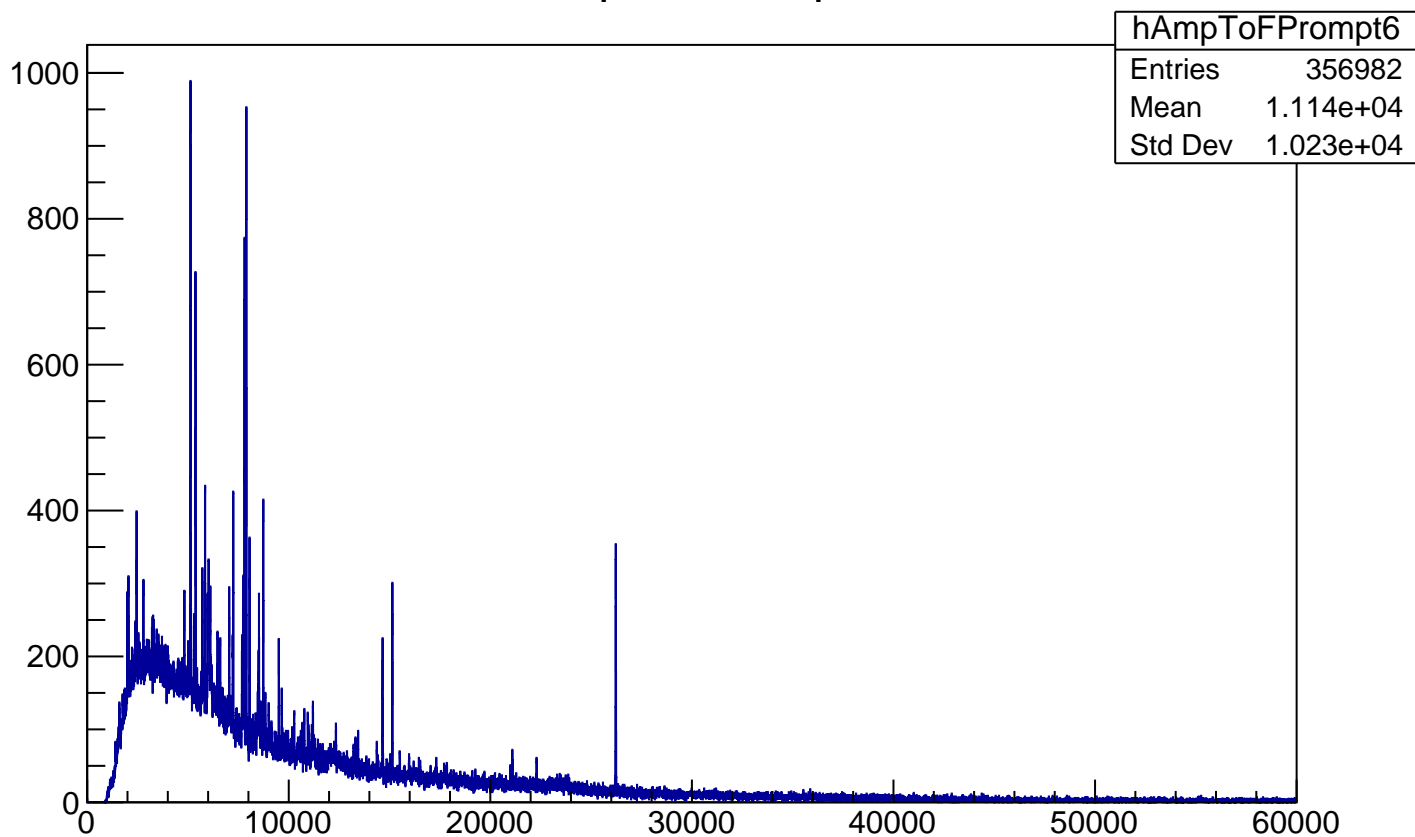


# hToF6

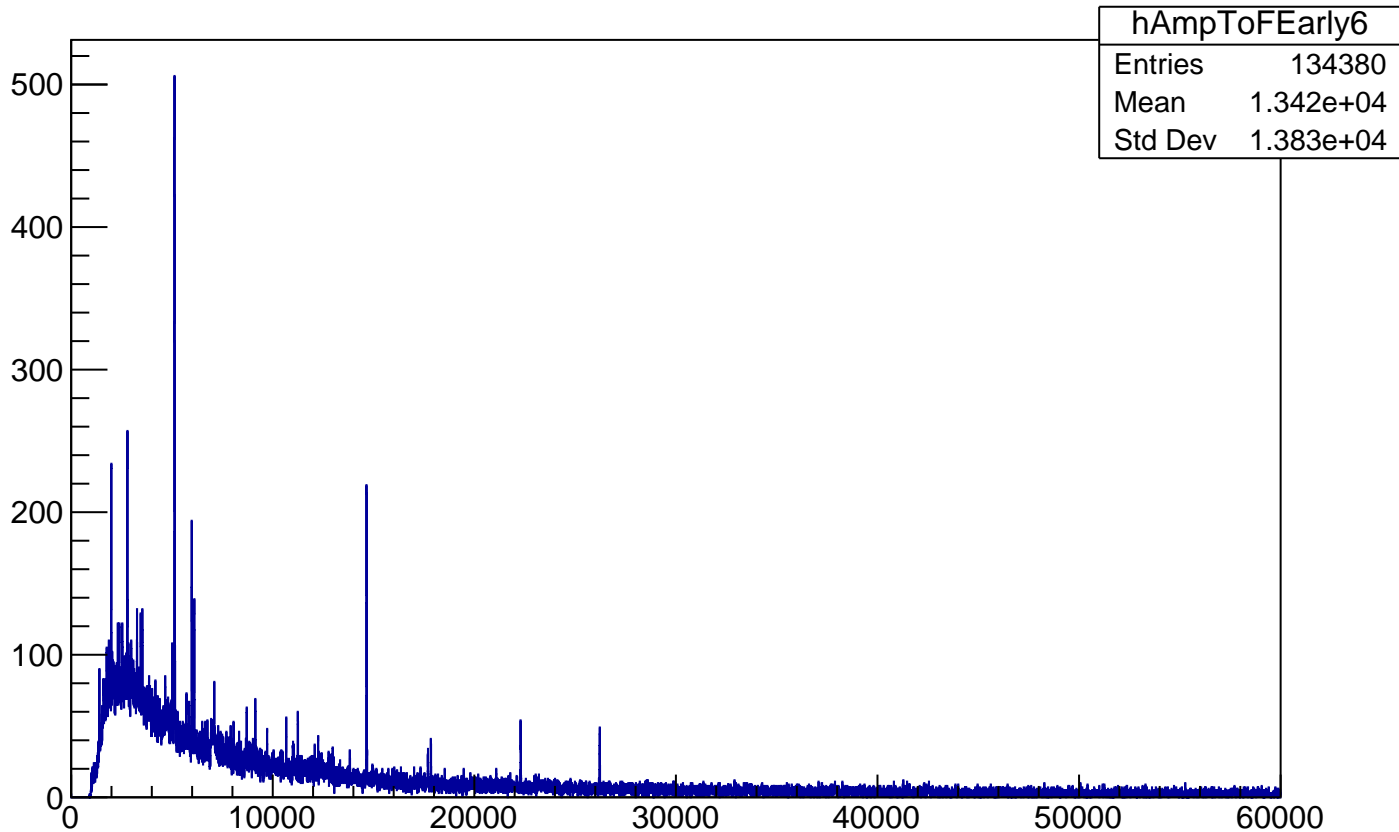


hToF6	
Entries	658855
Mean	-2057
Std Dev	910.4

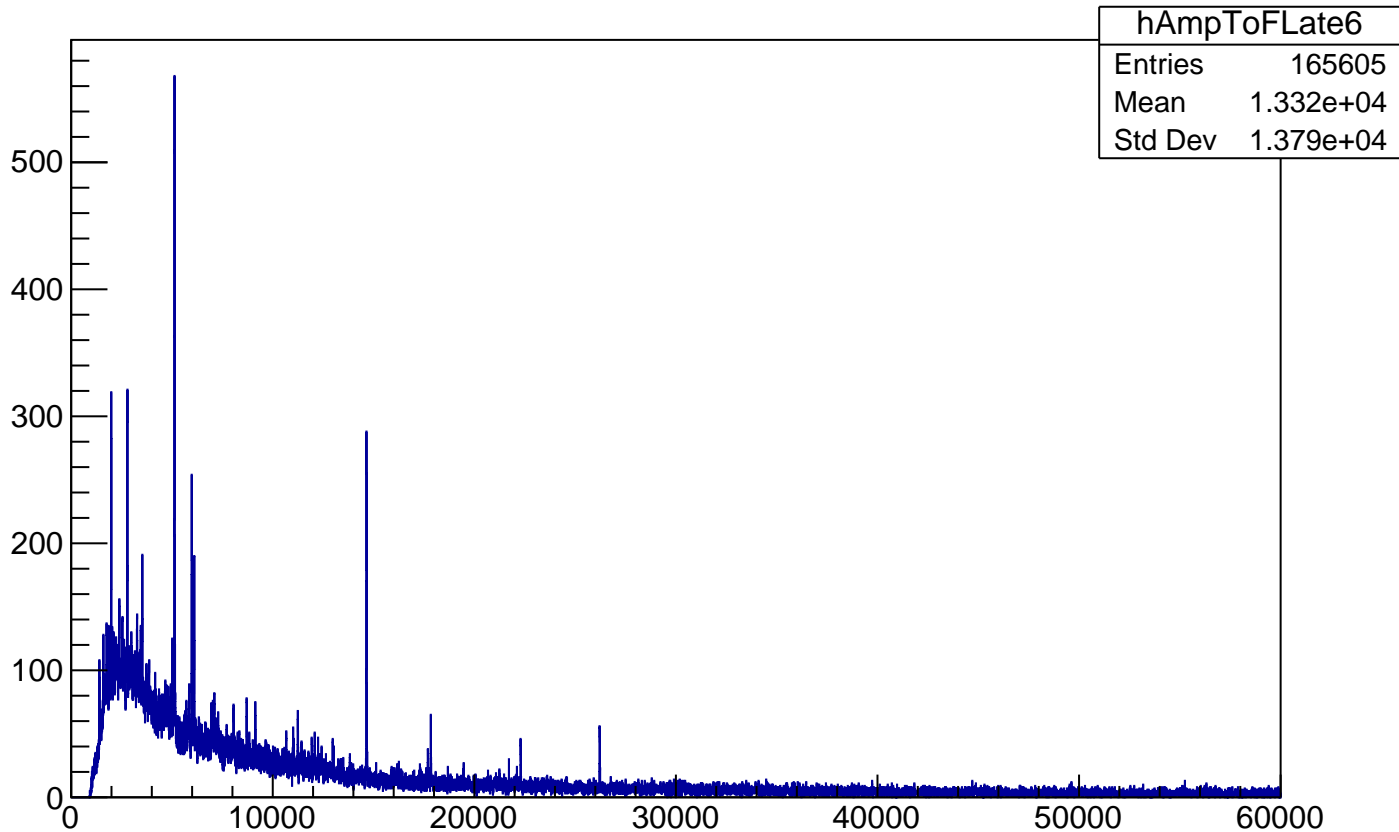
# hAmpToFPrompt6



# hAmpToFEarly6



# hAmpToFLate6



# hAmpToF6

