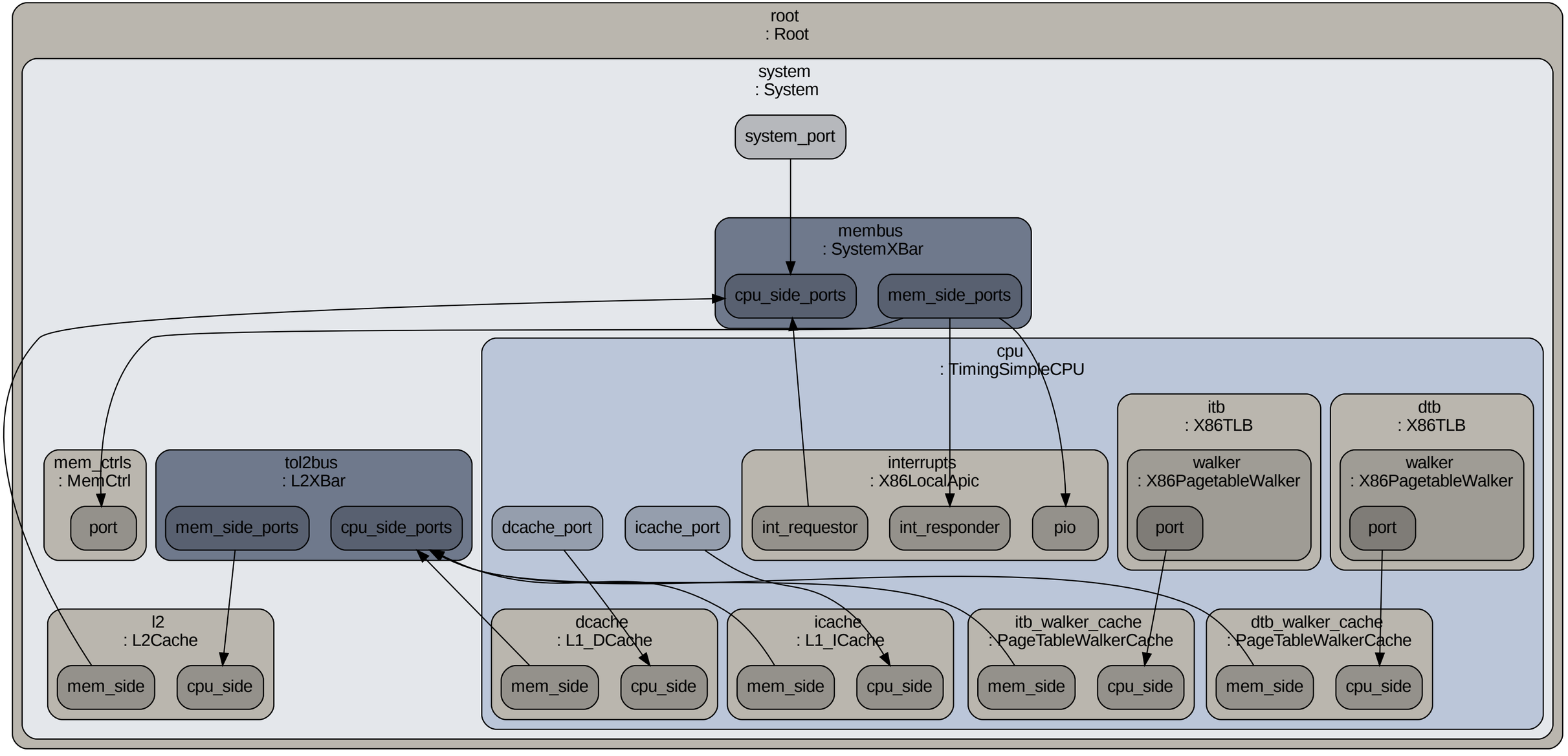
root
: Root
system
: System
system_port
membus
: SystemXBar
cpu_side_ports
mem_side_ports
cpu
: TimingSimpleCPU
itb
: X86TLB
walker
: X86PagetableWalker
port
dtb
: X86TLB
walker
: X86PagetableWalker
port
mem_ctrls
: MemCtrl
port
tol2bus
: L2XBar
mem_side_ports
cpu_side_ports
interrupts
: X86LocalApic
int_requestor
int_responder
pio
dcache_port
icache_port
l2
: L2Cache
mem_side
cpu_side
dcache
: L1_DCache
mem_side
cpu_side
icache
: L1_ICache
mem_side
cpu_side
itb_walker_cache
: PageTableWalkerCache
mem_side
cpu_side
dtb_walker_cache
: PageTableWalkerCache
mem_side
cpu_side