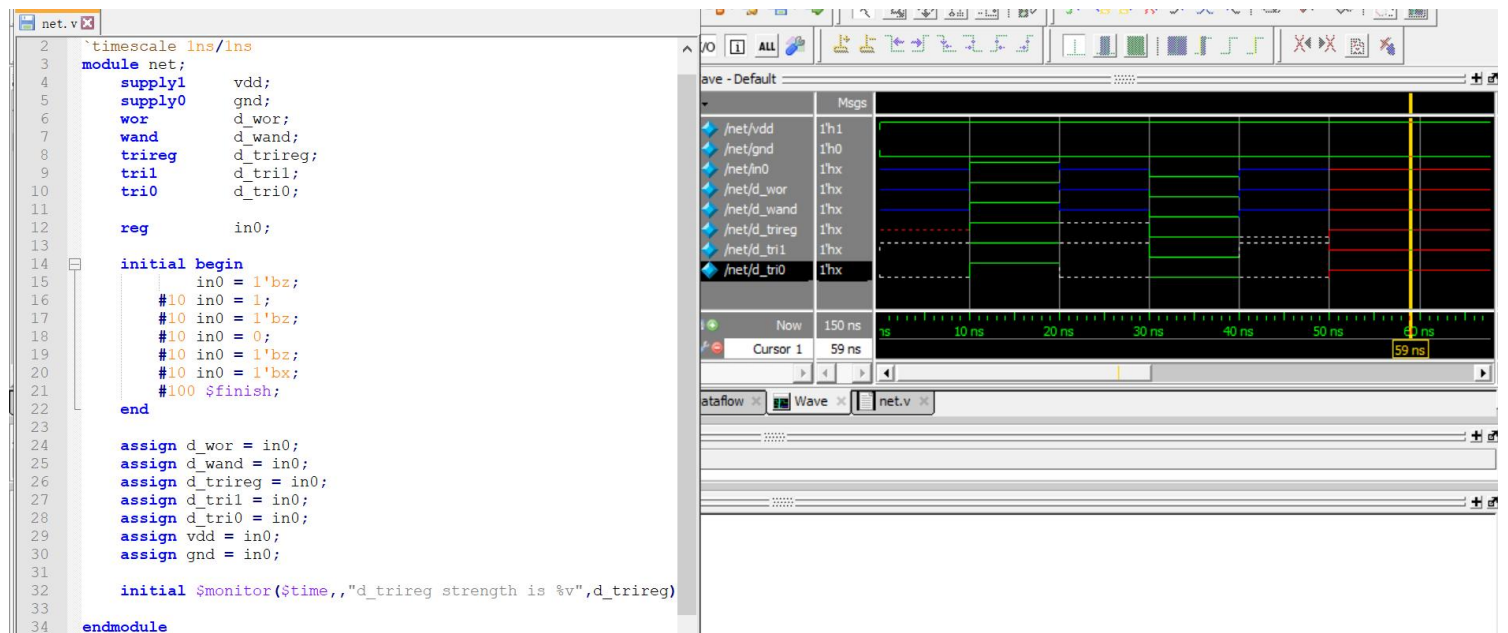


1. 利用 ModelSim 实现 PPT 样例，即显示 d\_trireg 的信号强度：



```
Transcript
VSIM 3> run -all
#
#      0 d_trireg strength is MeX
#      10 d_trireg strength is St1
#      20 d_trireg strength is Me1
#      30 d_trireg strength is St0
#      40 d_trireg strength is Me0
#      50 d_trireg strength is StX
# ** Note: $finish      : H:/study_master/Digital_Integrated_Circuit_Design/homework/net.v(21)
#      Time: 150 ns  Iteration: 0  Instance: /net
```

2. 利用 ModelSim，修改 monitor 中的变量为 vdd，显示 vdd 的信号强度：Sul

```
VSIM 6> run -all
#
#      0 vdd strength is Sul
# ** Note: $finish      : H:/study_master/Digital_Integrated_Circuit_Design/homework/net.v(21)
#      Time: 150 ns  Iteration: 0  Instance: /net
# 1
```

3. 利用 ModelSim，修改 monitor 中的变量为 d\_tri1，显示 d\_tri1 的信号强度：

```
VSIM 9> run -all
#
#      0 d_tril strength is Pul
#      10 d_tril strength is St1
#      20 d_tril strength is Pul
#      30 d_tril strength is St0
#      40 d_tril strength is Pul
#      50 d_tril strength is StX
# ** Note: $finish      : H:/study_master/Digital_Integrated_Circuit_Design/homework/net.v(21)
#      Time: 150 ns  Iteration: 0  Instance: /net
```