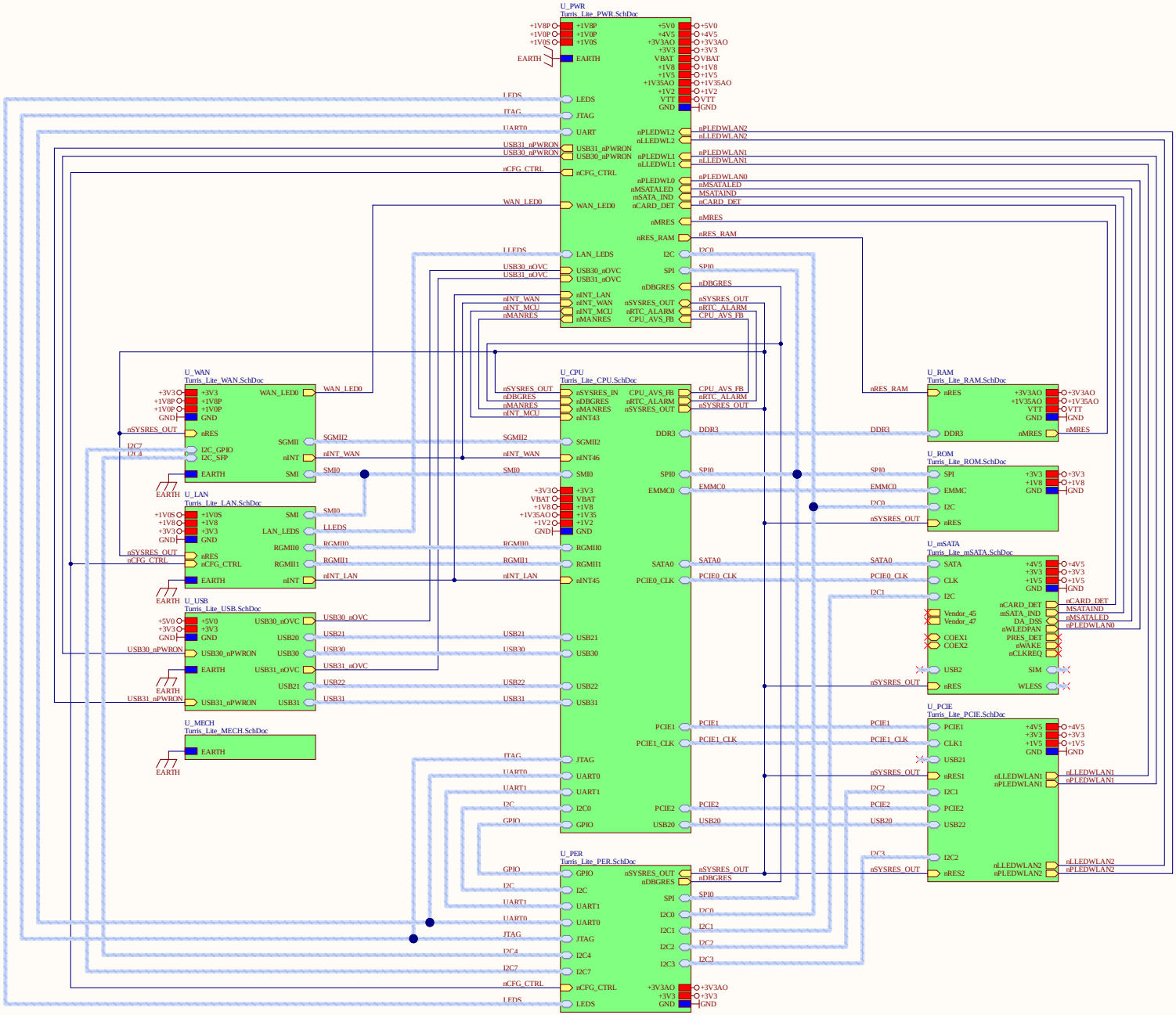
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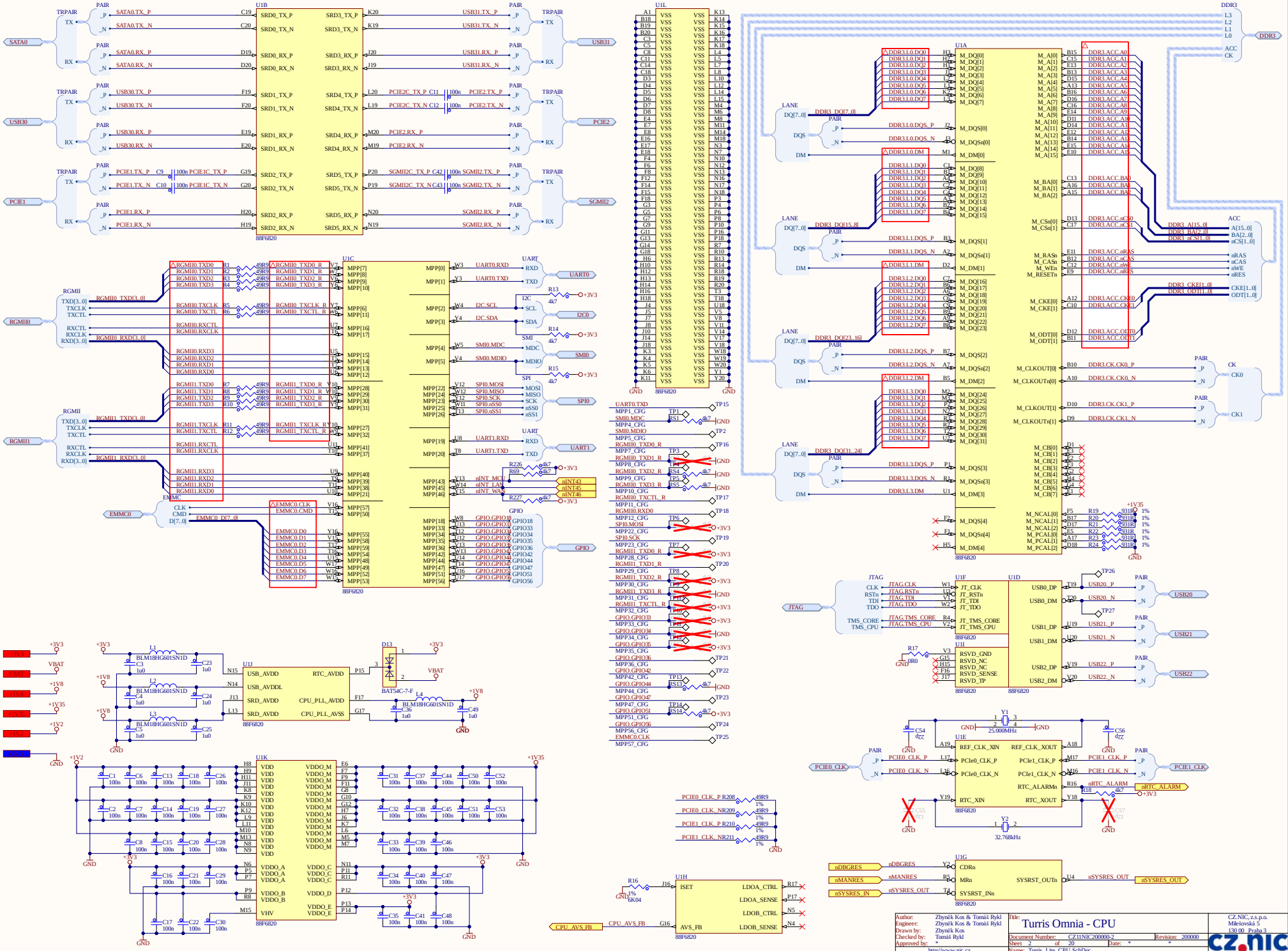
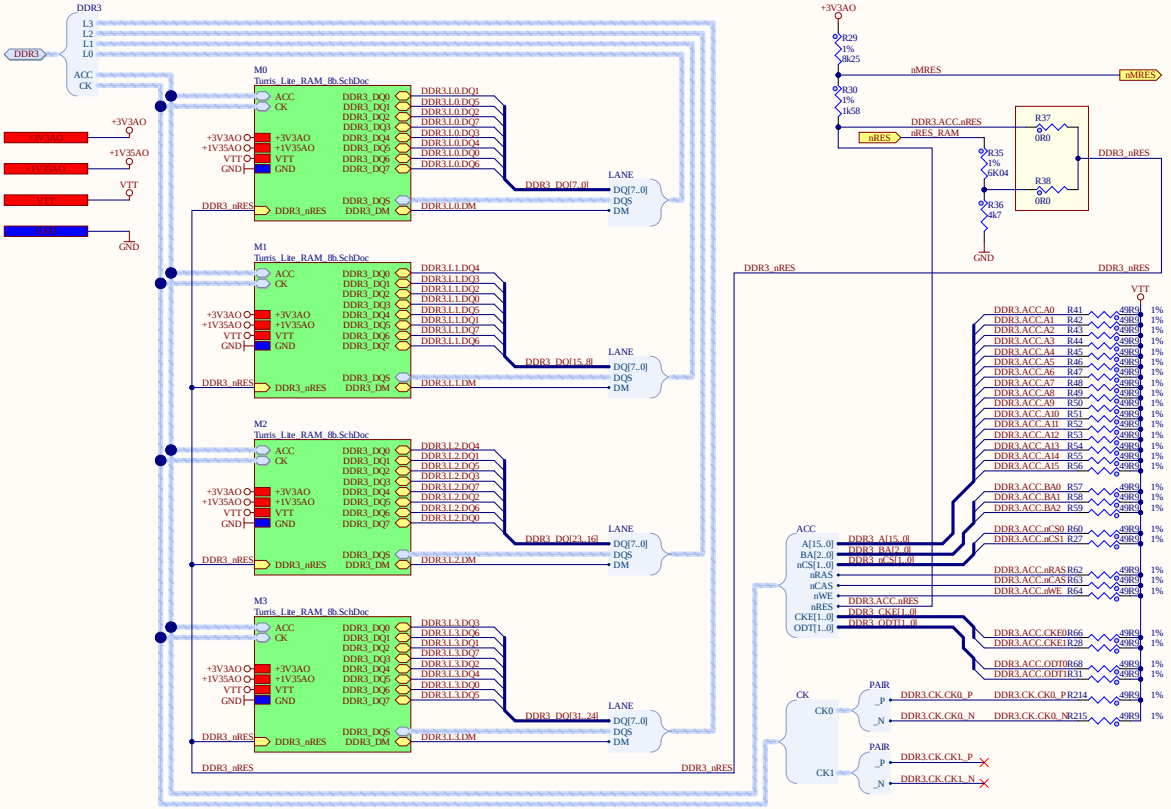
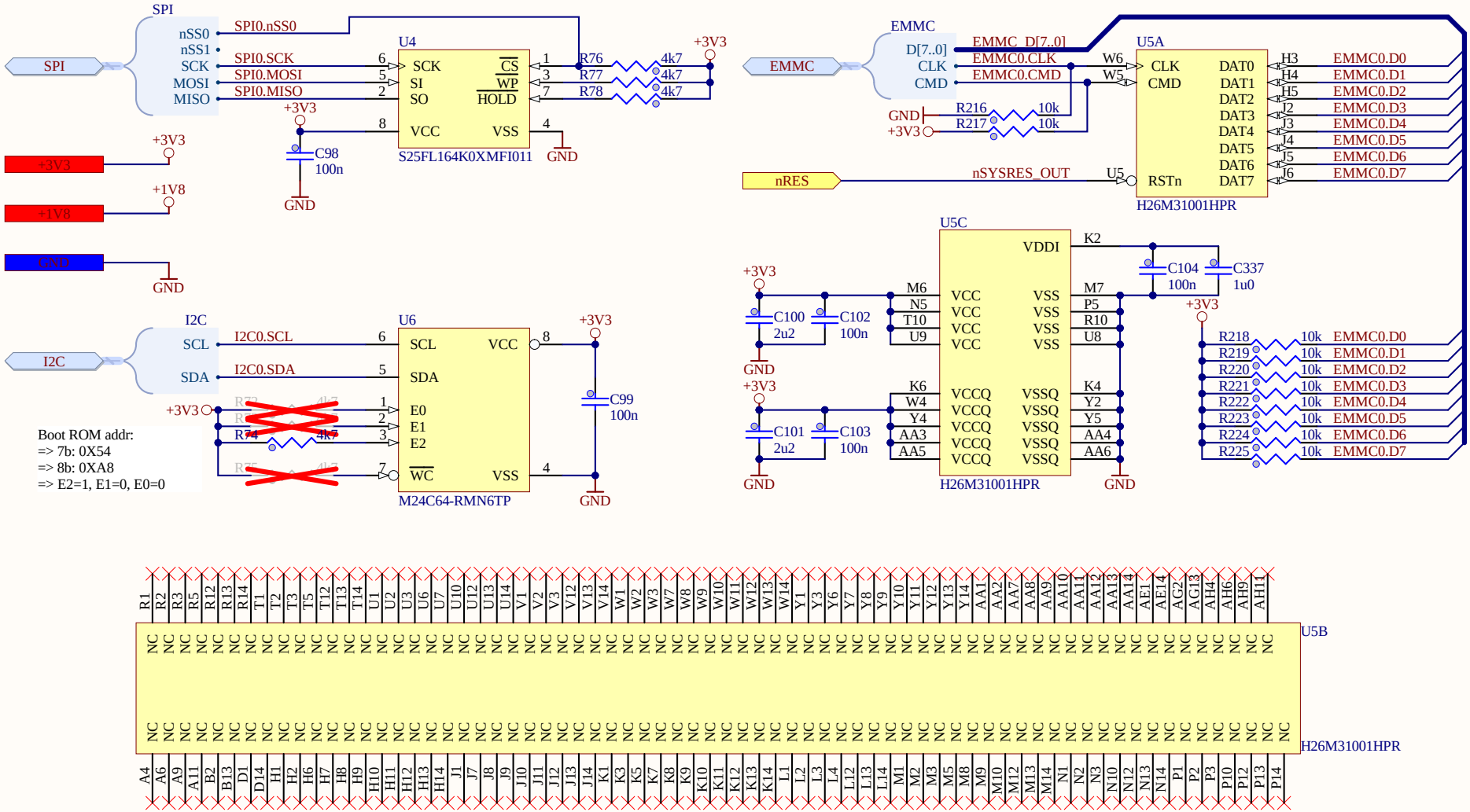
Turris_Lite_PWR.SchDoc
U_WAN
Turris_Lite_WAN.SchDoc
U_LAN
Turris_Lite_LAN.SchDoc
U_USB
Turris_Lite_USB.SchDoc
U_MECH
Turris_Lite_MECH.SchDoc
U_CPU
Turris_Lite_CPU.SchDoc
U_PER
Turris_Lite_PER.SchDoc
U_RAM
Turris_Lite_RAM.SchDoc
U_ROM
Turris_Lite_ROM.SchDoc
U_mSATA
Turris_Lite_mSATA.SchDoc
U_PCIE
Turris_Lite_PCIE.SchDoc
EARTH
LEDS
JTAG
UART0
USB31_nPWRON
USB30_nPWRON
nCFG_CTRL
WAN_LED0
LLEDS
USB30_nOVC
USB31_nOVC
nINT_WAN
nINT_MCU
nMANRES
nRES_RAM
I2C0
SPI0
nDBGRES
nSYSRES_OUT
nRTC_ALARM
CPU_AVS_FB
nMRES
nMSATALED
MSATAIND
nCARD_DET
nPLEDWLAN0
nLLEDWLAN1
nPLEDWLAN1
nLLEDWLAN2
nPLEDWLAN2
SGMII2
SMI0
LLEDS
RGMII0
RGMII1
nINT_LAN
USB30_nOVC
USB21
USB30
USB31_nOVC
USB22
USB31
DDR3
EMMC0
SATA0
PCIE0_CLK
PCIE1
PCIE1_CLK
PCIE2
USB20
I2C1
I2C2
I2C3
I2C4
I2C7
+1V8P
+1V0P
+1V0S
+5V0
+4V5
+3V3AO
+3V3
VBAT
+1V8
+1V5
+1V35AO
+1V2
VTT
GND
+3V3
+1V0S
+1V8
+1V35
nRES
SMI
LAN_LEDS
nINT
I2C_GPIO
I2C_SFP
USB20
USB21
USB22
USB31
nNES
SATA
CLK
I2C
Vendor_45
Vendor_47
COEX1
COEX2
USB2
SIM
WLESS
CLK1
nRES1
nRES2
nLLEDWLAN1
nPLEDWLAN1
nLLEDWLAN2
nPLEDWLAN2
SPI
EMMC
nRES
nMRES
DDR3
nSYSRES_IN
nINT43
nINT46
nINT45
Author: Zbyněk Kos & Tomáš Rykl
Engineer: Zbyněk Kos & Tomáš Rykl
Drawn by: Zbyněk Kos
Checked by: Tomáš Rykl
Approved by: *
http://www.nic.cz
Title: Turris Omnia - Top
Document Number: CZ11NIC200000-1
Revision: 200000
Sheet 1 of 20
Date: * *
Name: Turris_Lite.SchDoc
CZ.NIC, z.s.p.o.
Milešovská 5
130 00 Praha 3
cz.nic


U1B
88F6820
SATA0
USB30
PCIE1
USB31
PCIE2
SGMII2
U1C
RGMII0
RGMII1
EMMC
UART0
UART1
SMI0
SPI0
I2C0
GPIO
U1L
U1A
DDR3
U1F
U1D
JTAG
USB20
USB21
USB22
U1J
U1K
U1E
PCIE0_CLK
PCIE1_CLK
U1U
U1G
nDBGRES
nMANRES
nSYSRES_IN
nSYSRES_OUT
U1H
CPU_AVS_FB
Author: Zbyněk Kos & Tomáš Rykl
Engineer: Zbyněk Kos & Tomáš Rykl
Drawn by: Zbyněk Kos
Checked by: Tomáš Rykl
Approved by: *
http://www.nic.cz
Title: Turris Omnia - CPU
CZ.NIC, z.s.p.o.
Milešovská 5
130 00 Praha 3
Document Number: CZ11NIC200000-2
Revision: 200000
Sheet 2 of 20
Date: * *
Name: Turris_Lite_CPU.SchDoc




| | | | |
|---|---|---|---|
| Author: | Zbyněk Kos & Tomáš Rykl | Title: | Turris Omnia - RAM |
| Engineer: | Zbyněk Kos & Tomáš Rykl | | |
| Drawn by: | Zbyněk Kos | | |
| Checked by: | Tomáš Rykl | Document Number: | CZ.11NIC200000-3 |
| Approved by: | * | Revision: | 200000 |
| http://www.nic.cz | | Sheet 3 of 20 | Date: * * |
| | | Name: Turris_Lite_RAM.SchDoc | |

CZ.NIC, z.s.p.o.
Milešovská 5
130 00 Praha 3

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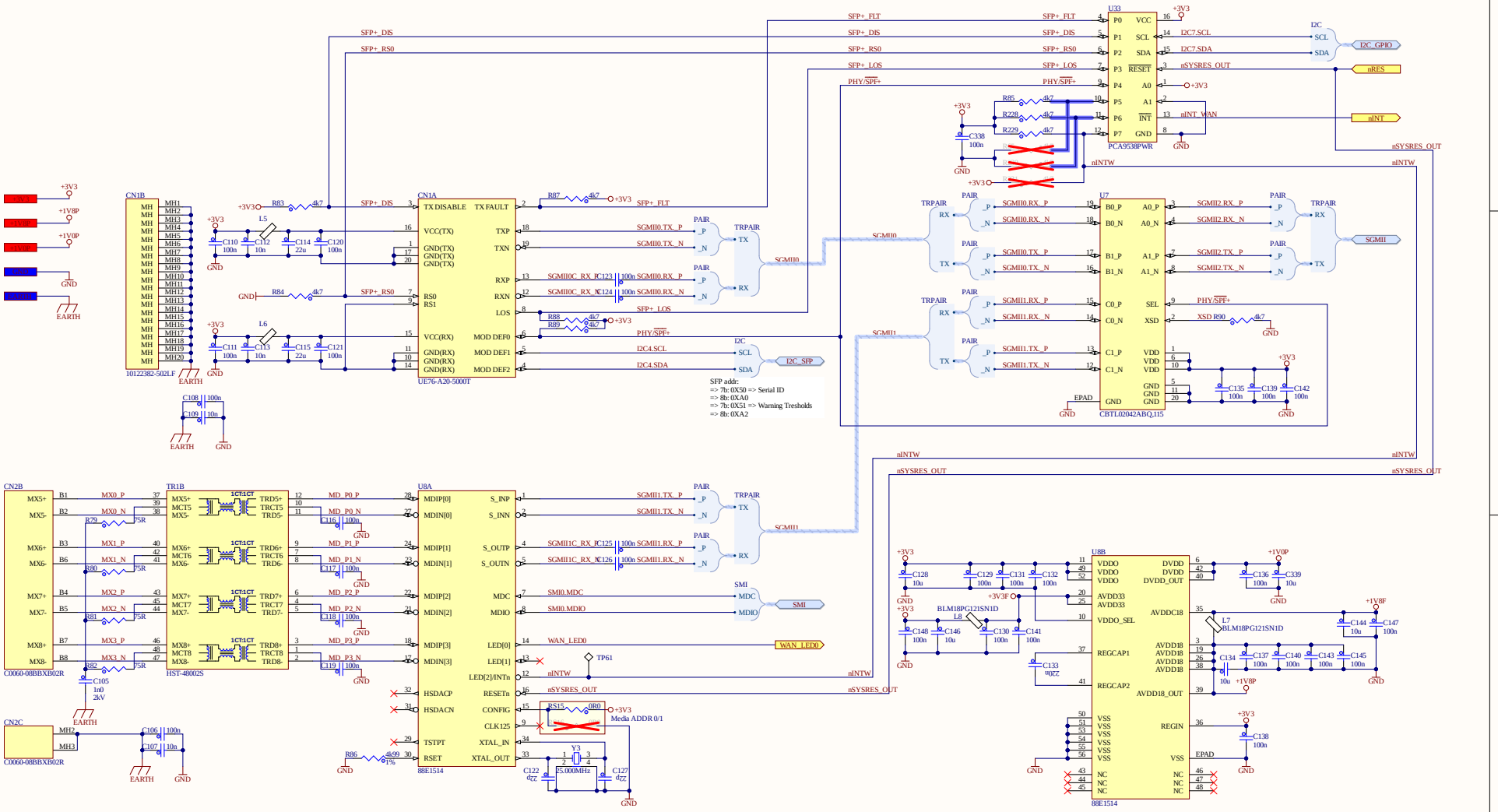
|---|---|---|---|
| Author: | Zbyněk Kos & Tomáš Rykl | Title: Turris Omnia - ROM | CZ.NIC, z.s.p.o.<br>Milešovská 5<br>130 00 Praha 3 |
| Engineer: | Zbyněk Kos & Tomáš Rykl | | |
| Drawn by: | Zbyněk Kos | | |
| Checked by: | Tomáš Rykl | Document Number: CZ11NIC200000-4 | Revision: 200000 |
| Approved by: | * | Sheet 4 of 20 | Date: * * |
| http://www.nic.cz | | Name: Turris_Lite_ROM.SchDoc | |

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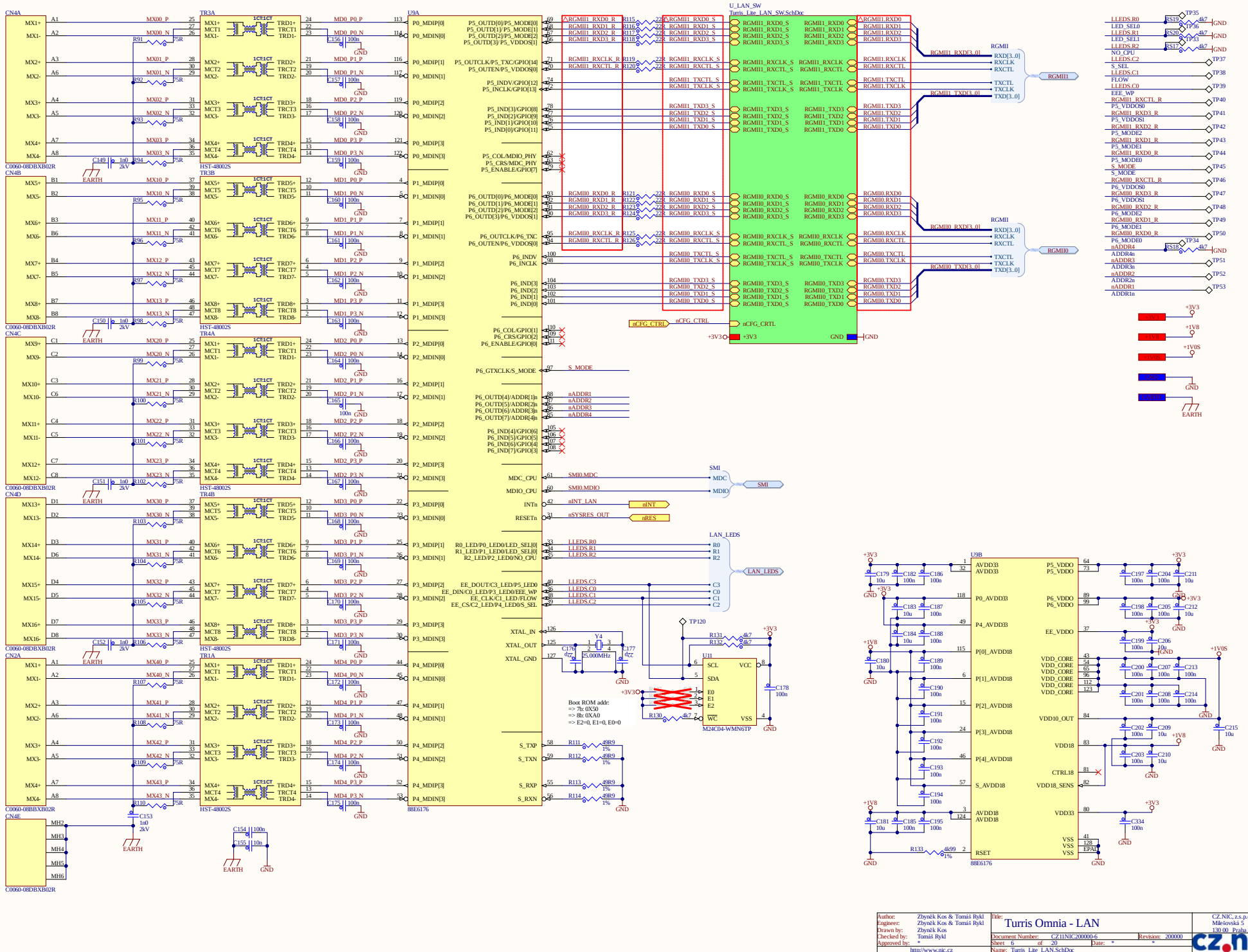
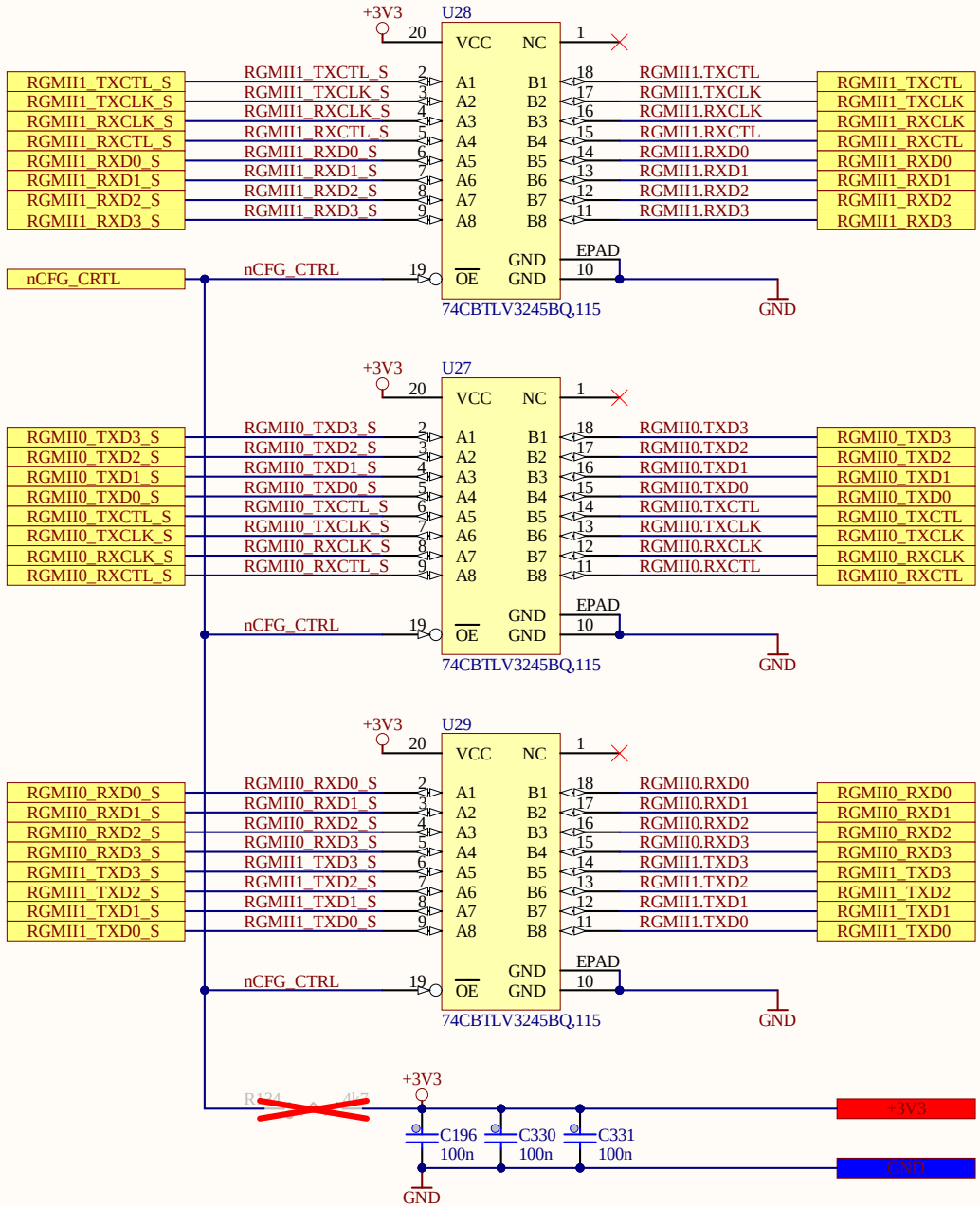
|---|---|---|---|
| Author: | Zbyněk Kos & Tomáš Rykl | Title: | Turris Omnia - WAN |
| Engineer: | Zbyněk Kos & Tomáš Rykl | | |
| Drawn by: | Zbyněk Kos | | |
| Checked by: | Tomáš Rykl | Document Number: | CZ11NIC200000-5 |
| Approved by: | * | Revision: | 200000 |
| | http://www.nic.cz | Sheet 5 of 20 | Date: * * |
| | | Name: Turris_Lite_WAN.SchDoc | |

CZ.NIC, z.s.p.o.
Milešovská 5
130 00 Praha 3


U_LAN_SW
Turris_Lite_LAN_SW.SchDoc
CN4A
CN4B
CN4C
CN4D
CN2A
CN4E
TR3A
TR3B
TR4A
TR4B
TR1A
U9A
88E6176
RGMII1
RGMII0
SMI
LAN_LEDS
U11
M24C04-WMN6TP
U9B
Boot ROM addr:
=> 7b: 0X50
=> 8b: 0XA0
=> E2=0, E1=0, E0=0
Author: Zbyněk Kos & Tomáš Rykl
Engineer: Zbyněk Kos & Tomáš Rykl
Drawn by: Zbyněk Kos
Checked by: Tomáš Rykl
Approved by: *
Title: Turris Omnia - LAN
Document Number: CZ11NIC200000-6
Revision: 200000
Sheet 6 of 20
Date: * *
Name: Turris_Lite_LAN.SchDoc
CZ.NIC, z.s.p.o.
Milešovská 5
130 00 Praha 3
http://www.nic.cz

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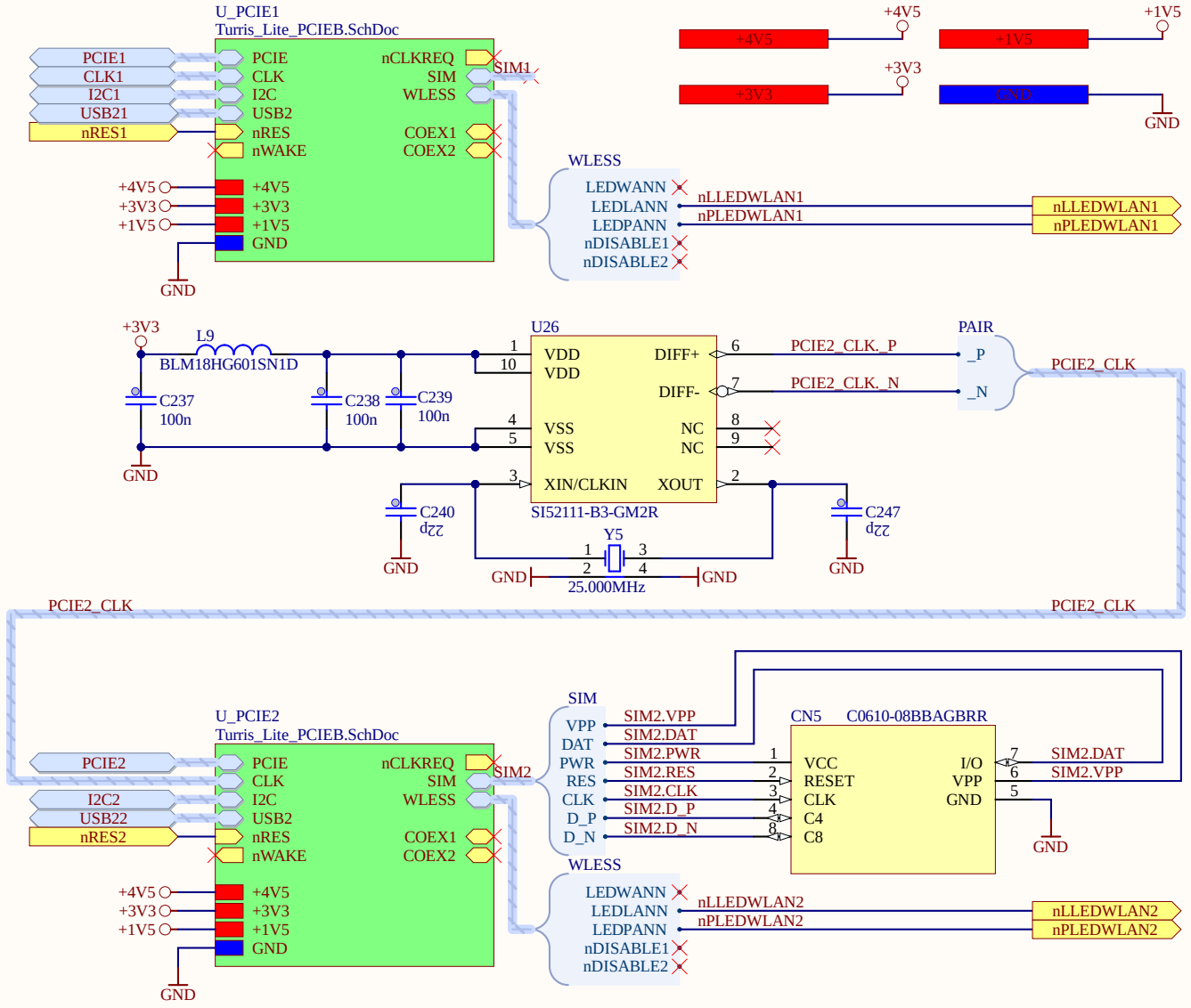
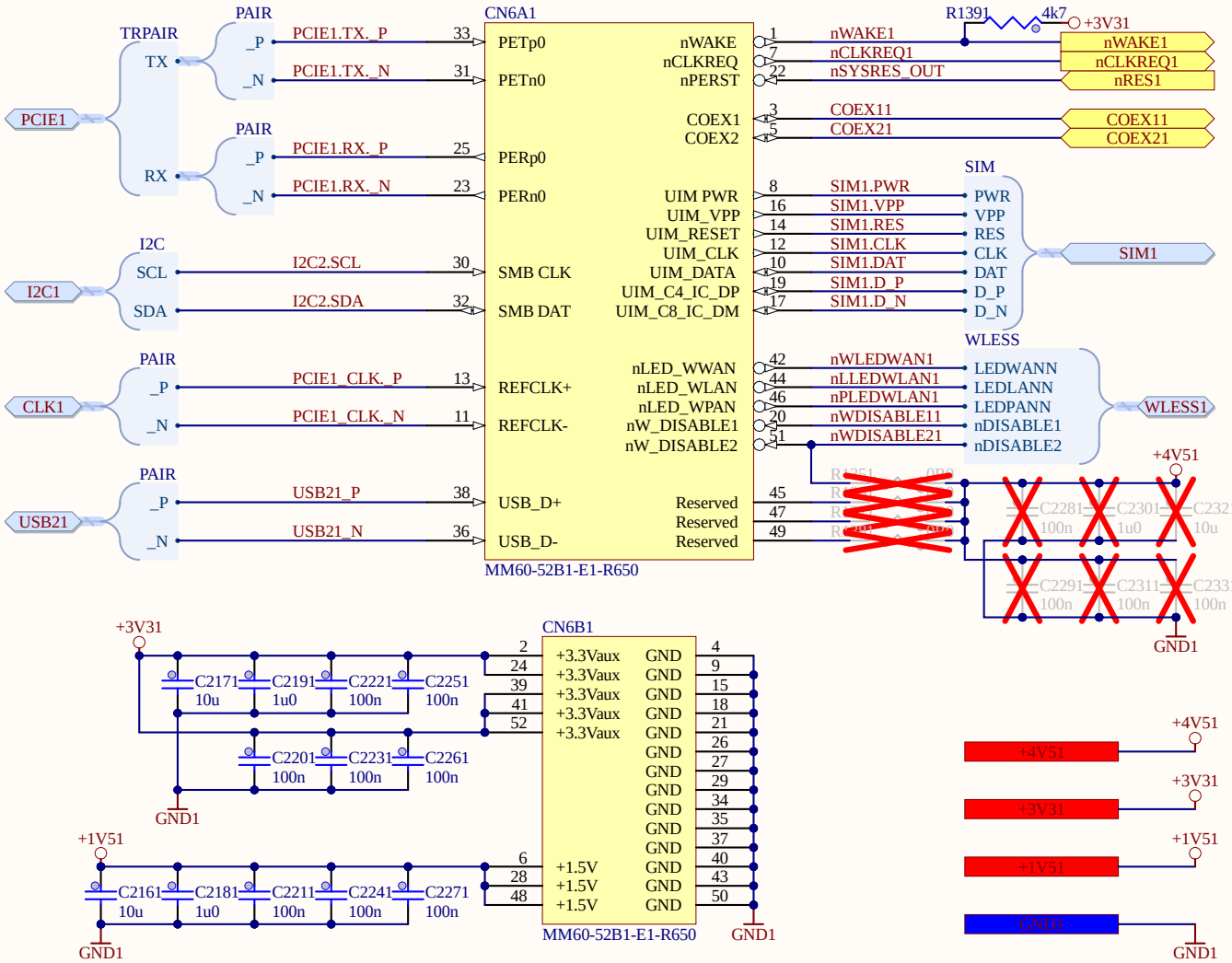
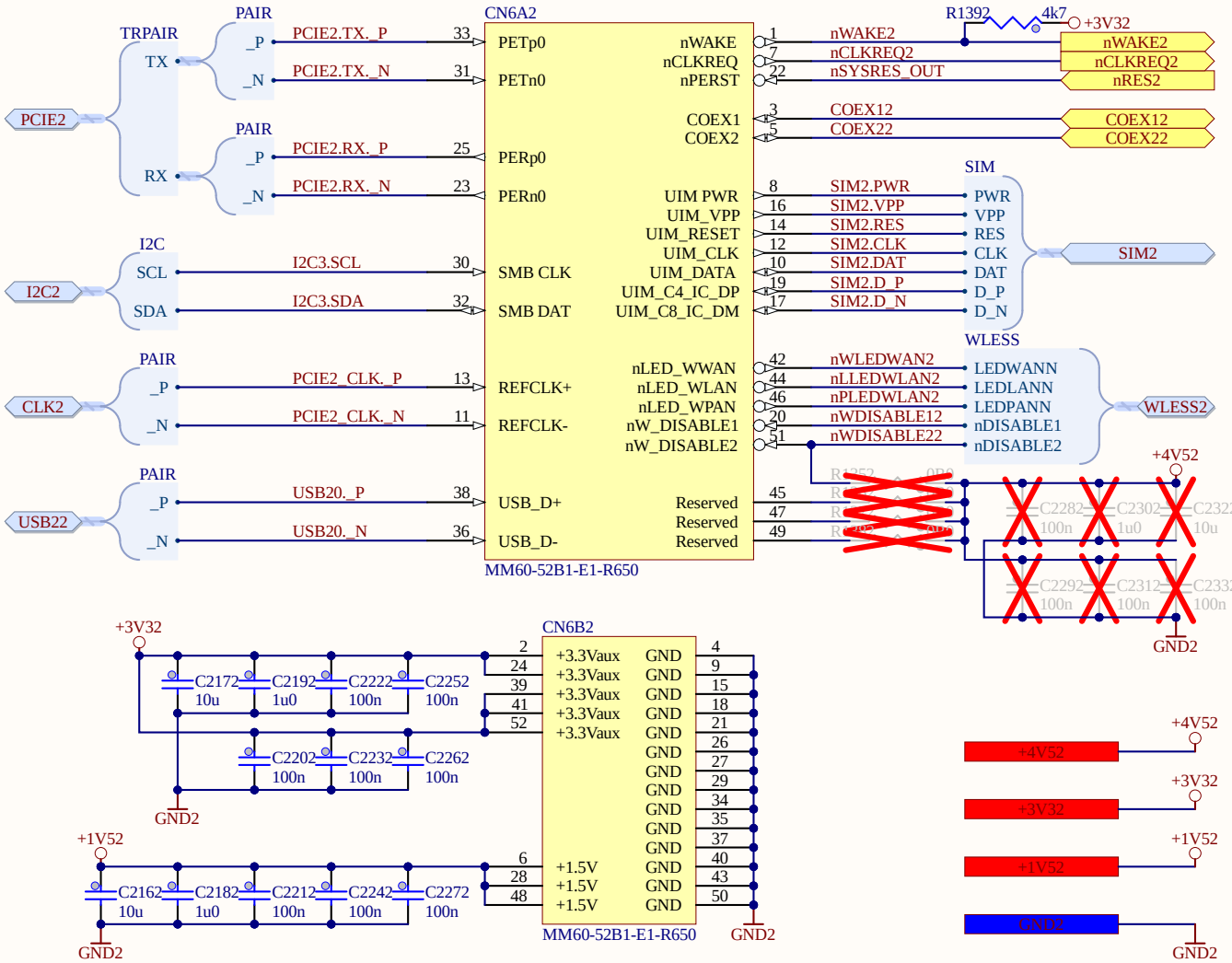
|---|---|---|---|---|
| Author: | Zbyněk Kos & Tomáš Rykl | Title: | Turris Omnia - LAN - Switch | CZ.NIC, z.s.p.o.<br>Milešovská 5<br>130 00 Praha 3 |
| Engineer: | Zbyněk Kos & Tomáš Rykl | | | |
| Drawn by: | Zbyněk Kos | | | |
| Checked by: | Tomáš Rykl | Document Number: | CZ11NIC200000-7 | Revision: 200000 |
| Approved by: | * | Sheet 7 of 20 | Date: * * | |
| http://www.nic.cz | | Name: Turris_Lite_LAN_SW.SchDoc | | |




| | | | | | |
|---|---|---|---|---|---|
| Author: | Zbyněk Kos & Tomáš Rykl | Title: | Turris Omnia - PCIE | | CZ.NIC, z.s.p.o.<br>Milešovská 5<br>130 00 Praha 3 |
| Engineer: | Zbyněk Kos & Tomáš Rykl | | | | |
| Drawn by: | Zbyněk Kos | | | | |
| Checked by: | Tomáš Rykl | Document Number: | CZ11NIC200000-8 | Revision: 200000 | |
| Approved by: | * | Sheet 8 of 20 | | Date: * * | |
| http://www.nic.cz | | Name: Turris_Lite_PCIE.SchDoc | | | |

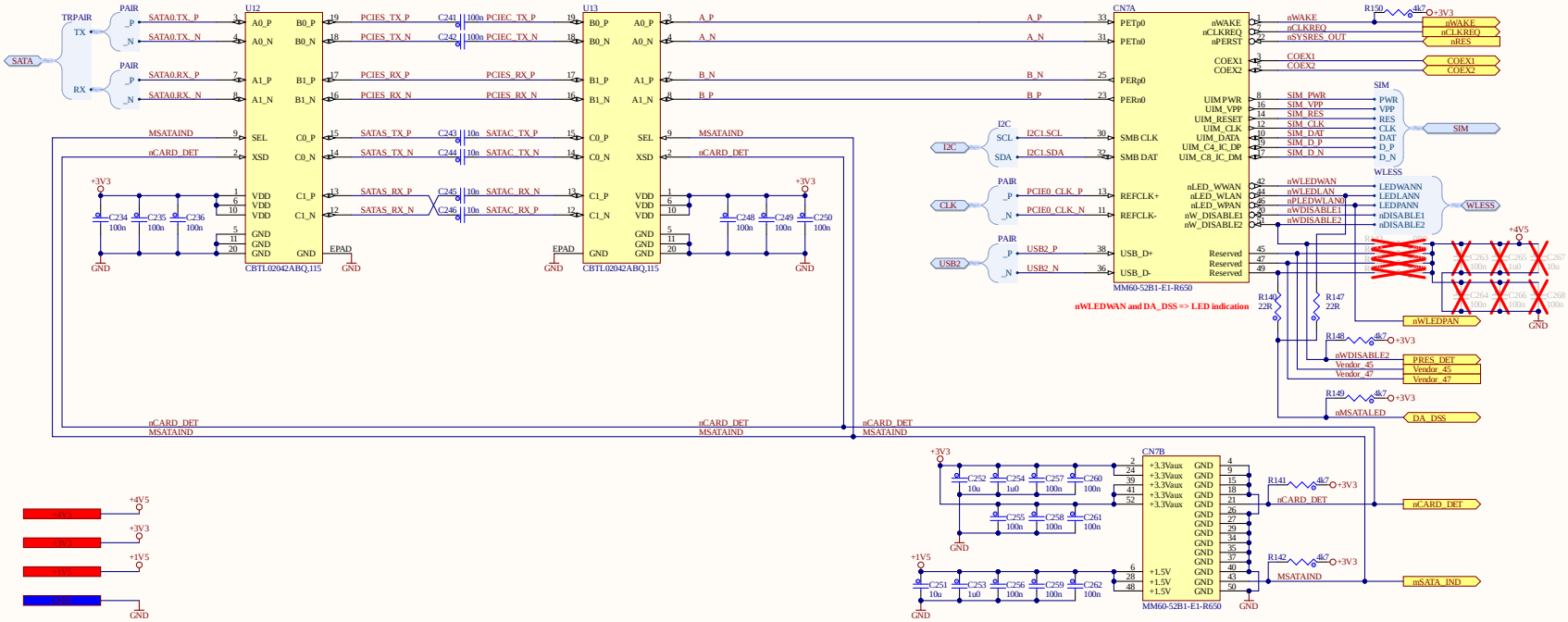



| | | | |
|---|---|---|---|
| Author: | Zbyněk Kos & Tomáš Rykl | Title: Turris Omnia - PCIE - Single Slot | CZ.NIC, z.s.p.o.<br>Milešovská 5<br>130 00 Praha 3 |
| Engineer: | Zbyněk Kos & Tomáš Rykl | | |
| Drawn by: | Zbyněk Kos | | |
| Checked by: | Tomáš Rykl | Document Number: CZ11NIC200000-9 | Revision: 200000 |
| Approved by: | * | Sheet 9 of 20 | Date: * * |
| http://www.nic.cz | | Name: Turris_Lite_PCIEB.SchDoc | |




| | | | |
|---|---|---|---|
| Author: | Zbyněk Kos & Tomáš Rykl | Title: | Turris Omnia - PCIE - Single Slot |
| Engineer: | Zbyněk Kos & Tomáš Rykl | | |
| Drawn by: | Zbyněk Kos | | |
| Checked by: | Tomáš Rykl | Document Number: CZ11NIC200000-9 | Revision: 200000 |
| Approved by: | * | Sheet 9 of 20 | Date: * * |
| http://www.nic.cz | | Name: Turris_Lite_PCIEB.SchDoc | |

CZ.NIC, z.s.p.o.
Milešovská 5
130 00 Praha 3




| | |
|---|---|
| Author: | Zbyněk Kos & Tomáš Rykl |
| Engineer: | Zbyněk Kos & Tomáš Rykl |
| Drawn by: | Zbyněk Kos |
| Checked by: | Tomáš Rykl |
| Approved by: | * |
| | http://www.nic.cz |

Title: Turris Omnia - mSATA/PCIE Slot

| | | | |
|---|---|---|---|
| Document Number: | CZ11NIC200000-10 | Revision: | 200000 |
| Sheet 10 | of 20 | Date: * | * |
| Name: Turris_Lite_mSATA.SchDoc | | | |

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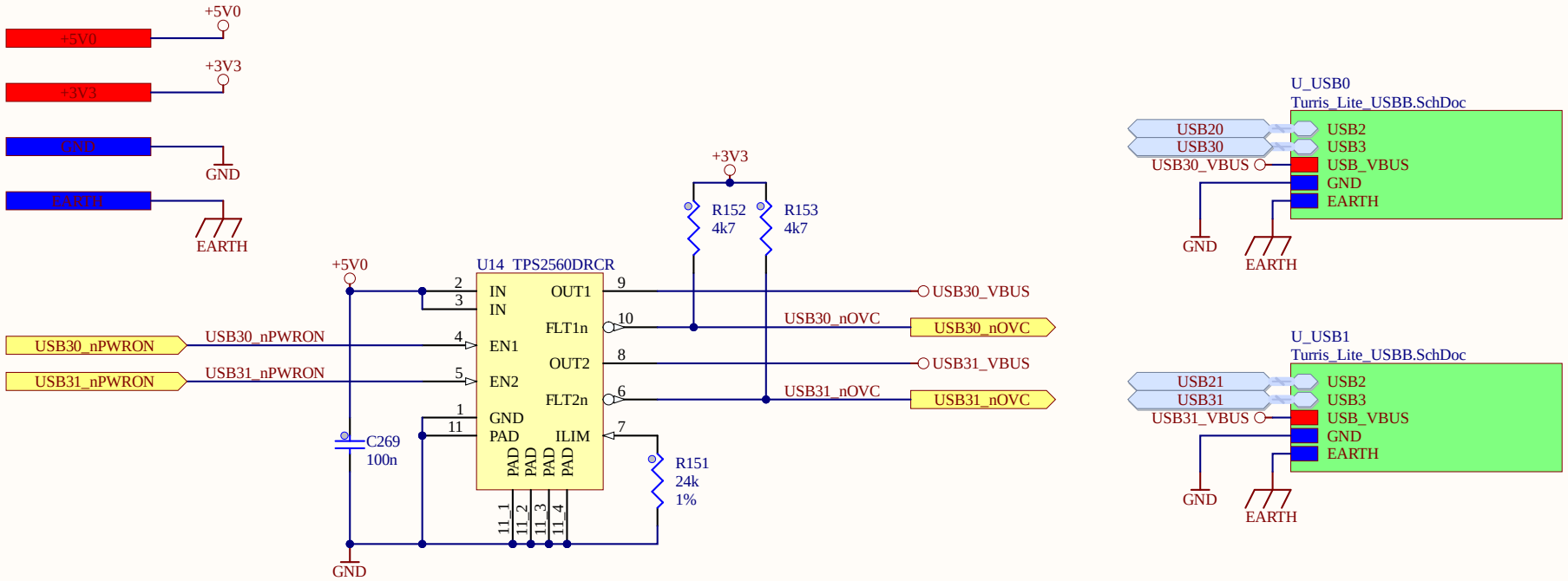
Milešovská 5
130 00 Praha 3

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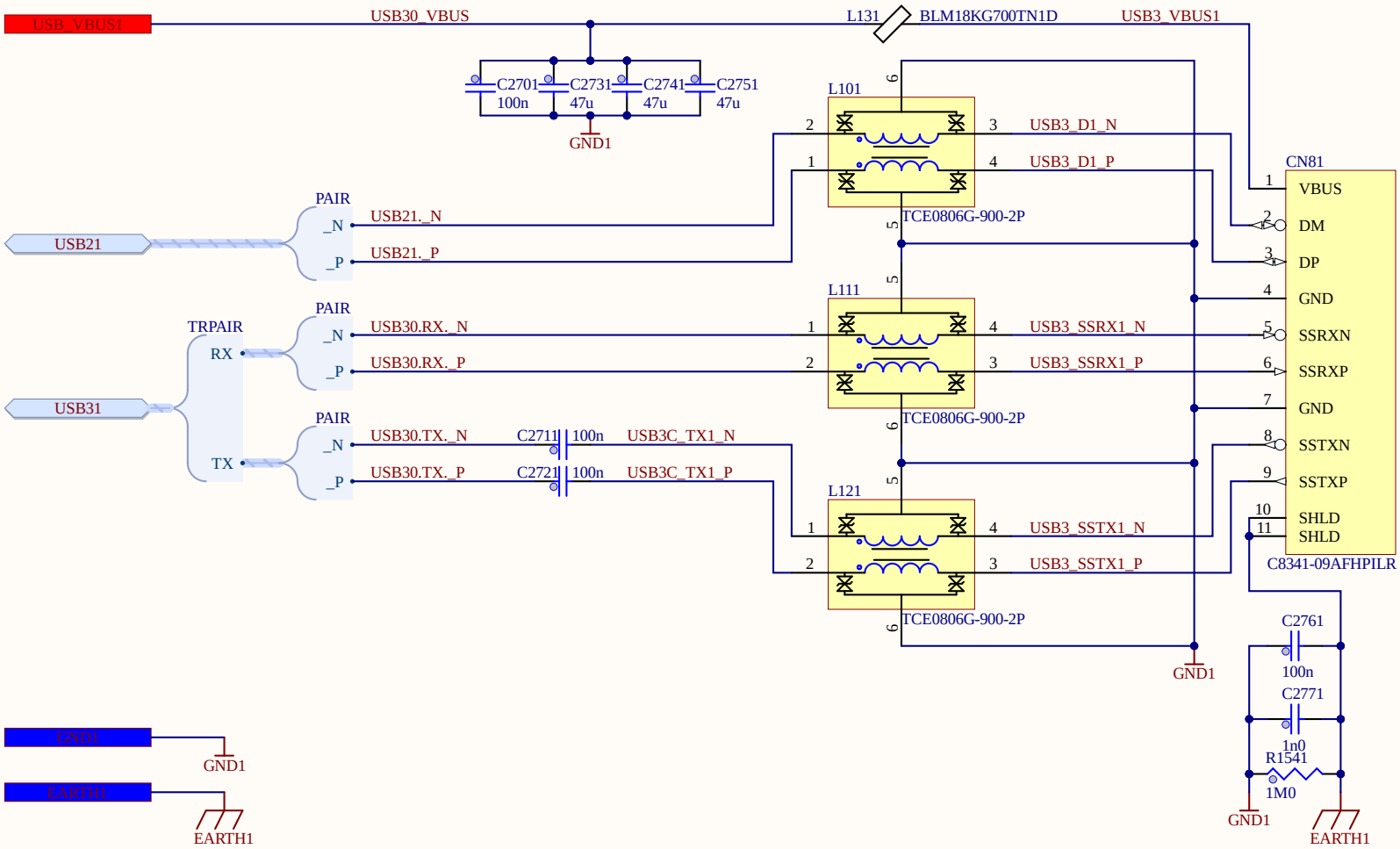
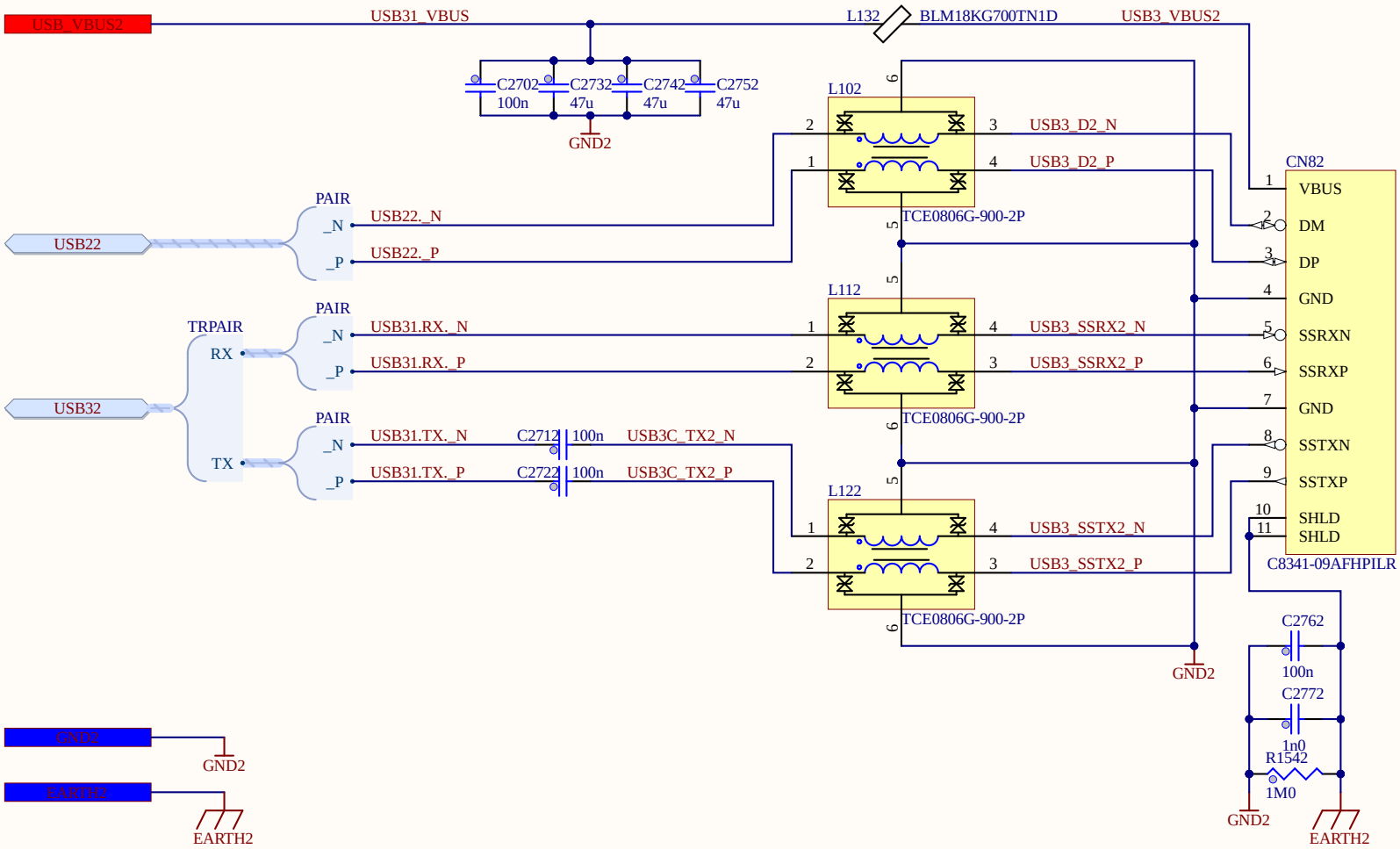
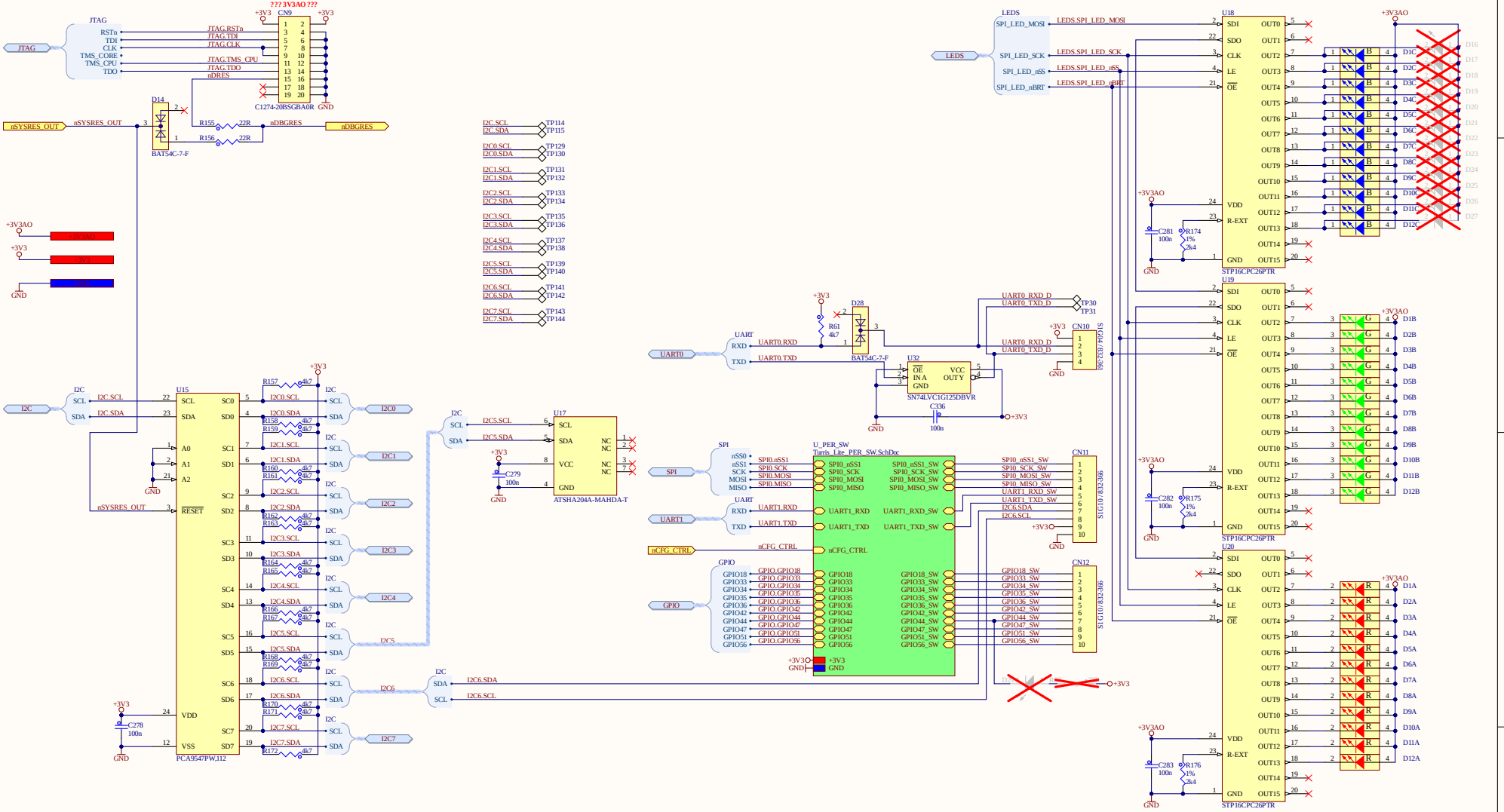
| Author: | Zbyněk Kos & Tomáš Rykl | Title: Turris Omnia - USB 3.0 | CZ.NIC, z.s.p.o.<br>Milešovská 5<br>130 00 Praha 3 |
| Engineer: | Zbyněk Kos & Tomáš Rykl | | |
| Drawn by: | Zbyněk Kos | | |
| Checked by: | Tomáš Rykl | Document Number: CZ11NIC200000-11 | Revision: 200000 |
| Approved by: | * | Sheet 11 of 20 | Date: * * |
| http://www.nic.cz | | Name: Turris_Lite_USB.SchDoc | |



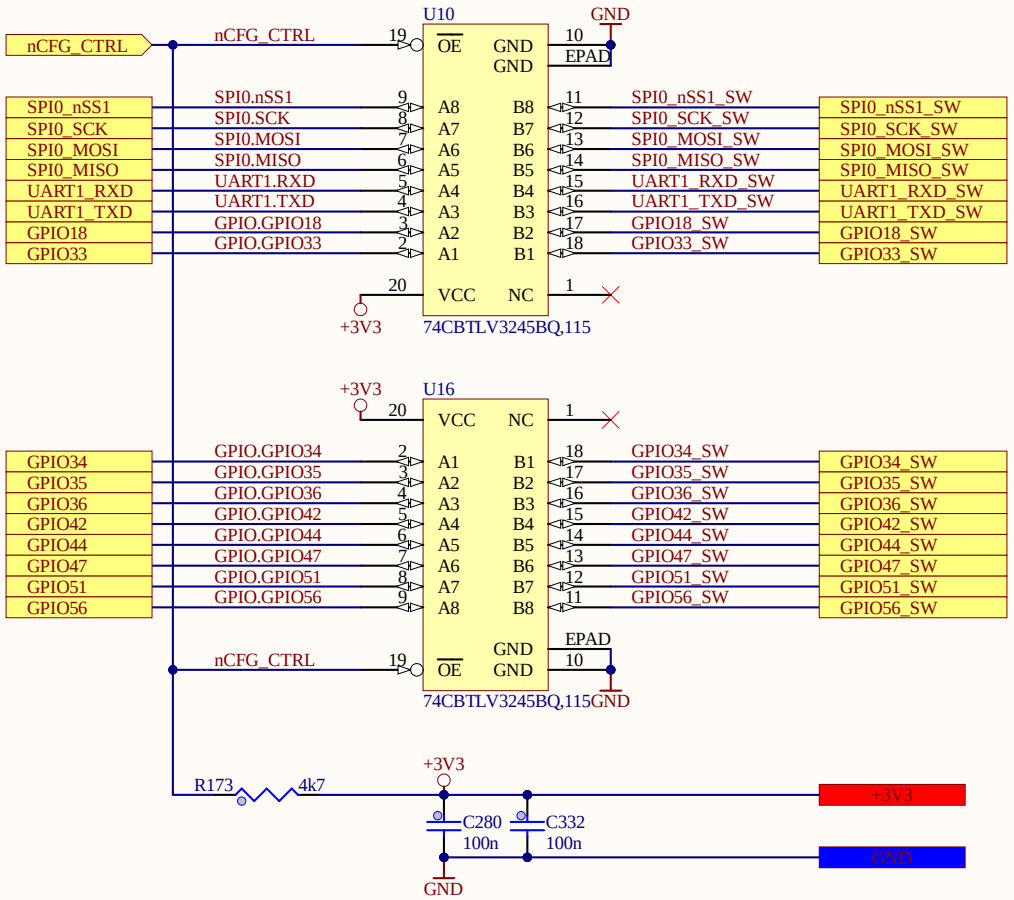

| | | | |
|---|---|---|---|
| Author: | Zbyněk Kos & Tomáš Rykl | Title: Turris Omnia - USB 3.0 - Single Slot | CZ.NIC, z.s.p.o.<br>Milešovská 5<br>130 00 Praha 3 |
| Engineer: | Zbyněk Kos & Tomáš Rykl | | |
| Drawn by: | Zbyněk Kos | | |
| Checked by: | Tomáš Rykl | Document Number: CZ11NIC200000-12 | Revision: 200000 |
| Approved by: | * | Sheet 12 of 20 | Date: * * |
| http://www.nic.cz | | Name: Turris_Lite_USBB.SchDoc | |




| | | | |
|---|---|---|---|
| Author: | Zbyněk Kos & Tomáš Rykl | Title: Turris Omnia - USB 3.0 - Single Slot | CZ.NIC, z.s.p.o.<br>Milešovská 5<br>130 00 Praha 3 |
| Engineer: | Zbyněk Kos & Tomáš Rykl | | |
| Drawn by: | Zbyněk Kos | | |
| Checked by: | Tomáš Rykl | Document Number: CZ11NIC200000-12 | Revision: 200000 |
| Approved by: | * | Sheet 12 of 20 | Date: * * |
| http://www.nic.cz | | Name: Turris_Lite_USBB.SchDoc | |




| | | | |
|---|---|---|---|
| Author: | Zbyněk Kos & Tomáš Rykl | Title: | Turris Omnia - Peripherals & JTAG |
| Engineer: | Zbyněk Kos & Tomáš Rykl | | CZ.NIC, z.s.p.o. Milešovská 5 130 00 Praha 3 |
| Drawn by: | Zbyněk Kos | | |
| Checked by: | Tomáš Rykl | Document Number: | CZ11NIC200000-13 Revision: 200000 |
| Approved by: | * | Sheet 13 of 20 | Date: * * |
| | http://www.nic.cz | Name: Turris_Lite_PER.SchDoc | |

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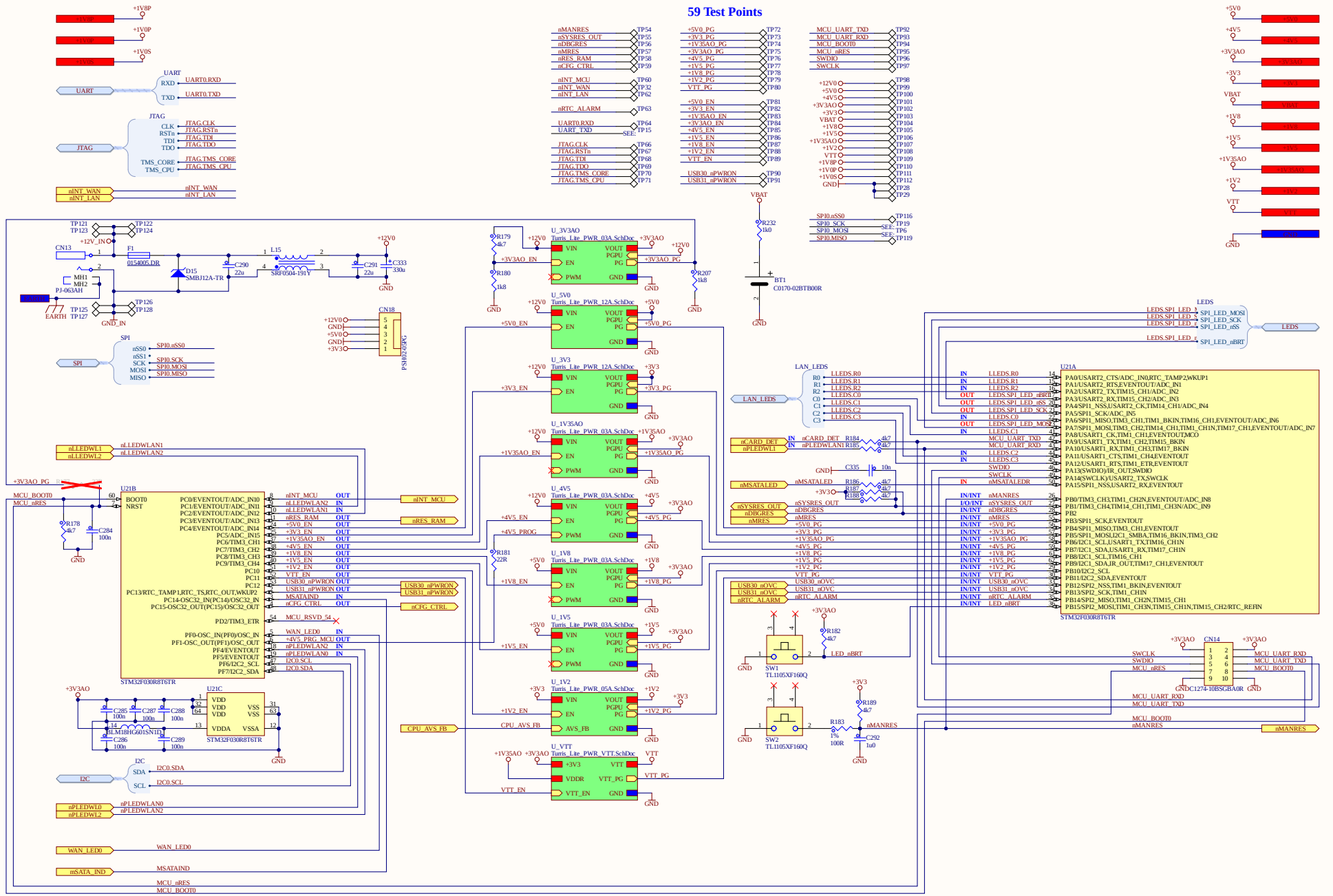
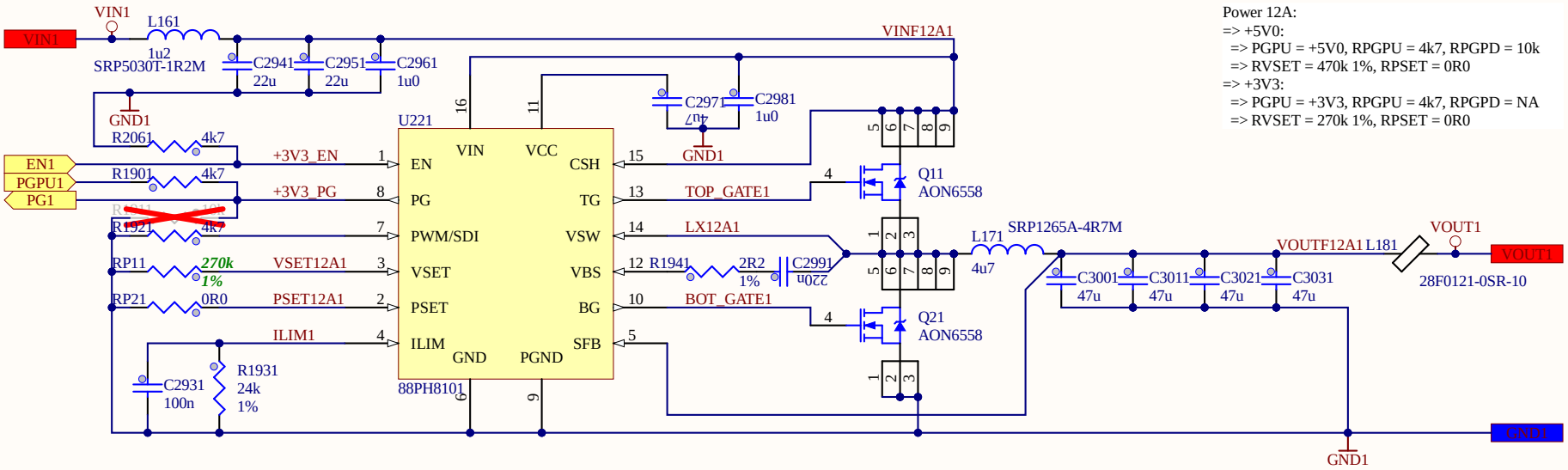
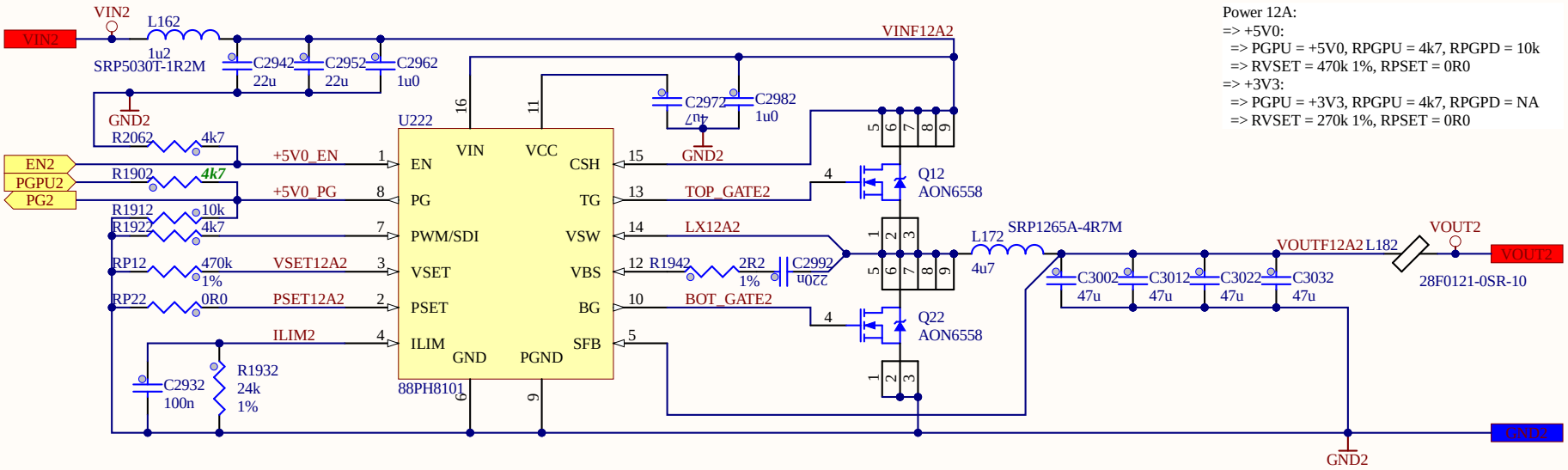
| Author: | Zbyněk Kos & Tomáš Rykl | Title: Turris Omnia - PER - SW | | CZ.NIC, z.s.p.o.<br>Milešovská 5<br>130 00 Praha 3 |
| Engineer: | Zbyněk Kos & Tomáš Rykl | | | |
| Drawn by: | Zbyněk Kos | | | |
| Checked by: | Tomáš Rykl | Document Number: CZ11NIC200000-14 | Revision: 200000 | |
| Approved by: | * | Sheet 14 of 20 | Date: * * | |
| http://www.nic.cz | | Name: Turris_Lite_PER_SW.SchDoc | | |

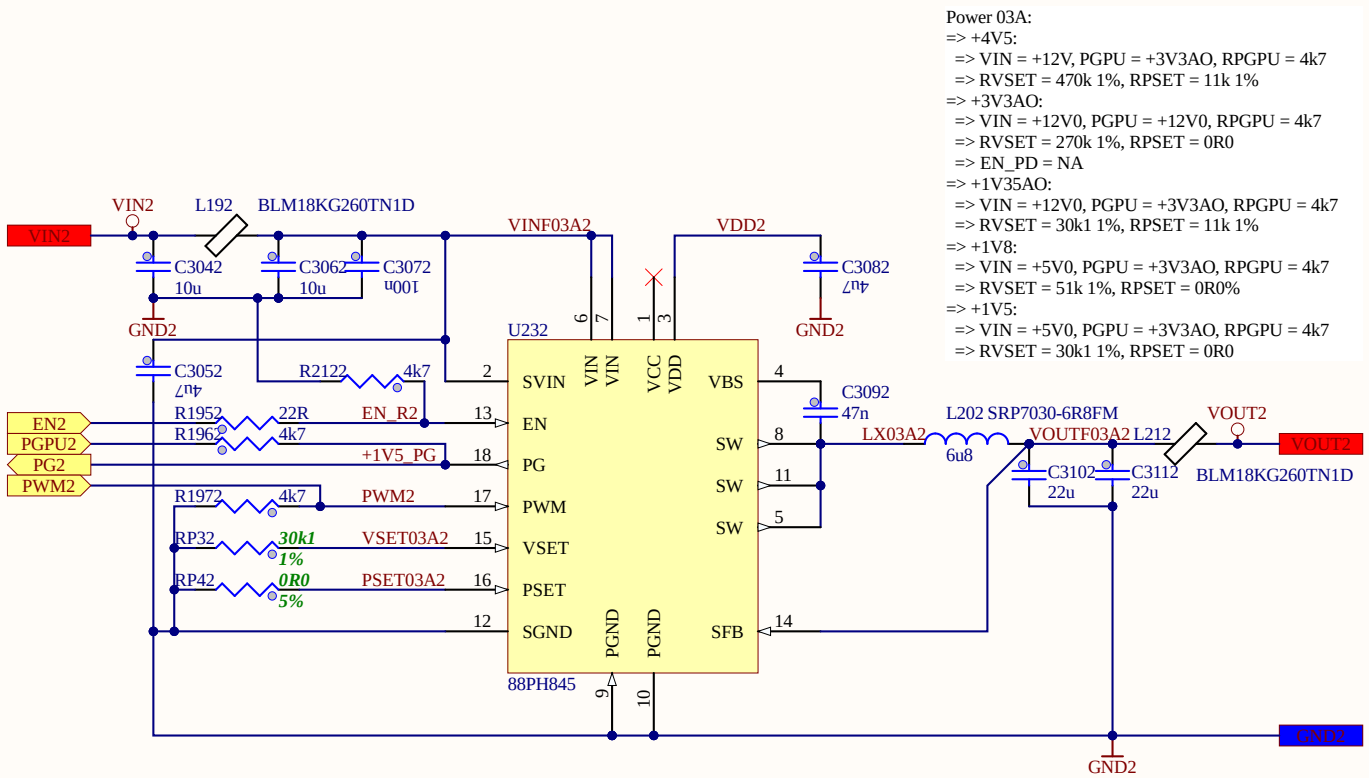

59 Test Points
+1V8P
+1V0P
+1V0S
UART
UART0.RXD
UART0.TXD
JTAG
JTAG.CLK
JTAG.RSTn
JTAG.TDI
JTAG.TDO
JTAG.TMS_CORE
JTAG.TMS_CPU
nINT_WAN
nINT_LAN
SPI
SPI0.nSS0
SPI0.SCK
SPI0.MOSI
SPI0.MISO
I2C
I2C0.SDA
I2C0.SCL
LEDS
SPI_LED_MOSI
SPI_LED_SCK
SPI_LED_nSS
SPI_LED_nBRT
LAN_LEDS
nCARD_DET
nPLEDWL1
nMSATALED
nSYSRES_OUT
nDBGRES
nMRES
USB30_nOVC
USB31_nOVC
nRTC_ALARM
LED_nBRT
nMANRES
nINT_MCU
nRES_RAM
USB30_nPWRON
USB31_nPWRON
nCFG_CTRL
CPU_AVS_FB
WAN_LED0
nPLEDWL0
nPLEDWL2
nPLEDWLAN0
nPLEDWLAN2
mSATA_IND
MSATAIND
MCU_nRES
MCU_BOOT0
MCU_UART_RXD
MCU_UART_TXD
SWCLK
SWDIO
U21A
U21B
U21C
STM32F030R8T6TR
U_3V3AO
U_5V0
U_3V3
U_1V35AO
U_4V5
U_1V8
U_1V5
U_1V2
U_VTT
Turris_Lite_PWR_03A.SchDoc
Turris_Lite_PWR_12A.SchDoc
Turris_Lite_PWR_05A.SchDoc
Turris_Lite_PWR_VTT.SchDoc
VBAT
BT1
C0170-02BTB00R
CN13
CN14
CN18
F1
0154005.DR
D15
SMBJ12A-TR
L15
SRF0504-191Y
SW1
SW2
TL1105XF160Q
EARTH
GND_IN
PJ-063AH
PSH02-04FC
C1274-10BSGBA0R
SLMH8H1608SN1D
Author: Zbyněk Kos & Tomáš Rykl
Engineer: Zbyněk Kos & Tomáš Rykl
Drawn by: Zbyněk Kos
Checked by: Tomáš Rykl
Approved by: *
http://www.nic.cz
Title: Turris Omnia - Power & Test
Document Number: CZ11NIC200000-15
Revision: 200000
Sheet 15 of 20
Date: * *
Name: Turris_Lite_PWR.SchDoc
CZ.NIC, z.s.p.o.
Milešovská 5
130 00 Praha 3



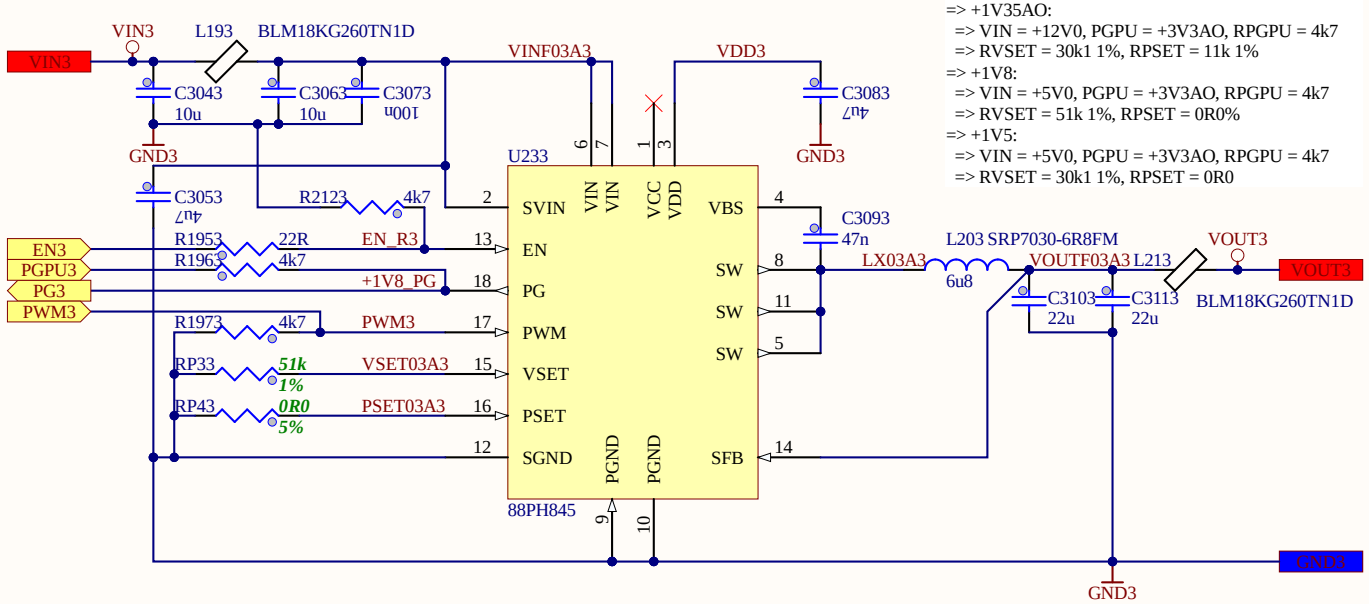

| | | | |
|---|---|---|---|
| Author: | Zbyněk Kos & Tomáš Rykl | Title: Turris Omnia - Power - 12A Regulator | CZ.NIC, z.s.p.o.<br>Milešovská 5<br>130 00 Praha 3 |
| Engineer: | Zbyněk Kos & Tomáš Rykl | | |
| Drawn by: | Zbyněk Kos | | |
| Checked by: | Tomáš Rykl | Document Number: CZ11NIC200000-16 | Revision: 200000 |
| Approved by: | * | Sheet 16 of 20 | Date: * * |
| http://www.nic.cz | | Name: Turris_Lite_PWR_12A.SchDoc | |




| | | | |
|---|---|---|---|
| Author: | Zbyněk Kos & Tomáš Rykl | Title: Turris Omnia - Power - 12A Regulator | CZ.NIC, z.s.p.o. Milešovská 5 130 00 Praha 3 |
| Engineer: | Zbyněk Kos & Tomáš Rykl | | |
| Drawn by: | Zbyněk Kos | | |
| Checked by: | Tomáš Rykl | Document Number: CZ11NIC200000-16 | Revision: 200000 |
| Approved by: | * | Sheet 16 of 20 | Date: * * |
| http://www.nic.cz | | Name: Turris_Lite_PWR_12A.SchDoc | |



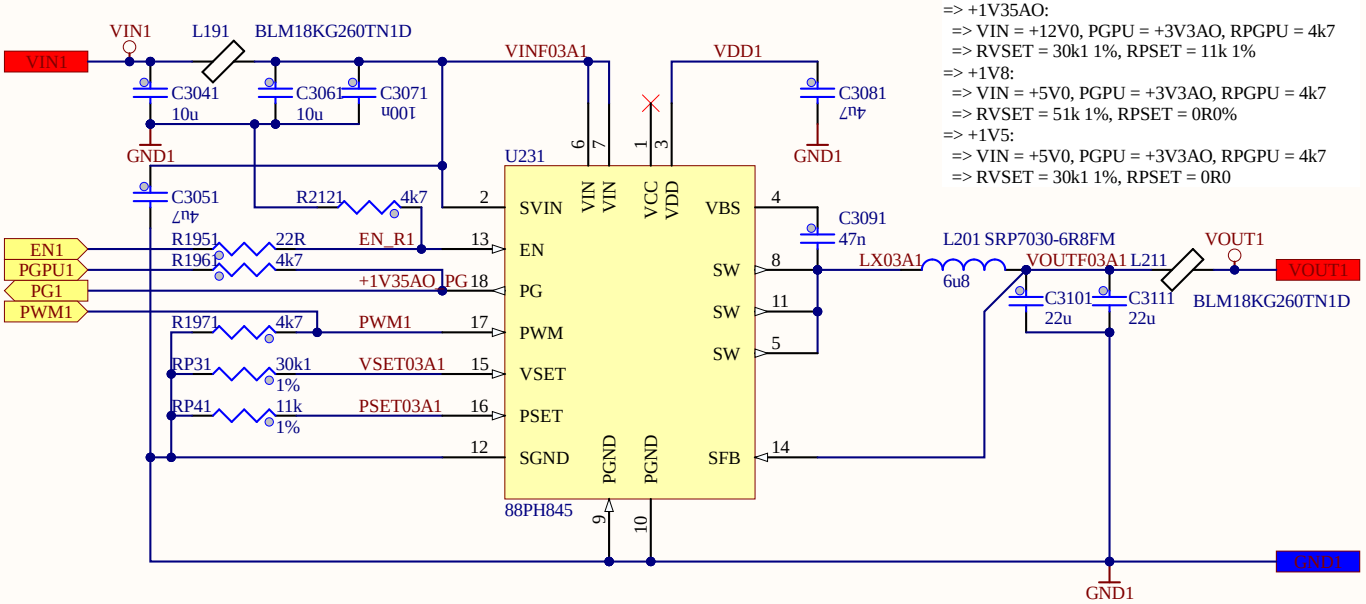

| | | | | |
|---|---|---|---|---|
| Author: | Zbyněk Kos & Tomáš Rykl | Title: Turris Omnia - Power - 3A Regulator | | CZ.NIC, z.s.p.o.<br>Milešovská 5<br>130 00 Praha 3 |
| Engineer: | Zbyněk Kos & Tomáš Rykl | | | |
| Drawn by: | Zbyněk Kos | | | |
| Checked by: | Tomáš Rykl | Document Number: CZ11NIC200000-17 | Revision: 200000 | |
| Approved by: | * | Sheet 17 of 20 | Date: * * | |
| http://www.nic.cz | | Name: Turris_Lite_PWR_03A.SchDoc | | |




Power 03A:
=> +4V5:
=> VIN = +12V, PGPU = +3V3AO, RPGPU = 4k7
=> RVSET = 470k 1%, RPSET = 11k 1%
=> +3V3AO:
=> VIN = +12V0, PGPU = +12V0, RPGPU = 4k7
=> RVSET = 270k 1%, RPSET = 0R0
=> EN_PD = NA
=> +1V35AO:
=> VIN = +12V0, PGPU = +3V3AO, RPGPU = 4k7
=> RVSET = 30k1 1%, RPSET = 11k 1%
=> +1V8:
=> VIN = +5V0, PGPU = +3V3AO, RPGPU = 4k7
=> RVSET = 51k 1%, RPSET = 0R0%
=> +1V5:
=> VIN = +5V0, PGPU = +3V3AO, RPGPU = 4k7
=> RVSET = 30k1 1%, RPSET = 0R0

| | | | |
|---|---|---|---|
| Author: | Zbyněk Kos & Tomáš Rykl | Title: Turris Omnia - Power - 3A Regulator | CZ.NIC, z.s.p.o.<br>Milešovská 5<br>130 00 Praha 3 |
| Engineer: | Zbyněk Kos & Tomáš Rykl | | |
| Drawn by: | Zbyněk Kos | | |
| Checked by: | Tomáš Rykl | Document Number: CZ11NIC200000-17 | Revision: 200000 |
| Approved by: | * | Sheet 17 of 20 | Date: * * |
| http://www.nic.cz | | Name: Turris_Lite_PWR_03A.SchDoc | |

Power 03A:
=> +4V5:
=> VIN = +12V, PGPU = +3V3AO, RPGPU = 4k7
=> RVSET = 470k 1%, RPSET = 11k 1%
=> +3V3AO:
=> VIN = +12V0, PGPU = +12V0, RPGPU = 4k7
=> RVSET = 270k 1%, RPSET = 0R0
=> EN_PD = NA
=> +1V35AO:
=> VIN = +12V0, PGPU = +3V3AO, RPGPU = 4k7
=> RVSET = 30k1 1%, RPSET = 11k 1%
=> +1V8:
=> VIN = +5V0, PGPU = +3V3AO, RPGPU = 4k7
=> RVSET = 51k 1%, RPSET = 0R0%
=> +1V5:
=> VIN = +5V0, PGPU = +3V3AO, RPGPU = 4k7
=> RVSET = 30k1 1%, RPSET = 0R0

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| Engineer: | Zbyněk Kos & Tomáš Rykl | | |
| Drawn by: | Zbyněk Kos | | |
| Checked by: | Tomáš Rykl | Document Number: CZ11NIC200000-17 | Revision: 200000 |
| Approved by: | * | Sheet 17 of 20 | Date: * * |
| http://www.nic.cz | | Name: Turris_Lite_PWR_03A.SchDoc | |

Power 03A:
=> +4V5:
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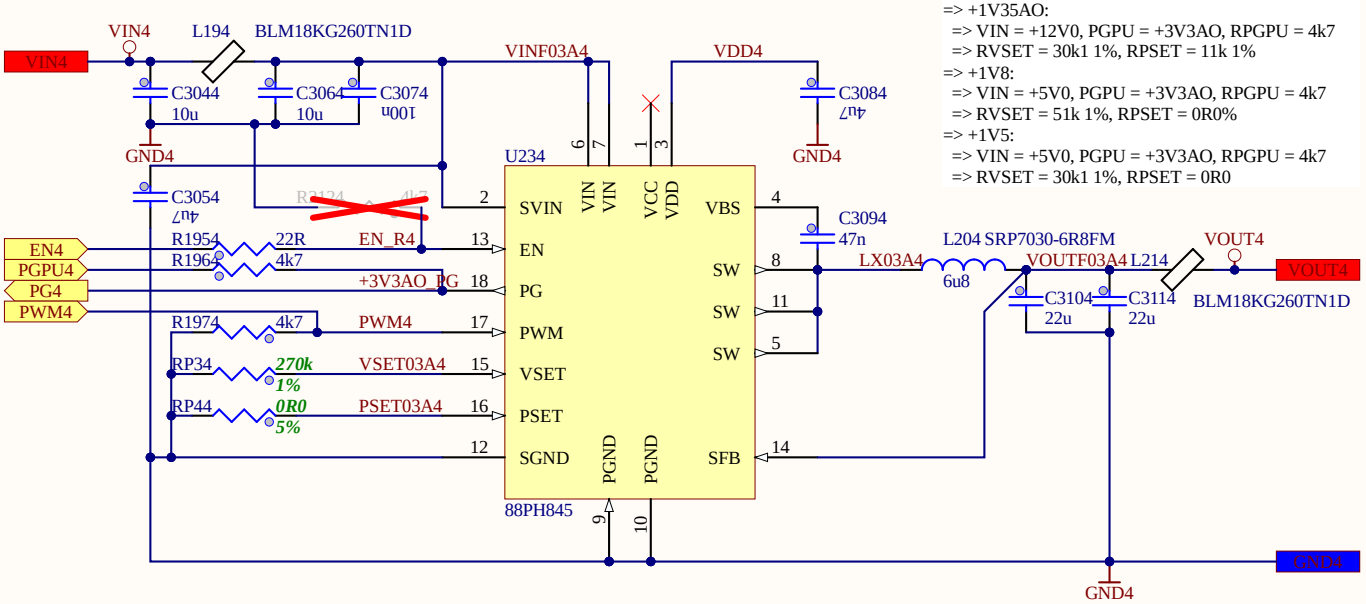
=> +1V8:
=> VIN = +5V0, PGPU = +3V3AO, RPGPU = 4k7
=> RVSET = 51k 1%, RPSET = 0R0%
=> +1V5:
=> VIN = +5V0, PGPU = +3V3AO, RPGPU = 4k7
=> RVSET = 30k1 1%, RPSET = 0R0

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|---|---|---|---|
| Author: | Zbyněk Kos & Tomáš Rykl | Title: Turris Omnia - Power - 3A Regulator | CZ.NIC, z.s.p.o.<br>Milešovská 5<br>130 00 Praha 3 |
| Engineer: | Zbyněk Kos & Tomáš Rykl | | |
| Drawn by: | Zbyněk Kos | | |
| Checked by: | Tomáš Rykl | Document Number: CZ11NIC200000-17 | Revision: 200000 |
| Approved by: | * | Sheet 17 of 20 | Date: * * |
| http://www.nic.cz | | Name: Turris_Lite_PWR_03A.SchDoc | |

Power 03A:
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 => RVSET = 270k 1%, RPSET = 0R0
 => EN_PD = NA
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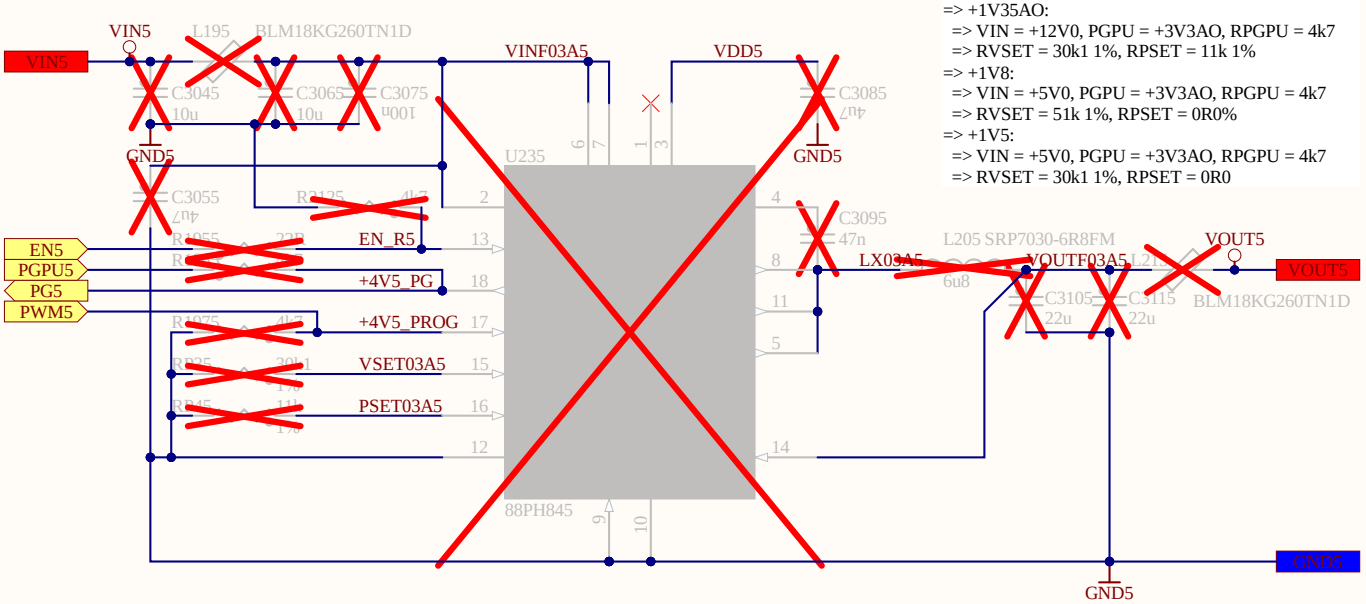
 => RVSET = 30k1 1%, RPSET = 11k 1%
=> +1V8:
 => VIN = +5V0, PGPU = +3V3AO, RPGPU = 4k7
 => RVSET = 51k 1%, RPSET = 0R0%
=> +1V5:
 => VIN = +5V0, PGPU = +3V3AO, RPGPU = 4k7
 => RVSET = 30k1 1%, RPSET = 0R0

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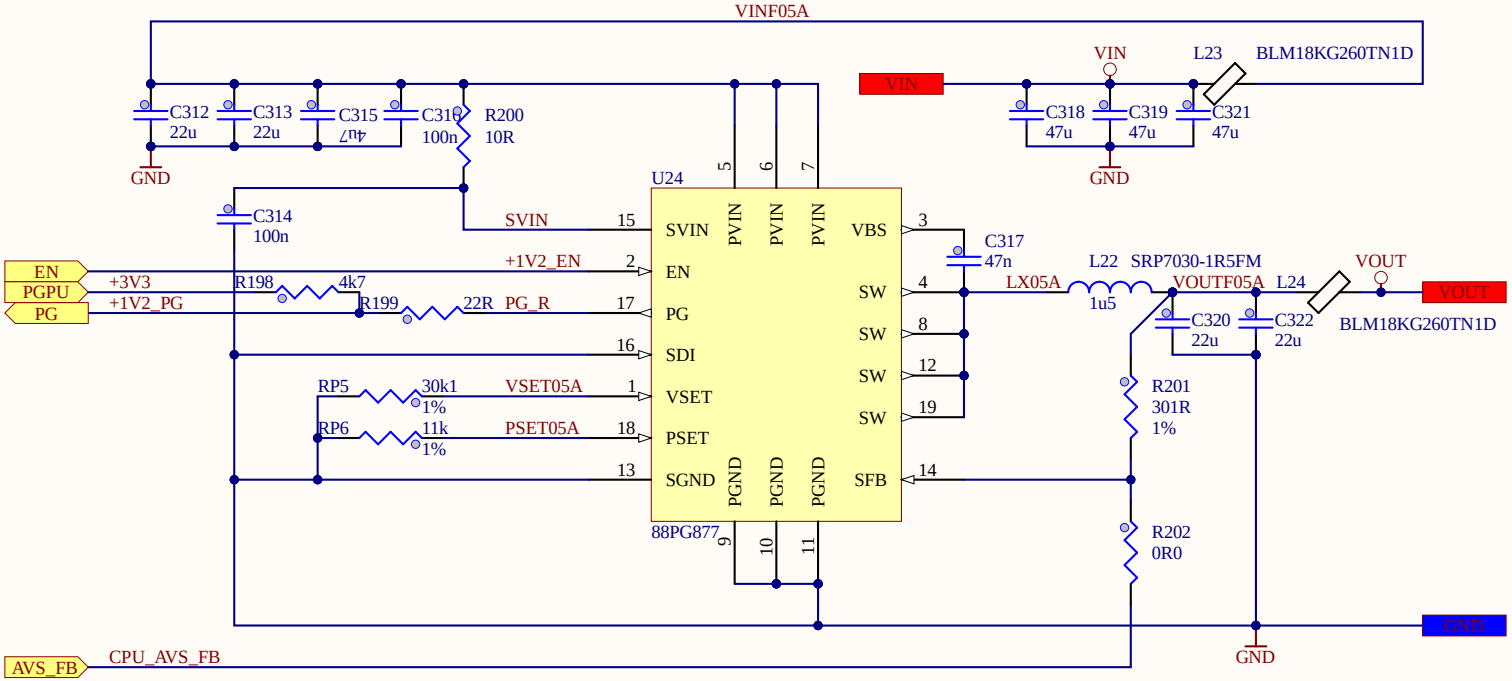
|---|---|---|---|
| Author: | Zbyněk Kos & Tomáš Rykl | Title: Turris Omnia - Power - 3A Regulator | CZ.NIC, z.s.p.o.<br>Milešovská 5<br>130 00 Praha 3 |
| Engineer: | Zbyněk Kos & Tomáš Rykl | | |
| Drawn by: | Zbyněk Kos | | |
| Checked by: | Tomáš Rykl | Document Number: CZ11NIC200000-17 | Revision: 200000 |
| Approved by: | * | Sheet 17 of 20 | Date: * * |
| http://www.nic.cz | | Name: Turris_Lite_PWR_03A.SchDoc | |

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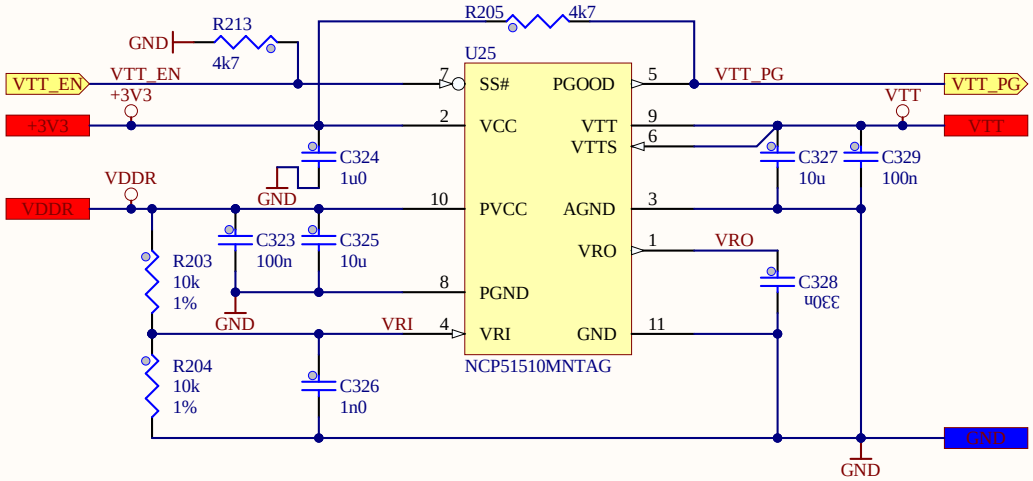
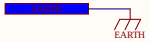
|---|---|---|---|---|
| Author: | Zbyněk Kos & Tomáš Rykl | Title: Turris Omnia - Power - 5A Regulator | | CZ.NIC, z.s.p.o.<br>Milešovská 5<br>130 00 Praha 3 |
| Engineer: | Zbyněk Kos & Tomáš Rykl | | | |
| Drawn by: | Zbyněk Kos | | | |
| Checked by: | Tomáš Rykl | Document Number: CZ11NIC200000-18 | Revision: 200000 | |
| Approved by: | * | Sheet 18 of 20 | Date: * * | |
| http://www.nic.cz | | Name: Turris_Lite_PWR_05A.SchDoc | | |




| | | | | | |
|---|---|---|---|---|---|
| Author: | Zbyněk Kos & Tomáš Rykl | Title: | Turris Omnia - Power - VTT Regulator | | CZ.NIC, z.s.p.o.<br>Milešovská 5<br>130 00 Praha 3 |
| Engineer: | Zbyněk Kos & Tomáš Rykl | | | | |
| Drawn by: | Zbyněk Kos | | | | |
| Checked by: | Tomáš Rykl | Document Number: | CZ11NIC200000-19 | Revision: 200000 | |
| Approved by: | * | Sheet 19 of 20 | | Date: * * | |
| http://www.nic.cz | | Name: Turris_Lite_PWR_VTT.SchDoc | | | |

Mechanical components
- mounting holes
- screws
- box elements
- distribution box
- distribution box content (manual etc.)
- fiducial marks???
- etc.

Connect all mount holes to EARTH



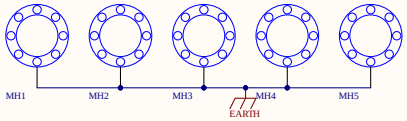

BV1 BV2 BV3 BV4 BV5 BV6 BV7 BV8 BV9 BV10 BV11 BV12

LED Optic

SLP3-250-100-F

CN15 CN16 CN17

U.FL/RSMA Pigtail




FIDU FID1 FID2 FID3 FID4 FID5 FID6 FID7 FID8 FID9 FID10 FID11 FID12 FID13 FID14 FID15 FID16

U30 U31

Logo Omnia 10mm

**Screws**

**Heatsinks**

**Thermal tapes**

**Thermal polymers**

**AL Box parts**

**Battery**

**Antennas**

**Paper Box**

**Manual**

**Power Supply**

**ETH cables - optional**

**WiFi cards - optional**

**Diplexers - optional**

**SFP module - optional**

**Jumper**

| | | | |
|---|---|---|---|
| Author: | Zbyněk Kos & Tomáš Rykl | Title: | Turris Omnia - Mech. & SW & Accs |
| Engineer: | Zbyněk Kos & Tomáš Rykl | | |
| Drawn by: | Zbyněk Kos | | |
| Checked by: | Tomáš Rykl | Document Number: | CZ11NIC200000-20 Revision: 200000 |
| Approved by: | * | Sheet 20 of 20 | Date: * * |
| http://www.nic.cz | | Name: Turris_Lite_MECH.SchDoc | |

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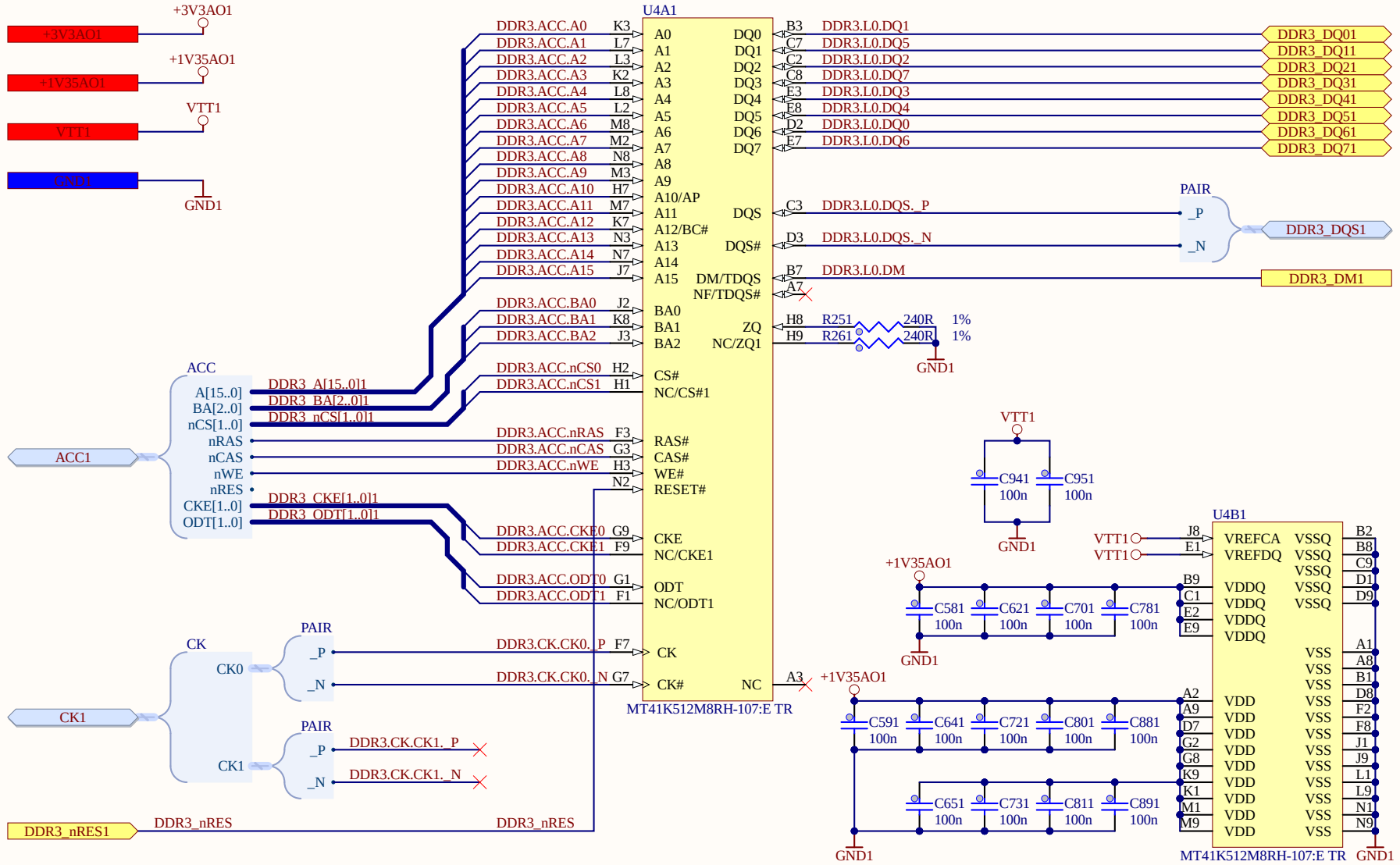
130 00 Praha 3




| | | | |
|---|---|---|---|
| Author: | Zbyněk Kos & Tomáš Rykl | Title: | Turris Omnia - RAM 8b |
| Engineer: | Zbyněk Kos & Tomáš Rykl | | |
| Drawn by: | Zbyněk Kos | | |
| Checked by: | Tomáš Rykl | Document Number: CZ11NIC200000-21 | Revision: 200000 |
| Approved by: | * | Sheet 21 of 21 | Date: * * |
| http://www.nic.cz | | Name: Turris_Lite_RAM_8b.SchDoc | |

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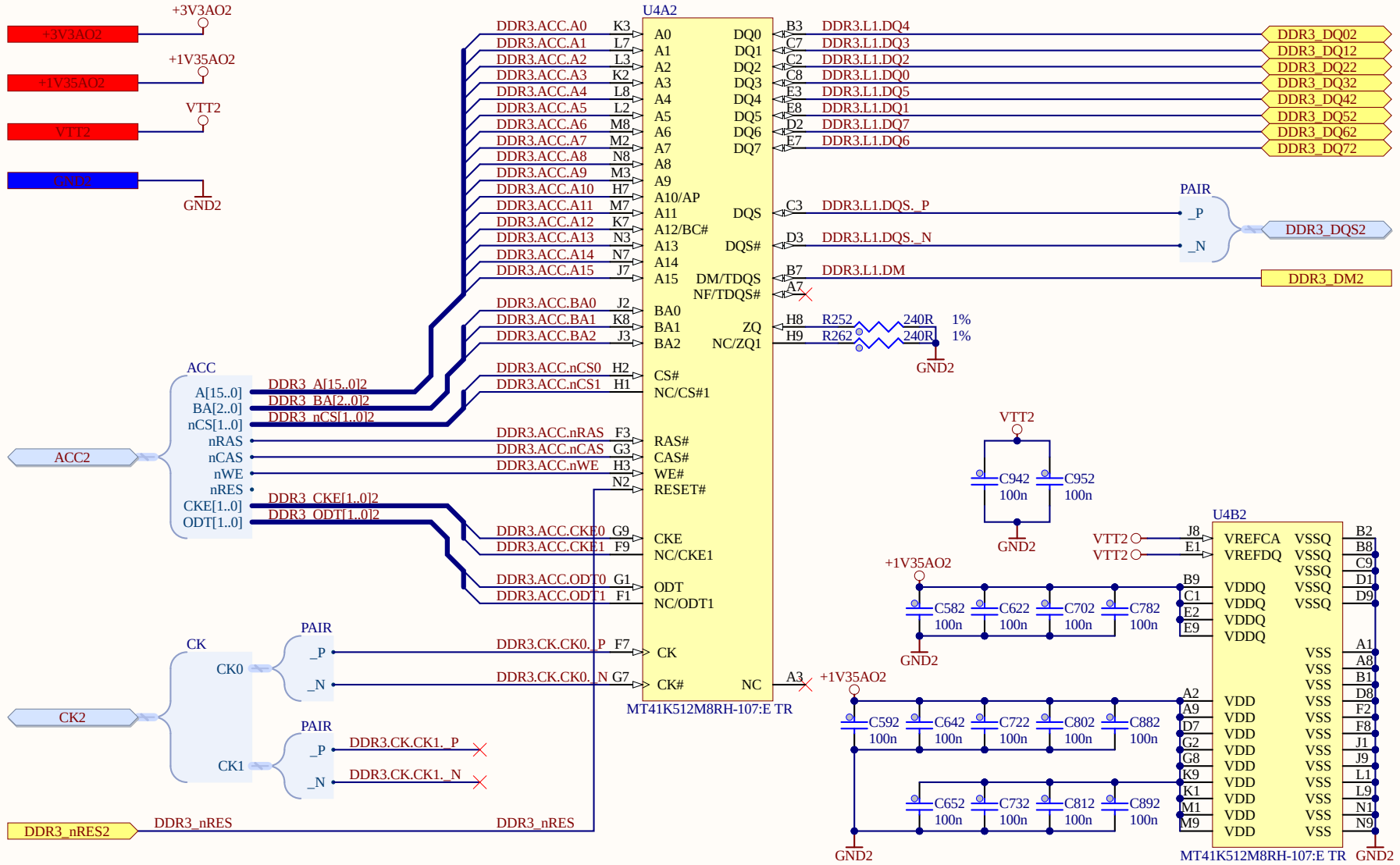
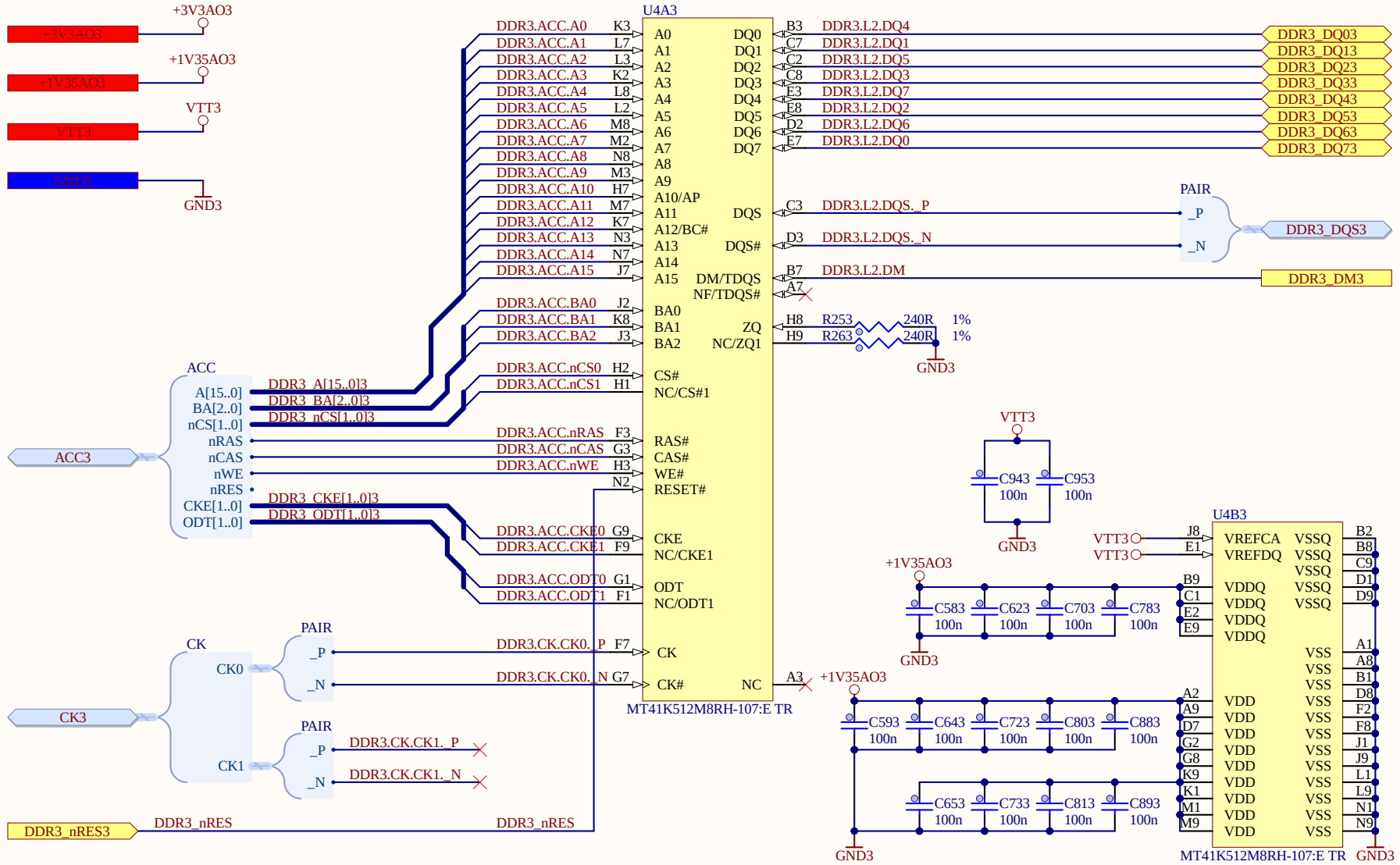
130 00 Praha 3




| | | | |
|---|---|---|---|
| Author: | Zbyněk Kos & Tomáš Rykl | Title: Turris Omnia - RAM 8b | CZ.NIC, z.s.p.o.<br>Milešovská 5<br>130 00 Praha 3 |
| Engineer: | Zbyněk Kos & Tomáš Rykl | | |
| Drawn by: | Zbyněk Kos | | |
| Checked by: | Tomáš Rykl | Document Number: CZ11NIC200000-21 | Revision: 200000 |
| Approved by: | * | Sheet 21 of 21 | Date: * * |
| http://www.nic.cz | | Name: Turris_Lite_RAM_8b.SchDoc | |




| | | | |
|---|---|---|---|
| Author: | Zbyněk Kos & Tomáš Rykl | Title: | Turris Omnia - RAM 8b |
| Engineer: | Zbyněk Kos & Tomáš Rykl | | CZ.NIC, z.s.p.o. Milešovská 5 130 00 Praha 3 |
| Drawn by: | Zbyněk Kos | | |
| Checked by: | Tomáš Rykl | Document Number: | CZ11NIC200000-21 Revision: 200000 |
| Approved by: | * | Sheet 21 of 21 | Date: * * |
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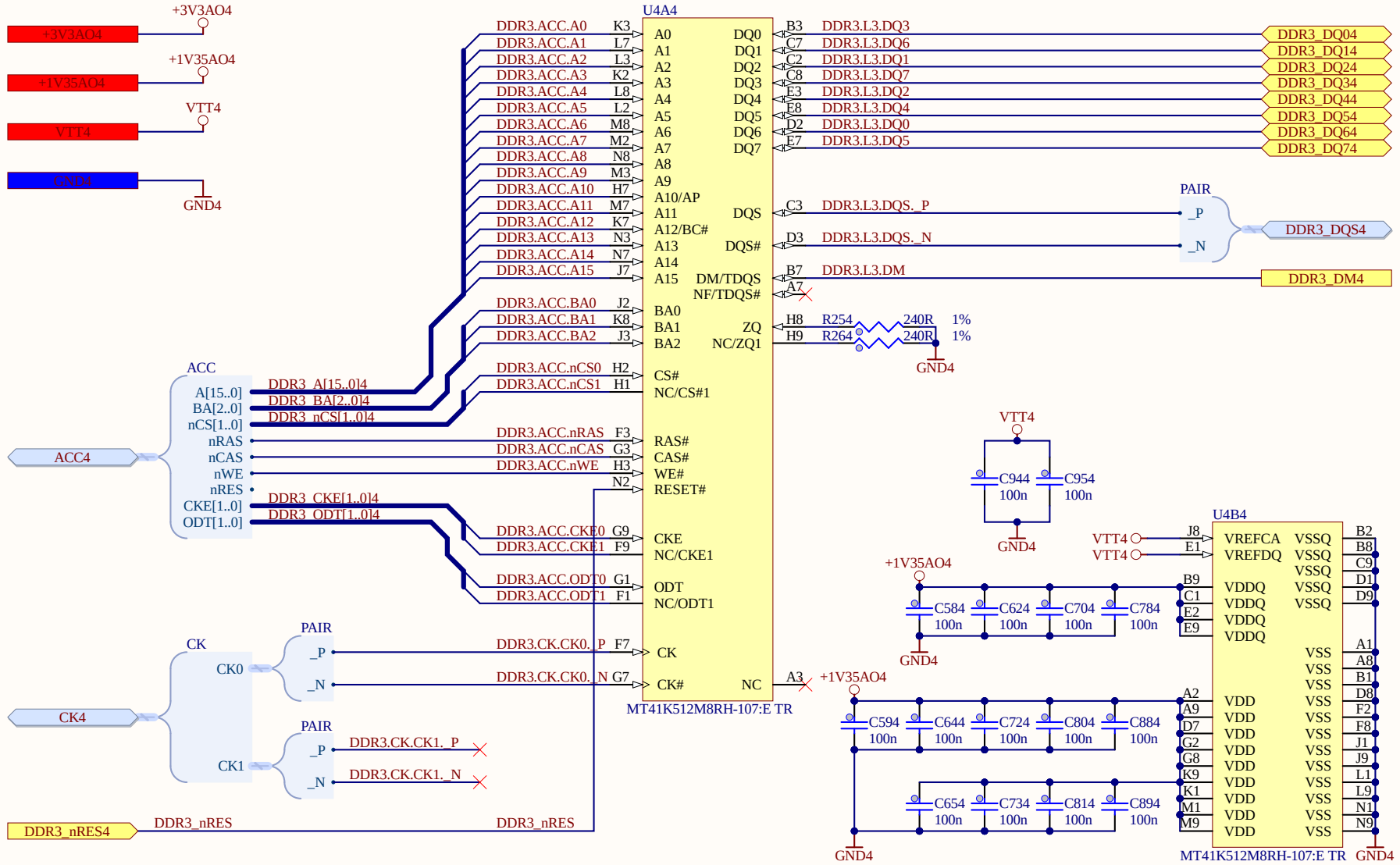


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