

PA0/SYNC/ADC0/ADC0/AC0/AREF
 PA1/SYNC/ADC1/ADC1/AC1
 PA2/SYNC/ADC2/ADC2/AC2
 PA3/SYNC/ADC3/ADC11/AC3
 PA4/SYNC/ADC4/ADC12/AC4
 PA5/SYNC/ADC5/ADC13/AC5
 PA6/SYNC/ADC6/ADC14/AC6
 PA7/SYNC/ADC7/ADC15/AC7/AC0/OUT

 PB0/SYNC/ADC8/ADC0/AC0/AREF
 PB1/SYNC/ADC9/ADC1/AC1
 PB2/SYNC/ASYNC/ADC10/ADC2/AC2
 PB3/SYNC/ADC11/ADC3/AC3/DAC1
 PB4/SYNC/ADC12/ADC4/AC4
 PB5/SYNC/ADC13/ADC5/AC5/TDI
 PB6/SYNC/ADC6/AC6/AC1/OUT/TCK
 PB7/SYNC/ADC15/ADC7/AC7/AC0/OUT/TDO

 PC0/SYNC/OC0A/OC0ALS/SDA
 PC1/SYNC/OC0B/OC0AHS/XCK0/SCL
 PC2/SYNC/ASYNC/OC0C/OC0BLS/RXD0
 PC3/SYNC/OC0D/OC0BHS/TXD0
 PC4/SYNC/OC0CLS/OC1A/SS
 PC5/SYNC/OC1B/XCK1/MOSI
 PC6/SYNC/OC0DLSRXD1/MISO/RTCOU
 PC7/SYNC/OC0DHSTXDI/SCK/clkPER

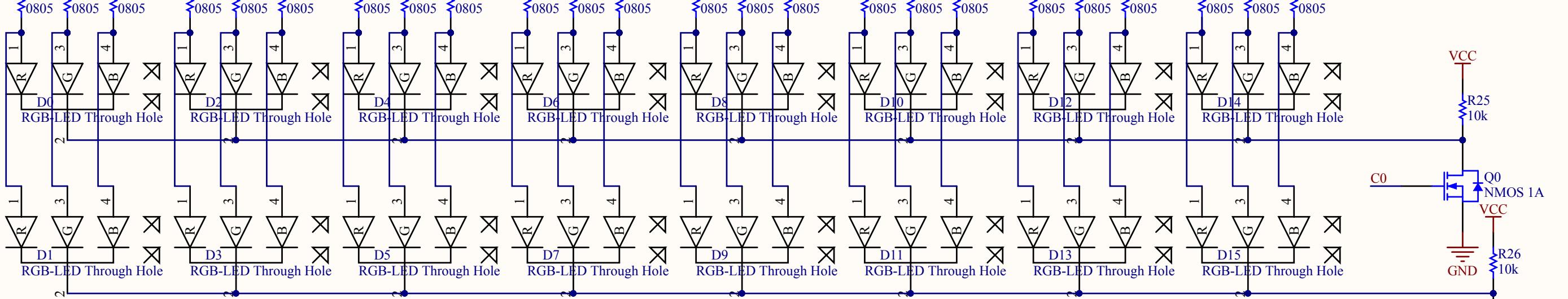
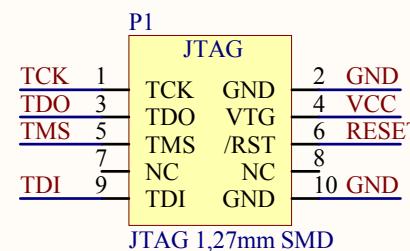
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 PE3/SYNC/OC0D/OC0BHS/TXD0
 PE4/SYNC/OC0CLS/OC1A/SS
 PE5/SYNC/OC1B/XCK1/MOSI
 PE6/SYNC/OC0DLS/RXD1/MISO
 PE7/SYNC/D+/TXDI/SCK/clkPER

PA0/SYNC/ADC0/ADC8/AC0/AREF
 PA1/SYNC/ADC1/ADC1/AC1
 PA2/SYNC/ADC2/ADC2/AC2
 PA3/SYNC/ADC3/ADC11/AC3
 PA4/SYNC/ADC4/ADC12/AC4
 PA5/SYNC/ADC5/ADC13/AC5
 PA6/SYNC/ADC6/ADC14/AC6
 PA7/SYNC/ADC7/ADC15/AC7/AC0/OUT

 PB0/SYNC/ADC8/ADC0/AC0/AREF
 PB1/SYNC/ADC9/ADC1/AC1
 PB2/SYNC/ASYNC/ADC10/ADC2/AC2
 PB3/SYNC/ADC11/ADC3/AC3/DAC1
 PB4/SYNC/ADC12/ADC4/AC4
 PB5/SYNC/ADC13/ADC5/AC5/TDI
 PB6/SYNC/ADC6/AC6/AC1/OUT/TCK
 PB7/SYNC/ADC15/ADC7/AC7/AC0/OUT/TDO

 PC0/SYNC/OC0A/OC0ALS/SDA
 PC1/SYNC/OC0B/OC0AHS/XCK0/SCL
 PC2/SYNC/ASYNC/OC0C/OC0BLS/RXD0
 PC3/SYNC/OC0D/OC0BHS/TXD0
 PC4/SYNC/OC0CLS/OC1A/SS
 PC5/SYNC/OC1B/XCK1/MOSI
 PC6/SYNC/OC0DLSRXD1/MISO/RTCOU
 PC7/SYNC/OC0DHSTXDI/SCK/clkPER

 PE0/SYNC/OC0A/OC0ALS/SDA
 PE1/SYNC/OC0B/OC0AHS/XCK0/SCL
 PE2/SYNC/ASYNC/OC0C/OC0BLS/RXD0
 PE3/SYNC/OC0D/OC0BHS/TXD0
 PE4/SYNC/OC0CLS/OC1A/SS
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 PE7/SYNC/D+/TXDI/SCK/clkPER



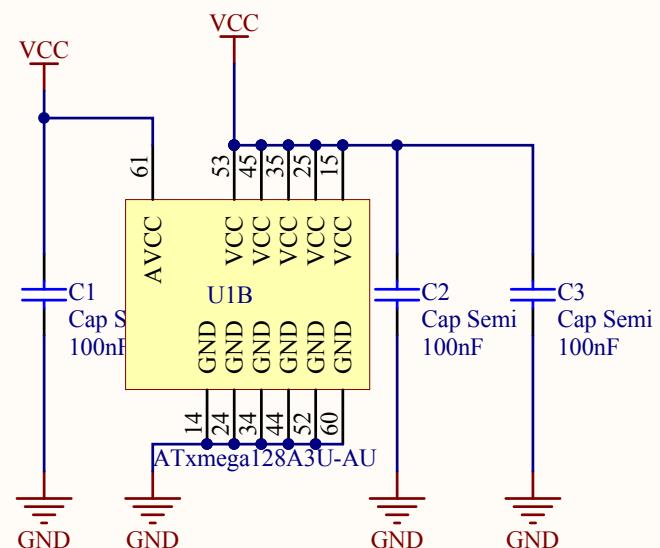
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 PD5/SYNC/OC1B/XCK1/MOSI
 PD6/SYNC/OC0DLS/RXD1/MISO
 PD7/SYNC/D+/TXDI/SCK/clkPER

 PE0/SYNC/OC0A/SDA
 PE1/SYNC/OC0B/SDA
 PE2/SYNC/ASYNC/OC0C/SCL
 PE3/SYNC/OC0D/TXD0
 PE4/SYNC/OC0CLS/OC1A/SS
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 PE7/SYNC/D+/TXDI/SCK/clkPER

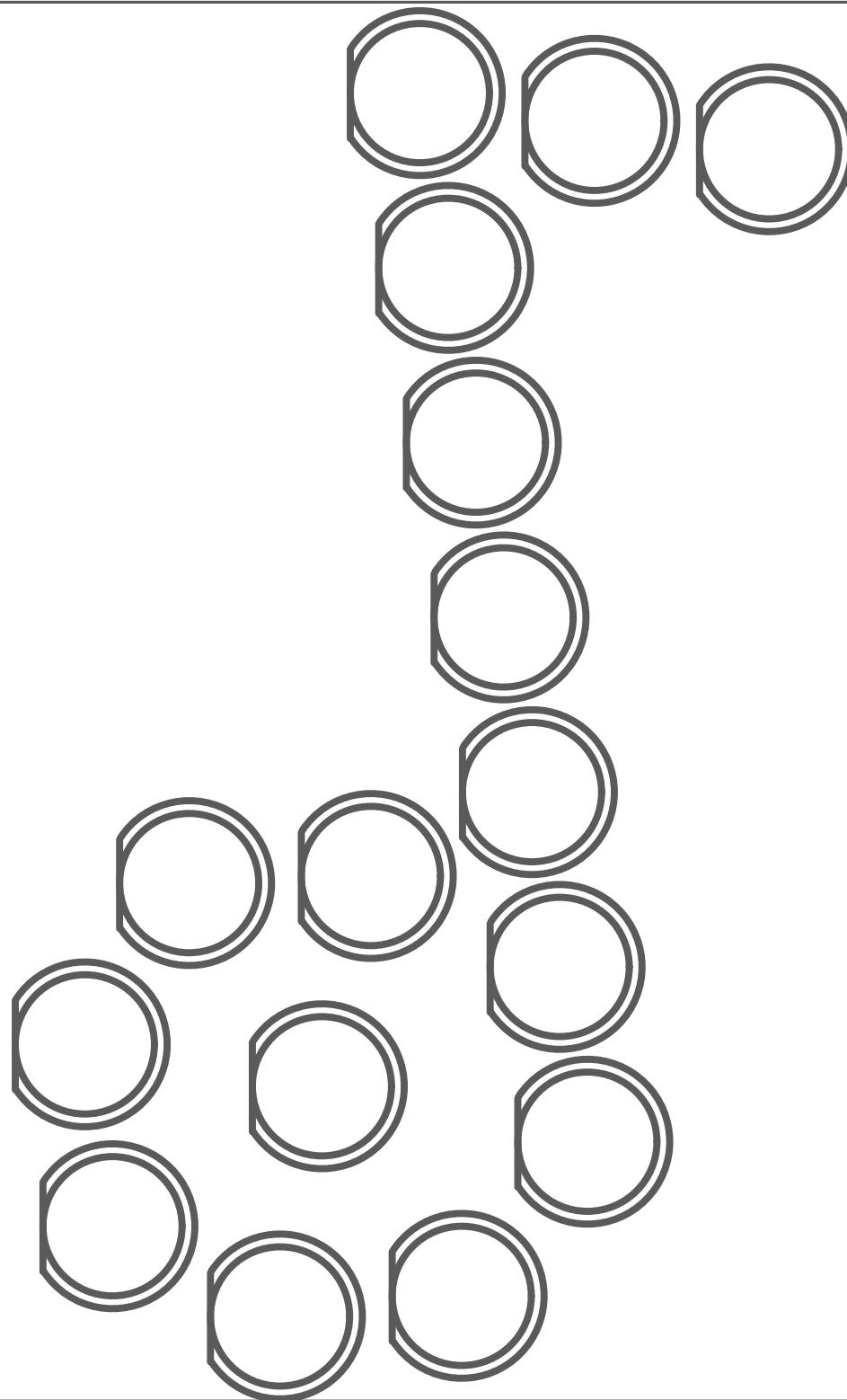
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 PF4/SYNC/OC0CLS/TXD0
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 PF6/SYNC/OC0DLS/RXD1/MISO
 PF7/SYNC/D+/TXDI/SCK/clkPER

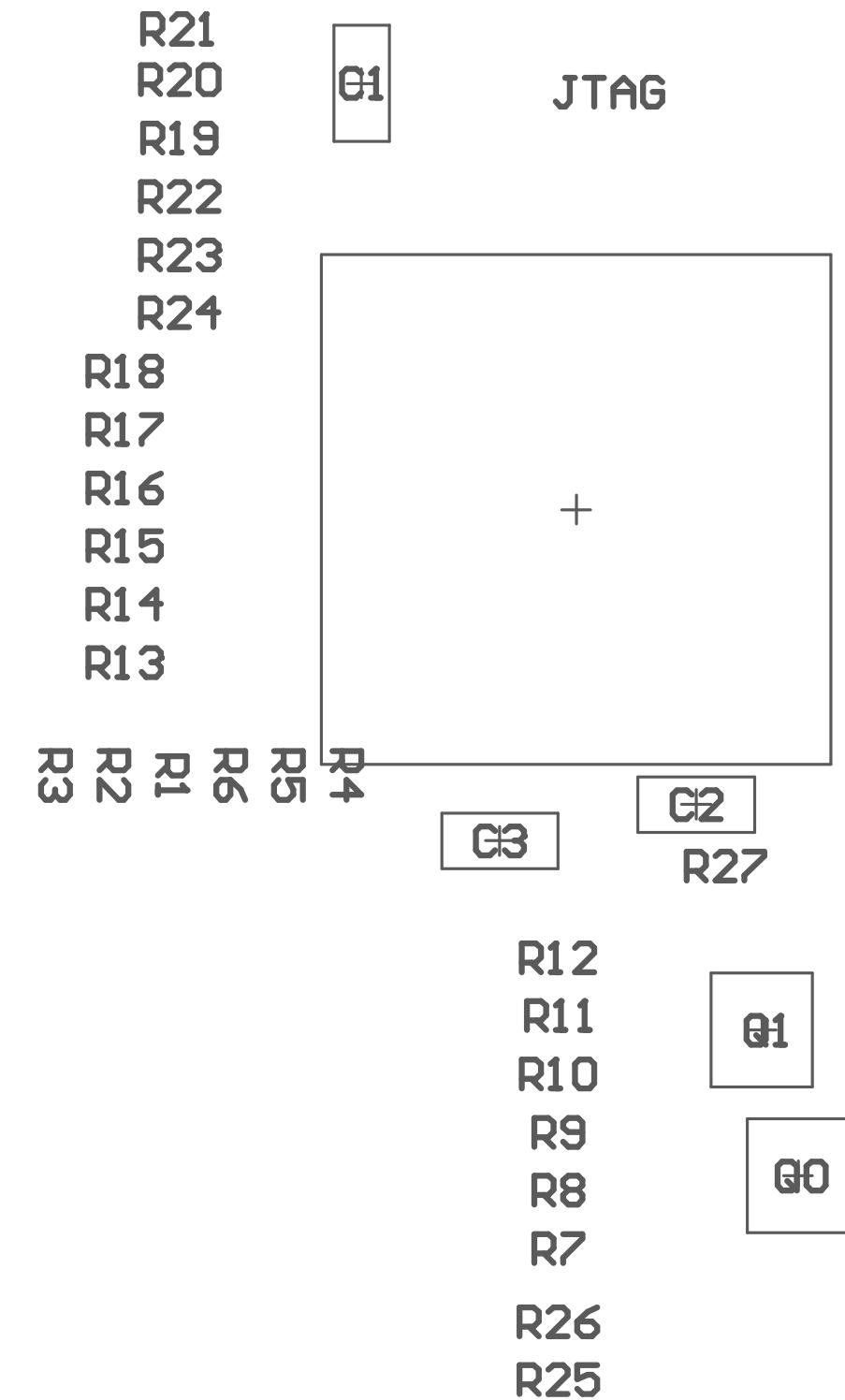
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 PD/PDI_RESET
 PD/PDI_DATA

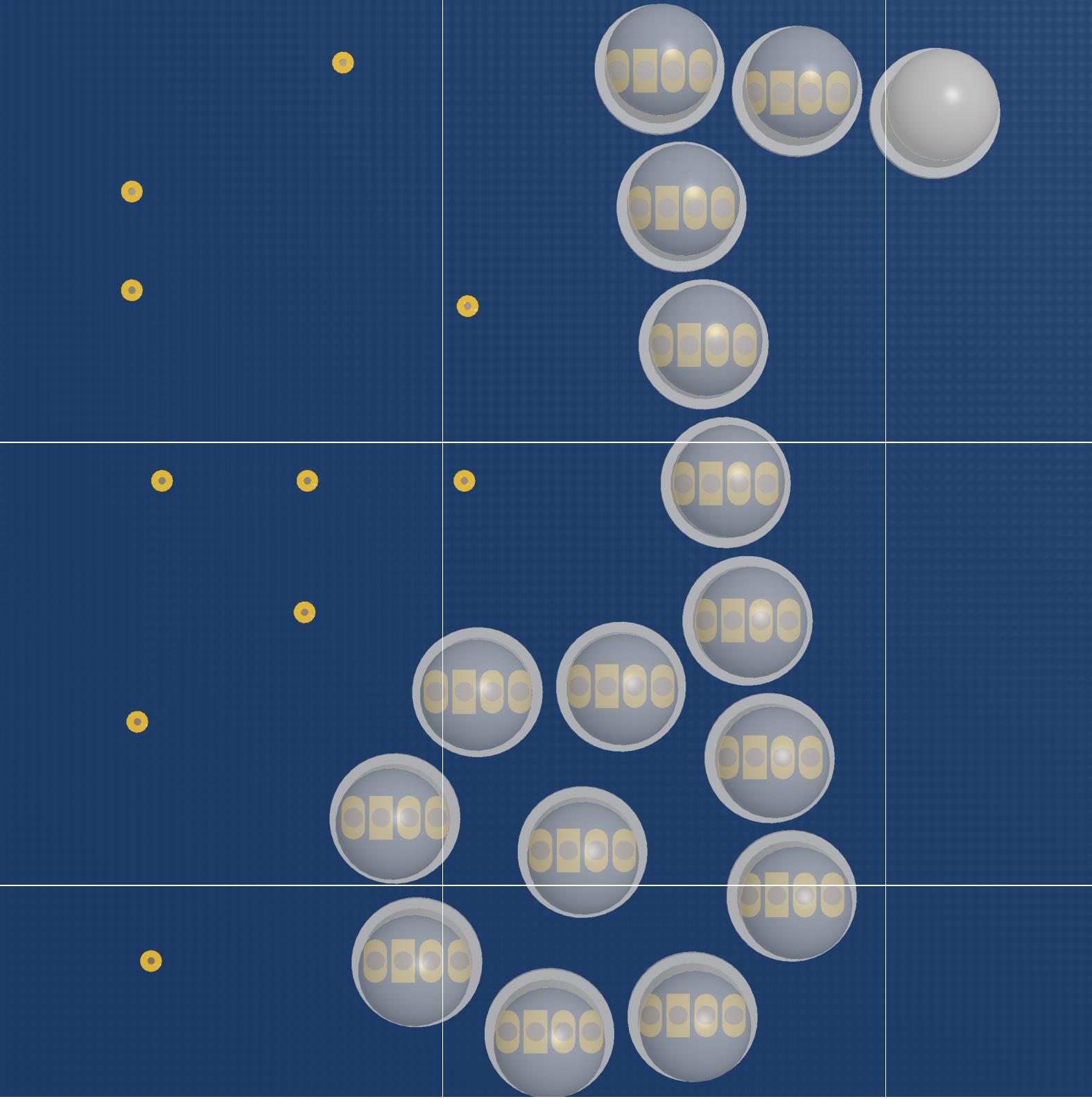
U1A
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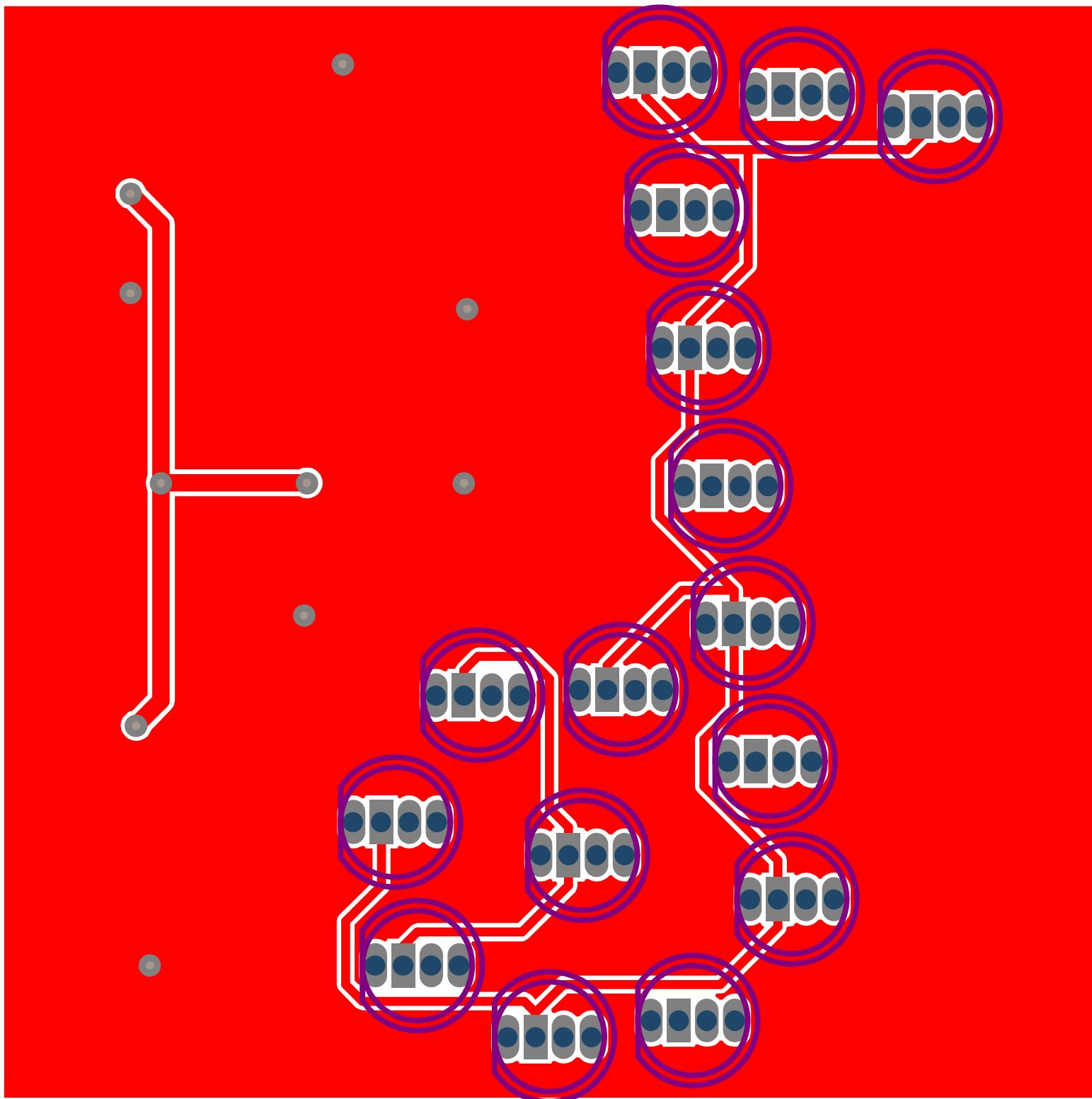


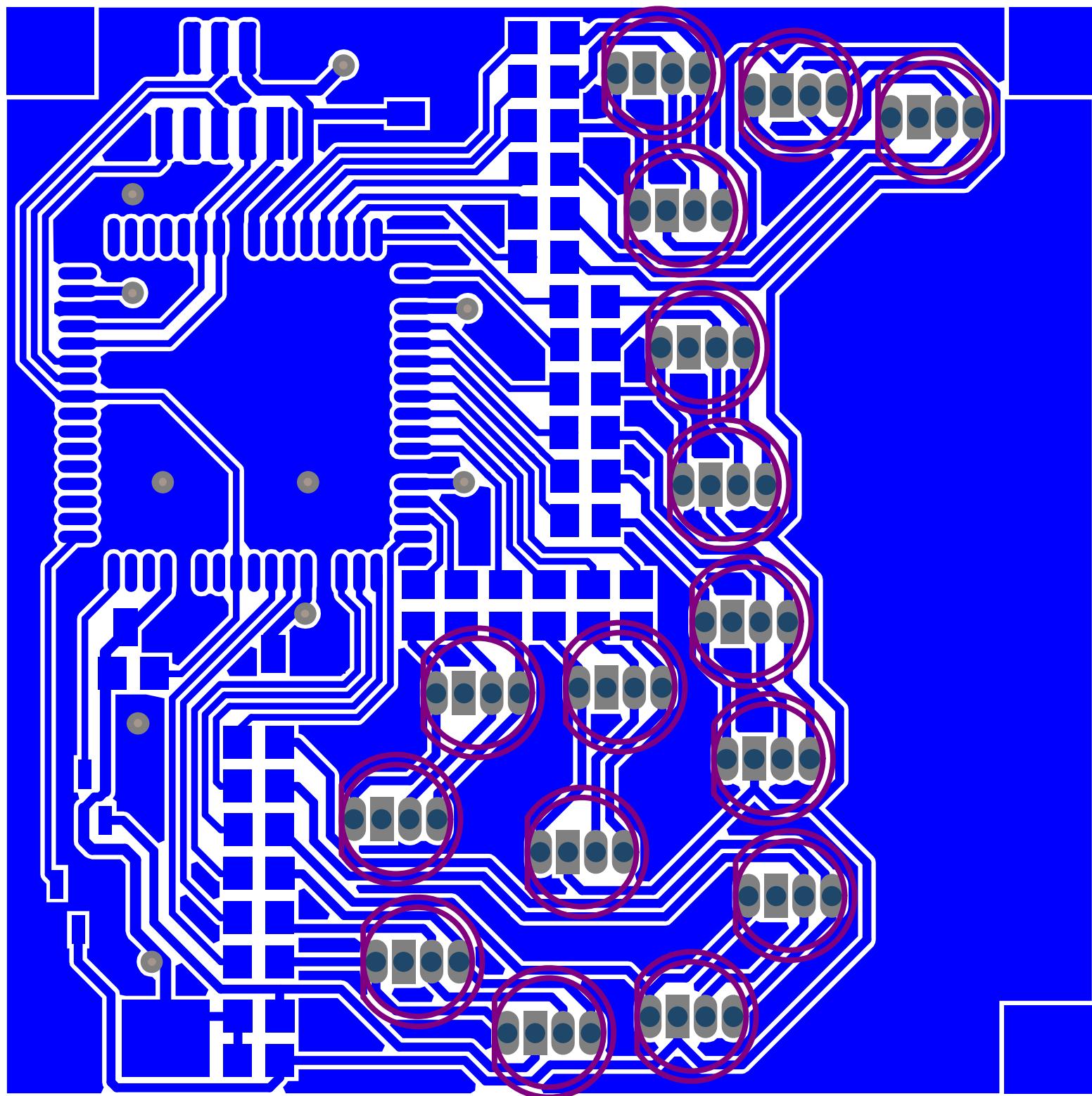


Dei Taktlause SMB RGB imponator Av Øystein Smith 2013 Rev. 2.12

XMEGA128A3U

GND





Designator	Quantity	Value	Description
C1, C2, C3	3	100nF	Capacitor (Semiconductor SIM Model)
D0, D1, D2, D3, D4, D5, D6, D7, D8, D9, D10, D11, D12, D13, D14, D15	16		RGB-LED Through Hole
P1	1		
Q0, Q1	2		N-channel Enhancement Mode Field-effect Transistor
R1, R4, R7, R10, R13, R16, R19, R22	8	68R	0805 SMD
R2, R3, R5, R6, R8, R9, R11, R12, R14, R15, R17, R18, R20, R21, R23, R24	16	120R	0805 SMD
R25, R26, R27	3	10k	Rectangular Thick Film Chip Resistor, 10 Ohm to 1M Ohm Range, 0.1% and 0.5% Tolerance, 0805 Size, 0.1 W, Rectangular Thick Film Chip Resistor, 10 Ohm to 1M Ohm Range, 0.1% and 0.5% Tolerance, 0805 Size, 0.1 W, 10kOhm
U1	1		8/16-bit AVR XMEGA A4 Microcontroller