

1 APB Compliance Plan

The APB is part of advanced microcontroller Bus architecture (AMBA) bus protocol is designed to be used with peripherals which requires low power bandwidth. All the peripherals of APB bus is connected to AHB-APB bridge which acts as master.

A write data bus is used to move data from the Bridge to Peripherals, while a read data bus is used to move data from Peripherals to Bridge. Psel is used to select the peripherals on the APB bus

1.1 BRIDGE Compliance VPlan

Logical Instances: APB_BUS_MONITOR

This section collects the coverage needed for Bridge APB compliance.

Name	Item Pattern	Bucket Filter	Parameters
Transfer to all address	APB_BUS_MONITOR. <i>bus_monitor.apb_transfer_cg.TRANS_ADDR</i>		
Read or write transfer	APB_BUS_MONITOR. <i>bus_monitor.apb_transfer_cg.TRANS_DIRECTION</i>		
Transfer direction with addr	APB_BUS_MONITOR. <i>bus_monitor.apb_transfer_cg.TRANS_ADDR_x_TRANS_DIRECTION</i>		
Transfer data	APB_BUS_MONITOR. <i>bus_monitor.apb_transfer_cg.TRANS_DATA</i>		