CME 2203 Lab 4 Pre-lab

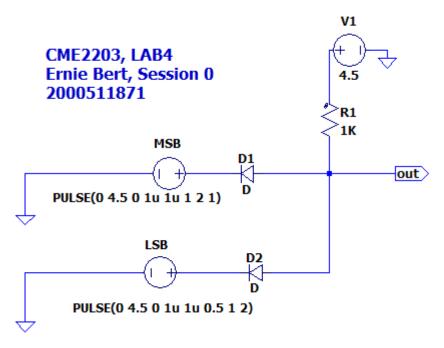
Name-Surname : Student Number: Section :

Subject : Diodes

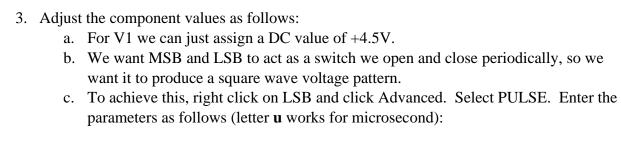
- Please read this document from <u>start to finish</u> carefully before starting your work.
- Prepare as a **PDF** including screenshot and simulation graph.
- Prelab must be prepared INDIVIDUALLY and uploaded to Sakai course page.

Draw the following circuit on LTSpice Schematics and save to a folder of your choice. Note that the screenshot we want is more than just the circuit; we want simulation graphs as well.

- 1. For your screenshot, top of the window should contain the graph plot panes, and the bottom of the window should contain:
 - a. The circuit diagram



- b. The text label
- Create the circuit schematic shown above. Note that we use square pulses to create all
 combinations of MSB and LSB. You can see the parameter values below the logic input
 voltage sources MSB and LSB. You can make both MSB and LSB provide pulses for 4
 cycles.



i.	$V_{initial}(V)$:	0
ii.	$V_{on}(V)$:	4.5
iii.	$T_{delay}(s)$:	0
iv.	$T_{rise}(\mu s)$:	1u
v.	$T_{fall}(\mu s)$:	1u
vi.	$T_{on}(s)$:0.5
vii.	$T_{period}(s)$:	1
viii.	Ncycles		:2

This setting creates a square wave with a period of 1s, and because it's on($\pm 4.5V$) at only 0.5s, so its DUTY CYCLE is 50%. It lasts for 2 cycles, so for (cycle no)x(period T) = 2x1 = 2 seconds.

d. Now, right click on MSB and click Advanced. Select PULSE. Enter the parameters as follows (letter **u** works for microsecond):

i.	$V_{initial}(V)$:	0
ii.	$V_{on}(V)$:	4.5
iii.	$T_{delay}(s)$:	0
iv.	$T_{rise}(\mu s)$:	1u
v.	$T_{fall}(\mu s)$:	1u
vi.	$T_{on}(s)$:	1
vii.	$T_{period}(s)$:	2
viii.	N _{cycles}	:	1

This setting creates a square wave with a period of 2s. It lasts for only one cycle, so 1x2 = 2 s.

- 4. Now, we are ready to run the simulation! Click on the running human and edit the simulation command (Remember, you can also change this command later by going to Simulate\Edit Simulation Command):
 - a. Under the Transient tab, select the following parameters:

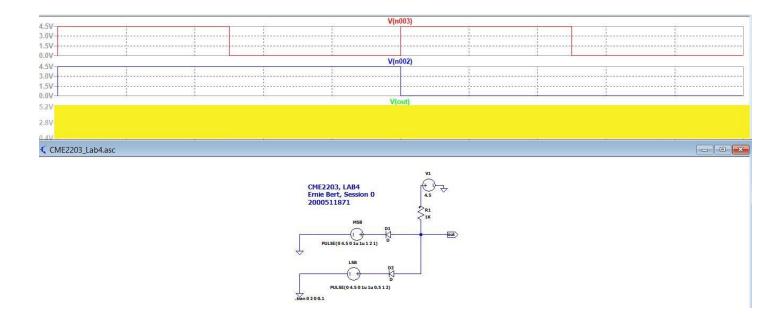
i. Stop Timeii. Time to Start Saving Dataii. 2

iii. Maximum Time Step : 0.1 and click OK.

This means you run the simulation from 0 to 2s. with time intervals of 100ms. Click the running human again. You should see an empty graph on top of the window now. Let's fill it with graphs!

- b. Now, move the cursor to the graph, right click and select Add Plot Pane. Now you should have a total of three voltage plot panes. If not you should add more.
- c. Click on the top graph pane, then click the red probe appearing on the circuit to the wire connecting to positive side of LSB, to the left of diode. This is our first logic input.
- d. Click on the middle graph pane, then click the red probe appearing on the circuit to the wire connecting to positive side of MSB, to the left of diode. This is our second logic input.
- e. Click on the bottom graph pane, then click on the wire that connects to **Out** port. This is the logic output.

So your final screenshot should be like this (**Out** voltage is not shown)



Congratulations! You are all set! Add screenshots of your circuit design and simulation graph and answer following questions.

- 1 How does this circuit work? Explain in detail.
- 2 What are some possible disadvantages of using diodes as gates?