Ignition: V8 Interpreter 评注及修订

针对 V8 的 v8.0 版本评注

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中文评注版说明

本评注版本适用于 LV3+实习生进入 V8 for RISC-V 项目的学习和培训。其他 ISA 的移植路径基本相同，可以作为参考。要求具有基本的编译知识，以及基本的虚拟机知识。同时参与 RISC-V 后端的同学需要事先自学 RISC-C 指令集。

Background

The machine code generated by V8's full-codegen compiler is verbose, and as such, can contribute significantly to the amount of memory used by V8's heap for typical web-pages (a previous analysis showed that the code-space contributed to around 15-20% of the JS heap).

As well as causing memory pressure, this means that V8 tries very hard to avoid generating code that it thinks might not be executed. It implements *lazy* parsing and compiling, where functions are typically only compiled on first-run. This has a significant cost during webpage startup since the function's source needs to be reparsed during the lazy compilation (e.g., crbug.com/593477).

The aim of the Ignition project is to build an interpreter for V8 which executes a low-level bytecode, thus enabling run-once or non-hot code to be stored more compactly in bytecode form. Since the bytecode is smaller, compilation time is much reduced, and we will also be able to be more eager about initial compilation, which significantly improve startup time. An added advantage is that the bytecode can be fed into a Turbofan graph generator directly, thereby avoiding the need to reparse the JavaScript source code when optimizing a function in TurboFan.

The goals of the Ignition project are:

- Reduce the size of the code-space to around 50% of its current size.
- Have reasonable performance compared to full-codegen (somewhere in the range of 2x slower on peak performance benchmarks such as Octane, significantly less slowdown on real world websites).
    - Note: the slowdown overall will be significantly less, hopefully negligible, due to hot code being optimized by either Crankshaft or TurboFan.
- Full support for DevTools debugging and cpu profiling.
- Replace full-codegen for first-level compilation.
    - We can't replace full-codegen completely until Crankshaft is deprecated, since Crankshaft can't deoptimize to Ignition, it instead needs to deopt to full-codegen code.
- A new frontend to the Turbofan's compiler to enable optimized re-compilation without re-parsing the JS source code.
- Support for deoptimizing from TurboFan to the interpreter.

Explicit non-goals of the project (at least at this stage) are:

- Support for platforms not allowed to JIT code (e.g., iOS)
    - JIT code generation is still required for ICs and code stubs.
- Support non-JavaScript code executed by V8 (e.g., WASM). 暂时不关注

Commented [WW1]: To 实习生：本节是背景介绍，涉及十几个虚拟机和编译器的术语和概念，看不懂没关系。需要不懂的词汇记录下来，在内网或 GitHub 上提问。

Commented [WW2]: 本段介绍推出解释器和字节码的动机：降低开发难度，减少内存使用。

Commented [WW3]: 是指过去版本的 baseline JIT。最新的 8.0 版本之后已经没有启用了。

Commented [WW4]: JS Heap 后续会学习掌握到具体含义。

Commented [WW5]: 本段继续介绍动机：使用解释器可以加快脚本的启动时间。

Commented [WW6]: 下面一段开始介绍为了满足动机而使用的方法：使用一种类似虚拟机字节码的执行方式。另外一个这里提到的重要好处（设计选择）是，turbofan 可以直接以字节码（bytecodes）作为编译器的前端输入方式，而不需要在从 JS 前端进行（重复的）解析。

Commented [WW7]: 暂时不用关注。

Commented [WW8]: 已经实现

Commented [WW9]: 对于你而言 deopt 可能是新概念，现在不懂没关系。后续涉及 deopt 的实现的时候会看到

Commented [WW10]: 对应 d8 --jitless 选项。【移植到 RISC-V 新平台的时候，解释器本身基本不需要修改】

Commented [WW11]: 移植到 RISC-V 平台，这里需要理解和开发
Builtins 和 CodeStubAssembler 都需要实现新后端代码

- Equivalent performance to full-codegen compiler.
- Completely replacing the full-codegen compiler. 已经替换了
    - As outlined above, we need full-codegen both as a deoptimization target for Crankshaft, and to build some type feedback required by Crankshaft which Ignition cannot provide. As such, full-codegen will become a middle tier for hot code which will eventually be optimized by Crankshaft (any functions which the parser decides should be optimized by TurboFan will not be compiled via full-codegen though, instead they will be optimized directly by TurboFan.

Overall Design

This section outlines the overall design of the Ignition bytecode interpreter, with following sections giving more details on the specifics.

The interpreter itself consists of a set of bytecode handler code snippets, each of which handles a specific bytecode and dispatches to the handler for the next bytecode. These bytecode handlers are written in a high level, machine architecture agnostic form of assembly code, as implemented by the `CodeStubAssembler` class and compiled by Turbofan.

As such, the interpreter can be written once and uses TurboFan to generate machine instructions for each of the architectures supported by V8. When the interpreter is enabled, each V8 isolate contains a global interpreter dispatch table, holding a code object pointer to each bytecode handler, indexed by the bytecode value. These bytecode handlers can be included in the startup snapshot and deserialized when a new isolate is created.

In order to be run by the interpreter, a function is translated to bytecode by a `BytecodeGenerator` during its initial unoptimized compile step. The `BytecodeGenerator` is an `AstVisitor` which walks the function's AST emitting appropriate bytecodes for each AST node. This bytecode is associated with the function as a field on the `SharedFunctionInfo` object, and the function's code entry address is be set to the `InterpreterEntryTrampoline` builtin stub.

When the function is called at runtime, the `InterpreterEntryTrampoline` stub is entered. This stub set up an appropriate stack frame, and then dispatch to the interpreter's bytecode handler for the function's first bytecode in order to start execution of the function in the interpreter. The end of each bytecode handler directly dispatches to the next handler via an index into the global interpreter table, based on the bytecode.

The Ignition interpreter is a register-based interpreter. These registers are not traditional machine registers, but are instead specific slots in a *register file* which is allocated as part of a function's stack frame. Bytecodes can specify the input and output registers on which they operate through bytecode arguments, which immediately follow the bytecode itself in the `BytecodeArray` stream.

In order to reduce the size of the bytecode stream, Ignition has an accumulator register, which is used by many bytecodes as implicit input and output register. This register is not part of the

Commented [WW12]: 这里的 bytecode handler 意思大概是对于每个 bytecode BARA 有一个 doBARA 的函数对应。
dispatches 这里表达的是如何在 doBARA 之间进行控制流传递（常规思路）。
设计者将需要 arch 移植的部分尽可能多的限制在了 JIT 部分，解释器部分尽可能不涉及（这里说“尽可能”是因为需要结合具体的代码才能确定。目前还没看完所有的代码，等看完之后更新。）
`CodeStubAssembler` 就是后续频繁提到的 CSA。

Commented [WW13]: 是指在 TurboFan JIT 实现了后端的代码之后，解释器可以跟 TurboFan 共用同一份后端实现（通过 CSA）。

Commented [WW14]: 这是个术语，后续培训和讨论课会涉及。

Commented [WW15]: 可以理解为解释器的常见的实现方式。现在不理解没关系，后续的培训&讨论会详细展开说明。

Commented [WW16]: 这里不理解没关系，涉及到另外的 V8 的功能特性，在第一期的后端移植中可以不用考虑。

Commented [WW17]: 有关 trampoline 是一个编译/运行时系统的常用的术语，后续会讲到。Builtin 在 V8 中也有对应的代码实现，概念上跟常见的 builtin 茶不错。Stub 除了 CSA 之外可能也有别的技术含义，总体上都是符合「stub」这个意思：一个弱弱的替身。

Commented [WW18]: Stack frame 的概念是在编译原理或者《编译技术入门与实战》课程中介绍过的，请阅读虎书或者 EaC。
Appropriate 这里就是个修饰词，没有特别含义。

Commented [WW19]: 现阶段分发可以简单理解为一个循环，这个循环做 2 件事情：第一件事情是从 JS 函数对应的字节码序列中取出一个字节码执行，取出位置由一个 index 指定，刚开始是函数入口点；第二件事情是调用对应的 handler，所有事情 handler 处理，同时 handler 也会更新 index，或者退出循环。
现阶段理解到这个程度就足够了。很快就会看到代码实现。

Commented [WW20]: 这里表示上面注释中说的循环是概念上的，代码中可能找不到 while loop。另一方面一般的解释器都会有一个 while loop，所以这个概念要记住。

Commented [WW21]: 虚拟机也好解释器也好，根据操作数的存储和索引方式，可以分为基于寄存器的和基于栈的。Java 虚拟机是基于栈的，经典的语言虚拟机基于栈的比较多，容易实现，体积小，代价是速度可能慢，跟物理 CPU 映射关系不好。 ... [1]

Commented [WW22]: 这句话对于后续的代码阅读比较重要。先保证自己理解字面意思。记住了，后续代码阅读的时候就容易理解了。

Commented [WW23]: 常见的实现方式。通过累加器来进行体积缩减。注意这个设计决定会影响到所有字节码的设计。

register file on the stack, but is instead maintained in a machine register by Ignition. This minimize loading and storing repeatedly to memory for register operations. It also reduces the size of bytecode by avoiding the need to specify an input and output register for many operations. E.g., binary op bytecodes only require a single operand to specify one of the inputs, with the other input and the output registers being implicitly the accumulator register, rather than having to explicitly specify all three registers.

Commented [WW24]: 可以理解为 CPU 的寄存器，例如 x86 的 EAX

Commented [WW25]: 这些好处和特点是教科书里有的，不是 V8 特有的。可以对照着看。

Generation of Bytecode Handlers

Commented [WW26]: LV2 实习生第一周不要求掌握本节内容或看懂源代码。
LV4 实习生要求一周内学习并掌握大概的原理，阅读过源代码。

Bytecode handlers are generated by the TurboFan compiler. Each handler is its own code object and is generated independently. The handlers are written as a graph of Turbofan operations, via an `InterpreterAssembler`, which is a subclass of the `CodeStubAssembler`, with some extra high-level primitives required by the interpreter (e.g., `Dispatch`, `GetBytecodeOperand`, etc.). An example of the generator function for the `Ldar` (LoaD Accumulator from Register) bytecode handler is as follows:

Commented [WW27]: 下面这段代码看不懂没关系，后续源代码讨论的时候会具体看。

```
void Interpreter::DoLdar(InterpreterAssembler* assembler) {
  Node* reg_index = __ BytecodeOperandReg(0);
  Node* value = __ LoadRegister(reg_index);
  __ SetAccumulator(value);
  __ Dispatch();
}
```

Commented [WW28]: 这里可能就是前后几段说的尾递归形式调用 dispatch。

The bytecode handlers are not intended to be called directly, instead each bytecode handler dispatches to the next bytecode. Bytecode dispatch is implemented as a tail call operation in TurboFan. The interpreter loads the next bytecode, indexes into the dispatch table to get the code object of the target bytecode handler, and then tail calls the code object to dispatch to the next bytecode handler.

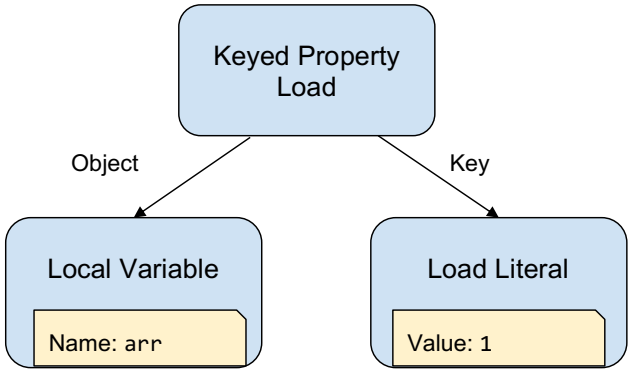
The interpreter also needs to maintain state across bytecode handlers in fixed machine registers. For example, a pointer to the `BytecodeArray`, the current bytecode offset and the value of the interpreter's accumulator. These values are treated as parameters by TurboFan, which are received as input from the previous bytecode dispatch, and passed to the next bytecode handler as parameters to the bytecode dispatch tail call. The bytecode handler dispatch calling convention specifies fixed machine registers for these parameters, which enables them to be threaded through interpreter dispatches without needing to be pushed / popped onto the stack.

Once the graph for a bytecode handler is produced it is passed through a simplified version of Turbofan's pipeline and assigned to the corresponding entry in the interpreter table.

Bytecode Generation

To compile a function to bytecode, the JavaScript code is parsed to generate its AST (Abstract Syntax Tree). The `BytecodeGenerator` walks this AST and generates bytecode for each of the AST nodes as appropriate.

For example, the snippet of Javascript "`arr[1]`" will be translated into the following AST tree:



The `BytecodeGenerator` will walk this tree, first visiting the `KeyedPropertyLoad` node, which will first visit the *Object* edge to generate code which evaluates the object on which the keyed load should be performed. In this case, the object is a local variable, which is already assigned to an interpreter register (e.g., `r3`), so code is generated to load this register into the accumulator (`Ldar r3`) and control returns to `KeyedPropertyLoad` node visitor. This visitor allocates a temporary register (e.g., `r6`) to save the object in, and generates code to store the accumulator into this register (`Star r6`)[1]. The *Key* edge is now visited to generate code to produce the key for the property load. In this case this node is an integer literal 1, and so a `LdaSmi #1` bytecode is output to load 1 into the accumulator. Finally the code to perform the keyed property load is output, resulting in a snippet of bytecode:

```
Ldar r3
Star r6
LdaSmi #1
KeyedLoadIC r6 <feedback slot>
```

The `BytecodeGenerator` uses a `BytecodeArrayBuilder` to generate well formed array of bytecodes for the interpreter. The `BytecodeArrayBuilder` provides flexibility in what raw bytecodes are emitted. For example, we can have multiple bytecodes which perform the same semantic operation, but with some having wider (e.g., 16 bit or 32 bit) operands. The `BytecodeGenerator` doesn't need to know any of this, instead it simply asks the

[1] Note: Since the object was already in a register, this move in and out of the accumulator is redundant, therefore we have register aliasing optimizations which avoid it, described in more detail in Section TODO.

Commented [WW29]: 本段描述的是经典流程。源文件生成 AST，再通过 tree walking 方式转成字节码（某种 IR）

Commented [WW30]: 这里，arr 是一个 JS 的对象/数组，1 是字面量，中括号是一个属性提取的操作。
注意这里的 AST 是属于实现选择，只是可能的 AST 实现的一种技术实现。对这个方面有困惑或者兴趣的同学的可以看 EaC 中的有关中间表示的部分。虎书对此讨论不多。

Commented [WW31]: IC 部分后续会单独出一个视频介绍。
TODO：插入视频地址

Commented [WW32]: 简单理解为能正确运行的。

Commented [WW33]: 意思是没有对字节码的格式和长度进行限定，只作为字节一级的存储而存在，根据（内部概念上的表）挑选合适的字节数量进行存储。
而 tree walker / BytecodeGenerator 也不需要操心这样的细节。
TODO 插入后续视频地址

`BytecodeArrayBuilder` to output a given bytecode with a set of operands, and the `BytecodeArrayBuilder` chooses the appropriate width for the operands.

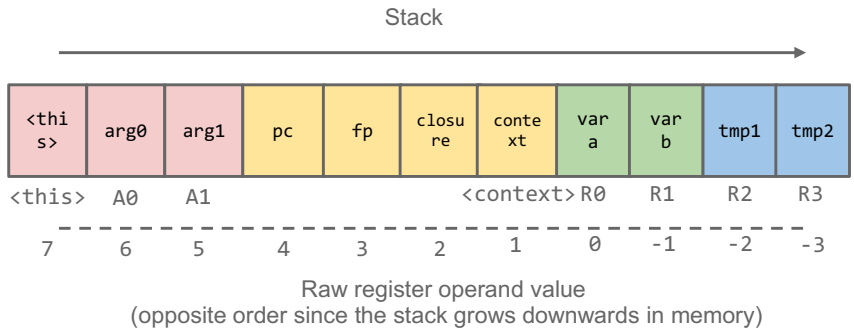
Once the bytecode is generated it is stored on a field in the `SharedFunctionInfo`. As well as being used by the interpreter for code execution, this bytecode is representative enough to enable generation of the TurboFan's compiler's graph without having to regenerate the function's AST. This avoids the current requirement to reparse the function's JavaScript source before recompile.

Interpreter Register allocation

During bytecode generation, the `BytecodeGenerator` allocates registers in a function's register file for local variables, context object pointers (used to maintain state across function closures), and temporary values required for expression evaluation (the expression stack). During execution, space for the register file is allocated during the function's prologue as part of the function's stack frame. Bytecodes operate on these registers by specifying a specific register index as a bytecode operand, which the interpreter uses to load from or store to the specific stack slot associated with this register.

Since the register indexes map directly to the function stack frame slots, the interpreter can also directly access other slots on the stack as registers. For example, the function context and function closure pointers which are pushed onto the stack by the prologue can be accessed directly as `Register::current_context()` and `Register::function_context()` by any bytecode which has register operands. Similarly, the arguments passed to the function (including the implicit <this> parameter) can also be accessed by register.

The figure below shows an example stack frame for a function and the mapping of registers indexes and their raw operand values, to stack slots:



Due to this register file approach, Ignition doesn't dynamically push and pop values onto the stack during expression evaluation as full-codegen does (the only exception is pushing arguments for calls, however this is done in a separate builtin, not in the interpreter itself). This has the advantage the the stack frame can be allocated once during the function prologue and can remain

Commented [WW34]: 这个 class 使用 tq 定义的。直接搜索源代码可能找不到对应的实现。编译构建一次之后会有。

Commented [WW35]: 是指 JIT 可以直接用字节码作为输入，而不用从 AST 开始进行。刚进入项目的同学记忆这个点就可以，不明白咩问题。

Commented [WW36]: 后续会在《深入 V8 引擎》中单独做一次视频介绍
TODO 插入视频 URL

Commented [WW37]: 有关闭包的知识自行查阅学习。Context obj 可以简单理解为函数调用栈销毁后还需要保留的局部变量都放在这里面。

Commented [WW38]: Prologue 和 epilogue 的概念需要自行学习，在虎书和 EaC 中有。
在函数被调用前在栈上分配好。而偏移地址是在 AST walk 生成字节码的时候分配和计算好的。

Commented [WW39]: 这里的意思是解释器中的寄存器的概念，直接映射到了 call stack 的 frame 中。所以可以泛化一下，使用同样的方式将 frame 中的内容也做

aligned to architecture specific requirements (e.g., Arm64's 16 byte stack alignment requirement). However, it does mean that the `BytecodeGenerator` needs to calculate the maximum size of the stack frame during code generation.

Locals variables declared within the function are semantically hoisted to the top of the function by the parser. As such the number of locals is known ahead of time, and each local is assigned an index in the register file during the initial stage of AST walking. The number of extra registers required for inner contexts is also known ahead of time by the parser, and thus can be preallocated by the `BytecodeGenerator` during the initial stage of AST walking.

Temporaries are required during expression evaluation, where there may be one or more live registers holding intermediate values in the expression tree until they are consumed. The `BytecodeGenerator` allocates registers using a scoped `BytecodeRegisterAllocator`. On each statement, a new scoped allocator is created for the statement, which is used by inner expression nodes to allocate temporary registers. These temporaries are only live for the lifetime of the expression statement, therefore when `BytecodeGenerator` finishes visiting this statement, the scoped allocator will go out of scope, and all temporary registers allocated by it will be released. The `BytecodeGenerator` keeps note of the maximum number of temporary registers which were allocated over the whole function's code generation, and allocates enough extra slots in the register file for the largest number of temporaries required.

The layout of the register file will consist of the locals followed by the temporary registers, with no overlap between locals and temporary registers to keep access simple. Once the emitter has calculated the total size of the register file, it will use this to calculate the required frame size of the function, and will save this value in the `BytecodeArray` object.

On entry to the function, the interpreter prologue will increment the stack pointer amount required to allocate space for the register file on the stack frame. All entries in the register file will initially be tagged pointers to undefined_value (like the locals pushed by full-codegen's prologue). The interpreter will only every store tagged pointers in the entries of the register file, so the GC can walk over any register files on the stack visiting all entries as tagged pointers.

Context chains

The interpreter tracks the current context object in the context stack slot (which is part of the function's fixed frame, and is accessed by bytecodes via `Register::current_context()`). When a new context is allocated, the `BytecodeGenerator` allocates a `ContextScope` object to track nested context chains. This allows the `BytecodeGenerator` to *unroll* nested context chain operations, enabling the interpreter to directly access any variables allocated within an inner context extension without having to walk the context chain.

When a `ContextScope` object is allocated, a `ContextPush` bytecode is emitted. This bytecode moves the current context object into a register allocated by the `BytecodeGenerator`, and pushes the new context into the current context register. When an operation is performed on a variable which was allocated to a local context, the `BytecodeGenerator` finds the associated

Commented [WW40]: 前面的评注中已经说明了这一点。

Commented [WW41]: 这个是 hoisted 是指将函数中某个位置的变量声明，移动到函数开头。注意这里需要不改变语义，也就是说原来错误的代码（未定义先使用）不能因此变为正确的。（TDZ？）

Commented [WW42]: TODO 解释

Commented [WW43]: 这里对于理解解释器寄存器分配的行为比较关键。

Commented [WW44]: 简单的取一个最大值。

Commented [WW45]: Layout 一般指内存布局，或者逻辑上的布局。

Commented [WW46]: 是指不存在同一块内存地址被 locals 和 temp 同时使用的情况。这样可以简化各类判断。

Commented [WW47]: 因为所有的需要的 slots 都是被映射到 frame 的，所以 call stack frame 的大小需要据此来决定。

Commented [WW48]: Tagged pointers 一般是使用指针的最后 1-2 个 bit 作为标记。

Commented [WW49]: Undefined_value 应该是 V8 的自定义的一个特殊值。

`ContextScope`'s, and checks which register the context object is currently allocated in, and can then directly load the variable out of the correct context slot in the context in pointed to by this register, rather than having to walk up the context chain to find the correct context. When the `ContextScope` goes out of scope, a `ContextPop` bytecode is emitted, which restores the parent context into the current context register.

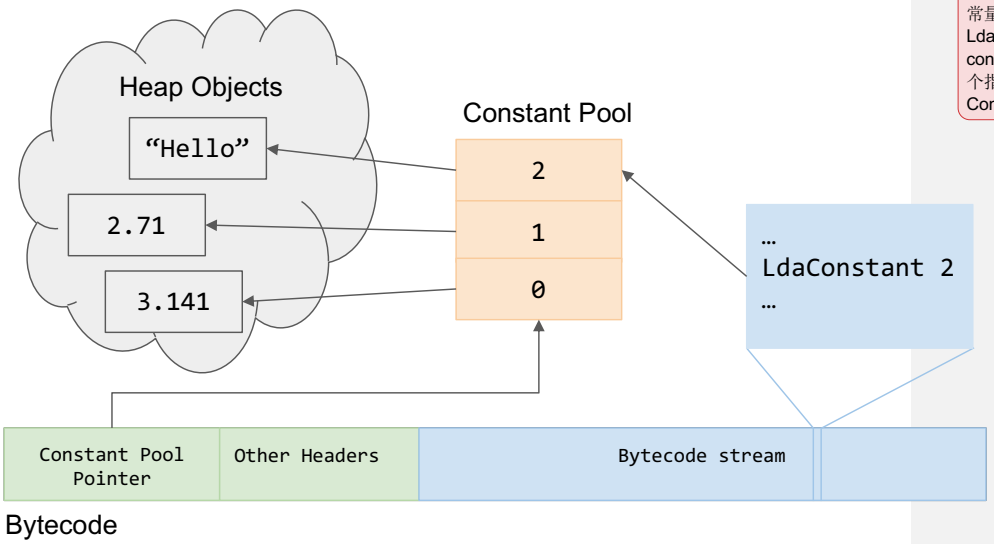
By maintaining the current context as an explicit register, rather than only relying on the `BytecodeGenerator` to keep track of which register holds the current context, enables the interpreter to perform operations which require the current context (e.g., passing the context to JS function calls or runtime builtin operations) without having to take an extra operand to specify which register holds the current context.

Commented [WW50]: 本段解释了，这里做成一个寄存器是为了效率考虑。这是编译器/虚拟机中的设计选择。

Constant Pool Entries

Constant pools are used to store heap objects and small integers that are referenced as constants in generated bytecode. Each `BytecodeArray` may have its own constant pool embedded in the `BytecodeArray` object. The constant pool is a `FixedArray` of pointers to heap objects. Bytecodes refer to constants in the constant pool by the index of each constant in the `FixedArray`. The indices are emitted as immediate unsigned operand values for bytecodes that take constant pool entries as inputs.

Commented [WW51]: 每个 BytecodeArray 一个常量池，每个常量池使用 FixedArray，
常量引用使用基址+非负偏移。
LdaConstant 这个字节码在执行的时候会将操作数加上 constant pool pointer，得到一个新的指针地址，通过这个指针地址，找到内存队中的对象。
Constantpool 可以简单看成一个 C 语言中的指针数组。



Strings and heap numbers are always placed into the constant pool as they live in the heap and are referenced by pointers. Small integers and the strong referenced oddball type's have bytecodes to load them directly and do not go into the constant pool. The constant pool can also store forward jump offsets and this is discussed below.

The constant pool is built during bytecode generation and there is a `ConstantArrayBuilder` class that is responsible for this. When bytecode needs to reference a constant, for instance loading a string, it passes the object to the constant array builder and asks for a constant pool index for the object. The `ConstantArrayBuilder` checks if the object is already present in the array and returns the existing index if so, otherwise it adds the object to the end of the constant pool being built and returns the index for the object. At the end of bytecode generation, the `ConstantArrayBuilder` emits a fixed array with the necessary object pointers.

The logic described so far is relatively simple. Complexity comes from the use of the constant pool to store forward jump offsets. The issue with forward jumps is that the jump displacement is not known at the time the jump bytecode is emitted so a space is reserved for an jump displacement offset in the bytecode stream. The `BytecodeArrayBuilder` keeps track of this location and patches the jump when the displacement is known.

In a simplified scenario, the bytecode generator emits a forward jump with an immediate byte operand left blank in the stream. When the displacement is known, the jump is patched. If the displacements fits within a byte operand then the displacement can be written directly into the blank operand. If the displacement is larger than a byte, then the displacement is put into the constant pool and both the jump bytecode and the operand are updated. The jump bytecode is updated to be a jump with a constant pool operand and the operand is updated to be the constant pool entry.

In practice, the constant pool grows as code is generated and there are no restrictions on how large it can be. When emitting code, the `BytecodeArrayBuilder` needs to know what flavor of jump bytecode to emit for forward jumps: a jump taking a byte operand, or two byte operand, or four byte operand. Therefor the `ConstantArrayBuilder` class supports reserving constant pool entries. The reservation guarantees an index of a particular size will be available in future even though the value for the index is not currently known. The `BytecodeArrayBuilder` creates a reservation when it emits a forward jump and emits the bytecode having an operand size that matches the reservation made in the `ConstantArrayBuilder`. When the jump is patched, the reservation is cancelled if the displacement fits inside an immediate operand of the same size as the reservation. Otherwise, the constant pool reservation is committed to be the jump displacement value and the jump bytecode and operand patched to use the jump displacement value in the constant pool.

Local Control Flow

Javascript has the usual set of imperative control flow primitives at the language level such as `if` statements, `if..else` statements, `for` loops, `while` loops, `do` loops, and `switch` statements. It also has the the language specific construct `for..in` for iterating over object

Commented [WW52]: TODO 不知道是什么。

Commented [WW53]: src/interpreter/constant-array-builder.h:40
做成了一个服务，lookup，跟 hash/map 的服务形式差不多。
最后是可以做成一个固定大小的数组的。

Commented [WW54]: 在编译器的各种代码生成阶段，跳转地址往往在第一遍的时候不知道。这时候就只能空着，并用某种形式记录下这个空缺的位置和需要的信息。后续有了之后，再回头来改写这个空白。也叫 patch。
位置无关代码（PIC）每次都在被加载的时候由加载器（loader）执行一次。

Commented [WW55]: 每次要填的相对地址有大小的区分。如果跳转足够小，那么直接作为立即数跳转；如果比较大，就变成查表跳转（从相对地址偏移变成了整数下标）。
这个过程，修改了字节码（都是 32bit 中的 7bit），但是没有修改字节码的序列。这是合理的，因为调整字节码序列的工作量太大，并且没有必要。

Commented [WW56]: 看起来本段的保留空间像是为了简化编程复杂度而进行的设计。

Commented [WW57]: 正常的控制流只要记住 3 个抽象：
1. 顺序执行（没错的，这个很容易被无视）
2. 条件跳转（branch 前向 forward）
3. 循环（goto 后跳 backward）

异常处理是更高抽象上的概念。具体实现可以用这些基础命令完成。

properties and also breakable named blocks. These control flow constructs only affect execution within a function compiled to bytecode.

The `BytecodeGenerator` converts the AST forms of Javascript's flow control statements into control flow builder class instances that generate the corresponding bytecode. Control flow at the bytecode level is implemented as a set of bytecodes for conditionally and unconditionally jumping. Supporting Javascript's for..in requires additional bytecodes to get the information to be iterated over and advancing to elements in the collection.

To facilitate building control flow structures, the `BytecodeArrayBuilder` supports the notion of labels that allow offsets to be specified. When the position of a label is known, the BytecodeGenerator binds the label to the current position with the `BytecodeArrayBuilder::Bind()` call. The `BytecodeGenerator` can refer to a label before it is bound when generating code for forward jumps. The `BytecodeArrayBuilder` checks that all labels referenced in bytecode are bound before the `BytecodeArray` is emitted. Because the Bind() call binds a label to the end of the bytecode being generated, labels are always constrained to target offsets within the bytecode array. The target offset for an emitted jump is present as either an immediate operand value in the bytecode or in the constant pool. There is no support for jumping to a dynamically computed value, for instance jumping to an offset in a register.

An example generating control flow is shown below for an `if..else` statement.

```
// Javascript
if (arg == true) {
  print("Hello
World\n");
} else {
  print("Mmmm\n");
}
```

```
// BytecodeGenerator (simplified)
void VisitIfStatement(IfStmt* stmt) {
  BytecodeLabel else_label,
                end_label;
  VisitForAccumulatorValue(
    stmt->condition());
  builder()->JumpIfFalse(&else_label);
  Visit(stmt->then_statement());
  if (stmt->HasElseStatement()) {
    builder()->Jump(&end_label);
    builder()->Bind(&else_label);
    Visit(stmt->else_statement());
  } else {
    builder()->Bind(&else_label);
  }
  builder()->Bind(&end_label);
}
```

```
// Generated Bytecode
    LdaTrue
    TestEqual r0
    JumpIfFalse else_label
    ...
    Call r0, r1, #2, [1]
    Jump end_label
else_label:
    ...
    Call r0, r1, #2, [5]
end_label:
```

`If..else` is the simplest control flow structure emitted by the generator. For the more complex control flow structures like loops and breakable blocks, the `BytecodeGenerator` employs control flow builders and control scopes.

All loops use instances of the same control flow builder, the `LoopBuilder`. The `LoopBuilder` has a set of labels for the loop condition, the loop header, the target of continue/next, and the end

Commented [WW58]: 不了解 for in 的同学去查下 ECMA262 或 ES6。
Python 也有类似语言定义。可以触类旁通。

Commented [WW59]: 此段刚开始看可能有点绕。实现和原理都非常简单。后续看到具体的代码就可以理解了。重要的思维方法是有一个「当前生成到哪里了」的指针。这个指针的值，就是指向某个字节码的位置，可以作为跳转的目标地址。
指针是递增的，有算法可以在这个约束下，将所有的跳转的地址都不需要回溯的填上。

Commented [WW60]: 理解这个例子的要点，是想象着有一个数组，字节码在一个一个的被填入，数组的 size 就是上文说到的指针。每次调用 bind，都可以理解为实时的读取了当前的 size 的值。

of the loop. The visitor for the loop statement establishes the label positions of the loop header, loop condition, and the of end loop. It also visits the statements in the iteration body by calling `BytecodeGenerator::VisitIterationBody`. This visitor body instantiates a `ControlScopeForIteration` and pushes this onto a stack in the `BytecodeGenerator` and then visits the statements in the loop body. If a break statement or continue statement is hit, the visitor for these statements peeks at the current `ControlScopeForIteration` and signals a break (or continue). The control scope then invokes the appropriate method on its associated `LoopBuilder` which emits the appropropriate jumps to its labels.

The `ControlFlowBuilder` classes ensure the emitted bytecode has the same control flow order as the source code. This natural ordering means the only time backwards branches are emitted in bytecode is in loop statements returning to the head of the loop. The ordering means no additional loop analysis need be performed when converting from bytecode form into TurboFan compiler graph form. The branch analysis for graph building need only identify the sites of jumps and their targets.

Exception Handling

TODO

Interpreter Code Execution

The Ignition interpreter is a register-based, indirect threaded interpreter. The entry point to the interpreter from other JavaScript code is the `InterpreterEntryTrampoline` builtin stub. This builtin sets up an appropriate stack frame, and loads reserved machine registers (e.g., the bytecode pointer, interpreter dispatch table pointer) with the appropriate values before dispatching to the first bytecode of the called function.

Stack frame layout and reserved machine registers

An example of the layout of a stack frame for the interpreter is shown below:

Commented [WW61]: 这里的 Scope 对应着每次循环体内声明的变量的生存周期。循环体内的变量，每次迭代（iteration），都是新的。虽然在具体的编译器实现中会重复使用同一个地址（因为生存周期没有重叠所以可以重复使用同一块内存）但是在概念上是无关的变量。

Commented [WW62]: 怀疑是 appropriate

Commented [WW63]: 本段无意料之外。构建控制流的时候，注意回边。在编译器工程中有时候通过相对偏移跳转的正负来判定是否回边，这个时候就需要保证一定的 ordering。

Commented [WW64]: TODO：还没看。

Commented [WW65]: TODO 这里的 indirect threaded 需要查询下。

Commented [WW66]: 做一些必须的初始化和现场恢复/保存的工作。

InterpreterEntryTrampoline 的代码直接 grep 不好找。被

```
36 #define BUILTIN_CODE(isolate, name) \
 37
(isolate)->builtins()->builtin_handle(Bui
ltins::k##name)
```

包裹起来了。

本体是 builtin_handle 函数，

src/builtins/builtins.cc

又被转换成

```
148 Handle<Code> Builtins::builtin_handle(int index) {
149   DCHECK(IsBuiltinId(index));
150   return Handle<Code>(
151
reinterpret_cast<Address*>(isolate_->heap()->builtin_a
ddress(index)));
152 }
```

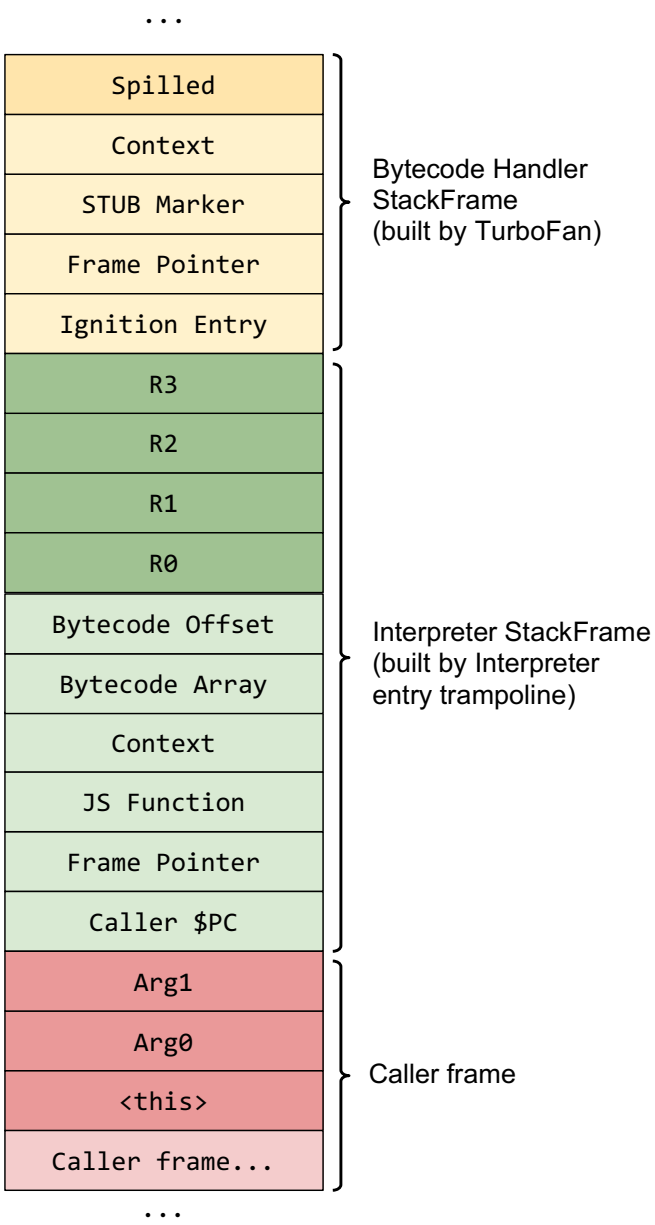
再看通过 CSA 进入

Builtins::Generate_InterpreterEntryTrampoline

就有各个 arch 自己根据 ABI 决定了。

TODO 后续在 PPT 中说明。

（commit 15ec4a09）


...
Spilled
Context
STUB Marker
Frame Pointer
Ignition Entry
Bytecode Handler StackFrame (built by TurboFan)
R3
R2
R1
R0
Bytecode Offset
Bytecode Array
Context
JS Function
Frame Pointer
Caller $PC
Interpreter StackFrame (built by Interpreter entry trampoline)
Arg1
Arg0
<this>
Caller frame...
Caller frame
...

The interpreter stack frame is built by the `InterpreterEntryTrampoline` builtin stub, which pushes the fixed frame values (Caller PC, Frame Pointer, JSFunction, Context, Bytecode Array and Bytecode Offset) and then allocates space in the stack frame for the register file (the `BytecodeArray` object contains an entry which tells the builtin how large a stack frame the function requires). It then writes `undefined` to all the registers in this register file, which ensures the GC doesn't see any invalid (i.e., non-tagged) pointers when it walks the stack.

The `InterpreterEntryTrampoline` builtin stub initializes some fixed machine registers which are used by the interpreter, namely:

Commented [WW67]: TODO 这部分需要后续挑选一个后端看看。移植到 RISC-V 后端应该需要实现对应的函数。

- `kInterpreterAccumulatorRegister`: stores the implicit accumulator interpreter register
- `kInterpreterBytecodeArrayRegister`: points the the start of the `BytecodeArray` object which is being interpreted.
- `kInterpreterBytecodeOffsetRegister`: the current offset of execution in the `BytecodeArray` (in essence the bytecode PC).
- `kInterpreterDispatchTableRegister`: points to the interpreter's dispatch table, used to dispatch to the next bytecode handler.
- `kInterpreterRegisterFileRegister`: points to the start of the register file (will soon be removed now that TurboFan can use the parent frame pointer directly).
- `kContextRegister`: points to the current context object (will soon be removed and all access will be via interpreter register `Register::current_context()`

It then *calls* the bytecode handler for the first bytecode in the bytecode array stream. The registers initialized above are available to the bytecode handlers as TurboFan parameter nodes, due to the fact that the calling convention for bytecode dispatch specifies associated parameters for each of the fixed machine registers.

If the bytecode handler is simple and doesn't call any other function (other than the tail call in the dispatch operation) then TurboFan can elide stack frame creation for this bytecode handler. However, in more complex bytecode handlers, TurboFan will build a new `STUB frame` when execution enters the bytecode handler. Other than the required fixed frame, this frame only stores machine register values spilled by TurboFan (e.g., callee saved registers across a call). All the state associated with the interpreted function lives in the parent interpreted frame.

Interpreter Register Access

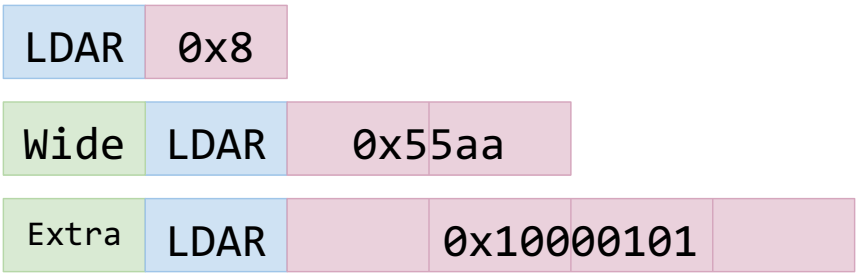
As described above, all local variables and temporaries will be allocated to specific registers within the function's *register file,* on its stack frame, during bytecode generation. These variables, as well as the function's arguments, can be accessed by bytecode handlers as interpreter registers. The register on which a bytecode handler should operate is specified as a bytecode operand in the bytecode stream. The interpreter loads the operand from the bytecode stream to get the register index on which it should operate. This index is a word-based offset from the start of the register file, which can be either positive (to access which live above the register file on the stack, such as function arguments) or negative (to access locals allocated by the function itself on its register file). The interpreter accesses a given register by scaling the operand's index to be a byte offset, and then loading from or store to the memory location at `kInterpreterRegisterFileRegister + offset`.

Wide operands

Commented [WW68]: 这里在阅读上没什么好讲的。ISA设计权衡&技巧。

Minimizing the size of generated bytecode is a major motivator for Ignition so the bytecode format is able to support operands of different widths. Ignition uses prefix special bytecodes to support wider operands. Ignition supports fixed width operand types and scalable width operands. The scalable width operands increase in size proportional to the prefix bytecode.

The fixed width operands are for runtime call identifiers (16-bits) and flag operands (8-bits). Register operands, immediate value operands, index value operands, and register count operands all have a base size of 8-bits and are scalable up to 32-bits wide.



The effects of prefix bytecodes on the load accumulator bytecode

The `Wide` prefix bytecode doubles the width of scalable operands making them 16-bits wide, and the `ExtraWide` prefix quadruples the width to 32-bits.

To support dispatching prefixed, the bytecode dispatch table has entries for the scalable operands offset by 256 for each scaling so indices between 0-255 correspond to unscaled bytecodes, 256-511 correspond to bytecodes with the `Wide` prefix, and 512-767 correspond to bytecodes with the `ExtraWide` prefix.

Earlier Ignition designs, placed wide bytecodes into the same 8-bit space limiting the number of free bytecodes that could be used for other purposes such as bytecode specialization. The designs also had an elaborated register translation scheme for accessing wide register operands as it was not feasible to have scaled versions of all bytecodes. Using prefix bytecodes is cleaner and the overhead of 1-byte prefix is only incurred when used and the size overhead was less than 1% on the Octane-2.1 benchmark.

JS Calls

Calls to other JS functions are handled by the `Call` bytecode. The `BytecodeGenerator` ensures that the arguments which will be passed to the call are in a contiguous set of registers. It then emits `Call` bytecode with a register operand specifying the register which holds the callee, a second register operand specifying the register which holds the start of the arguments to be passed to the call, and a third operand which specifies the number of arguments to pass.

The interpreter `Call` bytecode handler updates the *bytecode offset* stack slot in the interpreted frame with the current bytecode offset. This allows stack walking code to calculate the bytecode pc for frames on the stack. It then calls the `InterpreterPushArgsAndCall` builtin, passing the callee, the memory location of the first argument register, and the number of arguments. The `InterpreterPushArgsAndCall` builtin then copies the argument values from the range <first_arg_register>...<first_arg_register + count> and pushes these values to the end of the stack. It then invokes the `Call` builtin, which is the same builtin used by full-codegen to evaluate the target address to call for the given callee. Calls to other interpreted functions are treated the same as calls to JITed functions - the `Call` builtin will load the `JSFunction`'s code entry field, which will point to the `InterpreterEntryTrampoline` builtin stub for interpreted functions, and thus the call will invoke the `InterpreterEntryTrampoline` and reenter the interpreter.

When an interpreted function returns, the interpreter tail calls the `InterpreterExitTrampoline` builtin stub, which tears-down the stack frame and restore control to the calling function, returning the value held in the accumulator at the time of the `return` bytecode as the return value.

Property loads / stores

Commented [WW69]: 后面会专门讲解。
TODO 讲解视频 URL

Bytecodes which load or store property on JS objects do so via inline caches (ICs). The bytecode handlers call the same LoadIC and StoreIC code stubs as are used by JITed code (e.g., full-codegen). Since the Load/StoreICs no longer patch code (instead operating directly on the `TypeFeedbackVector`) this means the same ICs can be used for both JITed code and Ignition.

The bytecode handler passes the function's `TypeFeedbackVector`, to the appropriate IC stub along with the type feedback slot associated with the AST node of the operation. The IC stub can update entries in the `TypeFeedbackVector` in the same manner as it does now for the full-codegen compiler, enabling learning of type feedback for later use by the optimizing compiler.

Binary ops

For BinaryOps and other UnaryOps (e.g., `ToBoolean`) full-codegen currently employs ICs which patch machine code. We can't use these ICs in Ignition since they would patch the bytecode handler code (which is not a function specific call site). As such, we currently don't collect any type feedback for Binary or Unary operations.

Commented [WW70]: TODO 需要看下现在的代码实现。

At present, most Binary and Unary ops call runtime functions which perform the given operations. We are in the process of replacing these calls with calls to stubs written using the `CodeStubAssembler` which perform the common cases inline, only calling out to the runtime for more complex situations. Currently we call these stubs, however since they are written using the `CodeStubAssembler`, we will be able to inline the code directly into the interpreter's bytecode handlers easily.

As a future optimization, we may employ back-patching of the actual bytecodes to point to specialized bytecode handlers for a given operation (e.g., inlining `SmiAdd` as a specific bytecode),

however this is dependent upon the decision we end up making regarding collection of type feedback for BinaryOps and UnaryOps in Ignition.

TurboFan Bytecode Graph Builder

The `BytecodeArray` built by the `BytecodeGenerator` contains all the information necessary to be fed into Turbofan and built a TurboFan compilation graph. This is implemented by the `BytecodeGraphBuilder`.

The `BytecodeGraphBuilder` starts by walking the bytecode to perform some basic branch analysis, which finds backward and forward branch target bytecodes. This is used to set up the appropriate loop header environments in the `BytecodeGraphBuilder` while building the TurboFan graph.

`BytecodeGraphBuilder` then walks the `BytecodeArray` again, visiting every bytecode and calling a bytecode specific visitor for each. The bytecode visitor functions read the bytecode operands from the `BytecodeArray`, and then add operations to the TurboFan graph to perform the operation of that bytecode. Many bytecodes have a direct mapping to an existing `JSOperator` in TurboFan.

The `BytecodeGraphBuilder` maintains an environment which tracks which nodes represent to value stored in each of the registers in the `BytecodeArray`'s register file (including the accumulator register). The environment also tracks the current context object (which is updated due to `Push/PopContext` bytecodes). When the visitors visit a bytecode which reads a value from register, it looks up the node in the environment associated with that register, and uses this as the input node to the `JSOperator` node being built for the current bytecode. Similarly, for operations which output a value to a register or the accumulator, the visitor will update the register in the environment with the new node when it is complete.

Deoptimization

Javascript operations can trigger deoptimizations from optimized code back to the unoptimized bytecode during execution. In order to support deoptimization, the `BytecodeGraphBuilder` needs to keep track of the state of the interpreter's stack frame so that it can rebuild the interpreter stack frame on deoptimization and re-enter the function at the deoptimization point.

The environment already tracks this, by keeping track of the nodes associated with each register at each point of the bytecode execution. The `BytecodeGraphBuilder` checkpoints this information in a `FrameState` node for `JSOperator` nodes which could trigger a deoptimization (either eager - with the checkpoint based on the state before the node executes, or lazy - with the checkpoint after the node has executed).

Since each bytecode maps to a single `JSOperator` node, this means that we will only ever deoptimize to a point immediately before or immediately after a bytecode. As such, we use the

bytecode offset as the `BailoutId` in the deoptimization points. When TurboFan generates code to deal with potential deoptimization, it serializes a `TranslatedState` record which describes how to rebuild an interpreted frame for this deoptimization point. This is based on `FrameState` node for this deoptimization point. When this deopt point is hit, the interpreted frame is built using this `TranslatedState` record, and the runtime re-enters into the interpreter at the appropriate bytecode offset using the `InterpreterNotifyDeoptimized` builtin.

Debugging support

To enable the debugger breakpoint code executed by the interpreter, the debugger copies the `BytecodeArray` object of the function, and then replace any bytecode which is the target of a breakpoint with a special `DebugBreak` bytecode. Since all bytecodes are at least one byte, no extra space needs to be allocated in the bytecode stream to ensure breakpoints can be set. There are variants of the `DebugBreak` bytecode for each size of bytecodes to ensure that the `BytecodeArray` can still be iterated correctly. There are also `DebugBreakWide` and `DebugBreakExtraWide` bytecodes, which as well as acting as breakpoints, also prefix the next bytecode as being a `Wide` or `ExtraWide` variant (in the same manner as the `Wide` and `ExtraWide` bytecodes on non-breakpoint code).

Once a breakpoint is hit, the interpreter calls into the debugger. The debugger can resume execution by looking at the function's real `BytecodeArray` and dispatching to the actual bytecode which was replaced by the `DebugBreak` bytecode.

More details are available in Debugging support for Ignition design doc.

Source Positions

Source positions are emitted alongside bytecode when bytecodes are emitted by the bytecode generator. There are two types of source position: statement positions and expression positions. Statement positions are used for stepping between statements in the debugger. A statement position occurs a location where the debugger can step to before the bytecode it is associated with is dispatched. Expression positions are used to provide stack trace information when exceptions are raised in expressions. When an exception is raised the debugger scans back from the site of the exception looking for an expression position and this is used in the stack trace.

```
// Javascript with source
positions
function multiply_by_global(n) {
[27]
[50][50]var result = n *
[52]my_global;
[65]return result;
[80]
```

```
// Bytecode with source
positions.
   [27] StackCheck
   [50] Ldar a0
        Star r1
   [52] LdaGlobal [0], [1]
        Mul r1
   [50] Star r0
```

```
Key:    [1] Statement position (offset
1)
```

Source positions in source code and bytecode.

The bytecode generation process needs to maintain source positions for the debugger and exception reporting to work correctly. Any optimizations in the bytecode generation process need to ensure source positions have the same causal ordering. Some expression positions can be elided because they are associated with bytecodes that can not produce an exception or are duplicates.

Profiler Support

TODO

Future work

TODO - describe possible specialized bytecode patching optimizations.
TODO - describe possible super-bytecodes.
TODO - describe type feedback for binary ops.

rel32Appendix A: Table of Bytecodes

Commented [WW71]: 注意，已经跟 v8.x 的代码不符合了。
TODO 更新。

Prefix Bytecodes
Wide
ExtraWide

Accumulator Load
LdaZero
LdaSmi
LdaUndefined
LdaNull
LdaTheHole
LdaTrue
LdaFalse
LdaConstant

Load/Store Globals
LdaGlobal
LdaGlobalInsideTypeof
StaGlobalSloppy
StaGlobalStrict

Context operations
PushContext
PopContext
LdaContextSlot
StaContextSlot

Unary Operators
Inc
Dec
LogicalNot
TypeOf
DeletePropertyStrict
DeletePropertySloppy

Control Flow
Jump
JumpConstant
JumpIfTrue
JumpIfTrueConstant
JumpIfFalse
JumpIfFalseConstant
JumpIfToBooleanTrue
JumpIfToBooleanTrueConstant
JumpIfToBooleanFalse
JumpIfToBooleanFalseConstant
JumpIfNull
JumpIfNullConstant
JumpIfUndefined
JumpIfUndefinedConstant
JumpIfNotHole
JumpIfNotHoleConstant

Load-Store lookup slots
LdaLookupSlot
LdaLookupSlotInsideTypeof
StaLookupSlotSloppy
StaLookupSlotStrict

Register Transfers
Ldar
Mov
Star

LoadIC operations
LoadIC
KeyedLoadIC

StoreIC operations
StoreICSloppy
StoreICStrict
KeyedStoreICSloppy
KeyedStoreICStrict

Binary Operators
Add
Sub
Mul
Div
Mod
BitwiseOr
BitwiseXor
BitwiseAnd
ShiftLeft
ShiftRight
ShiftRightLogical

For..in support
ForInPrepare
ForInDone
ForInNext
ForInStep

Stack guard check
StackCheck

Non-local flow control
Throw
ReThrow
Return

Illegal bytecode
Illegal

Calls
Call
TailCall
CallRuntime
CallRuntimeForPair
CallJSRuntime

Intrinsics
InvokeIntrinsic

New operator
New

Test Operators
TestEqual
TestNotEqual
TestEqualStrict
TestLessThan
TestGreaterThan
TestLessThanOrEqual
TestGreaterThanOrEqual
TestInstanceOf
TestIn

Cast operators
ToName
ToNumber
ToObject

Literals
CreateRegExpLiteral
CreateArrayLiteral
CreateObjectLiteral

Closure allocation
CreateClosure

Arguments allocation
CreateMappedArguments
CreateUnmappedArguments
CreateRestParameter

Debugger Support
DebugBreak0
DebugBreak1
DebugBreak2
DebugBreak3
DebugBreak4
DebugBreak5
DebugBreak6
DebugBreakWide
DebugBreakExtraWide

Appendix B: Reading Material

Threaded Code, in Wikipedia entry, https://en.wikipedia.org/wiki/Threaded_code

Revolutionizing Embedded Software, Kasper Verdich Lund and Jakob Roland Andersen, in Master's Thesis at University of Aarhus, http://verdich.dk/kasper/RES.pdf.

Inside JavaScriptCore's Low-Level Interpreter, Andy Wingo's blog, https://wingolog.org/archives/2012/06/27/inside-javascriptcores-low-level-interpreter

SquirrelFish, David Manelin's blog, https://blog.mozilla.org/dmandelin/2008/06/03/squirrelfish/

Context Threading, Marc Berndl, Benjamin Vitale, Mathew Zaleski, and Angela Demke Brown, in CGO '05: Proceedings of the international symposium on Code generation and optimization, http://www.cs.toronto.edu/syslab/pubs/demkea_context.pdf (also Mathew Zeleski's thesis http://www.cs.toronto.edu/~matz/dissertation/)

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Page 3: [1] Commented [WW21] **Wu Wei** **2/12/20 11:11:00 AM**

虚拟机也好解释器也好，根据操作数的存储和索引方式，可以分为基于寄存器的和基于栈的。Java 虚拟机是基于栈的，经典的语言虚拟机基于栈的比较多，容易实现，体积小，代价是速度可能慢，跟物理 CPU 映射关系不好。

这里解释器的基于寄存器的，跟编译原理/虚拟机原理书中的概念是一致的，在实现上有所差别，后续会看到。

这一段不懂没关系，把黄色的术语记录下来，以后遇到了再反复理解。