VentSys

Variant: [No Variations]

30/11/2022
V1I1

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DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for informational design notes .

DESIGN NOTE:
Example text for critical design notes.

DESIGN NOTE:
Example text for cautionary design notes.

LAYOUT NOTE:
Example text for critical layout guidelines.

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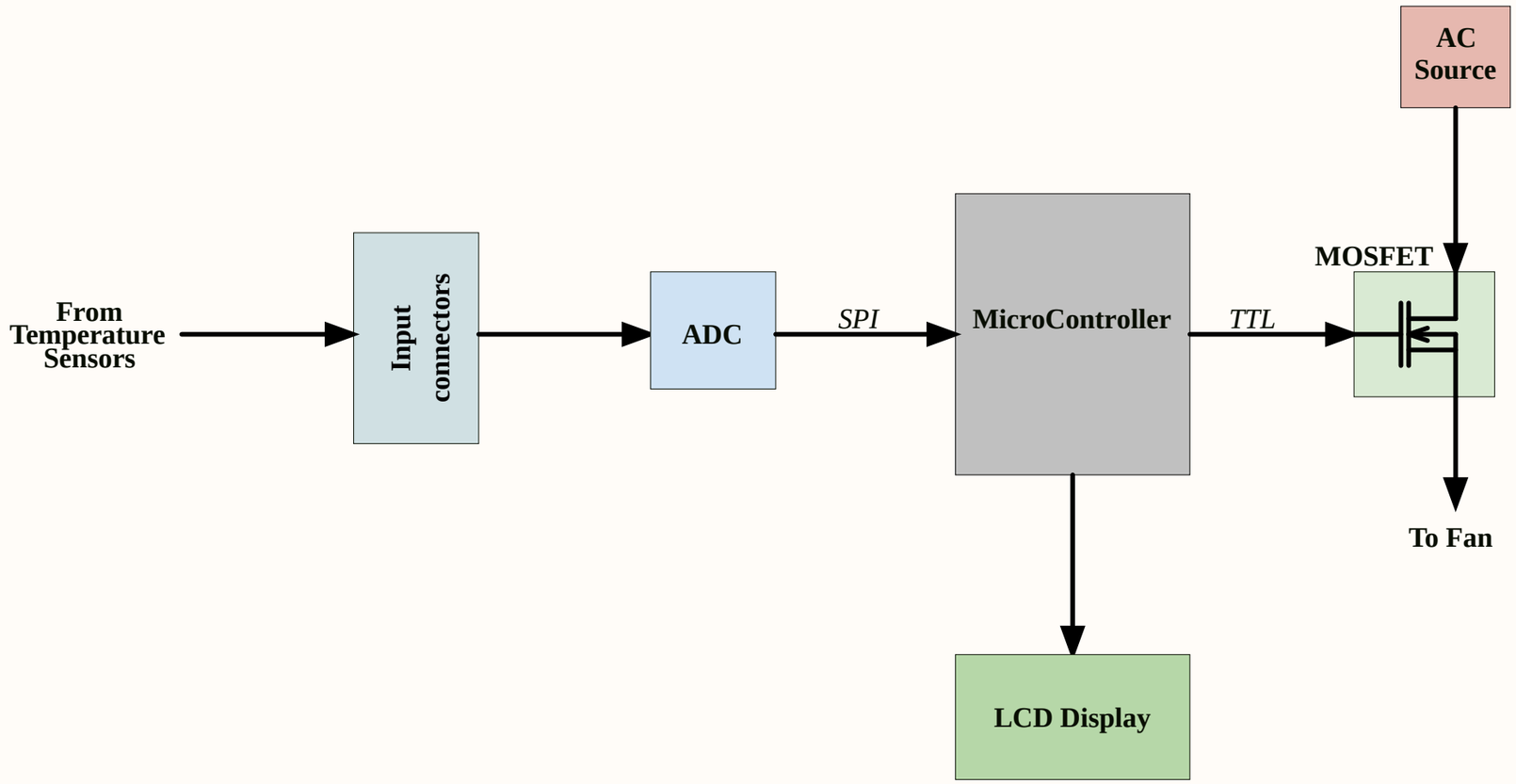
VentSys

(Block Diagram)



CLOCKS (CPU & PCIE)

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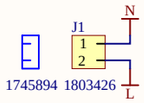
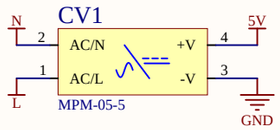
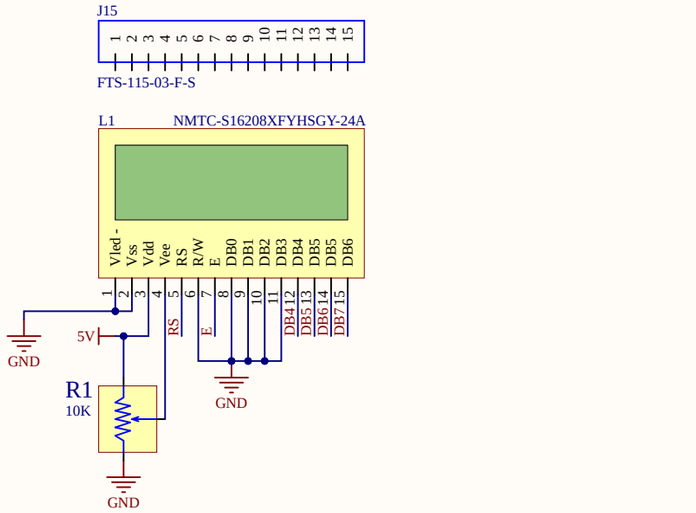
Input Connector (80-250VAC)



AC/DC Converter

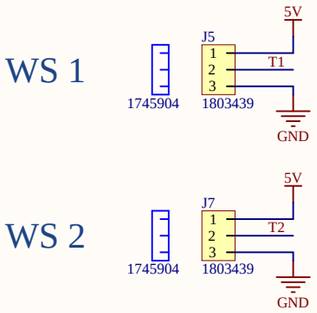


LCD Display

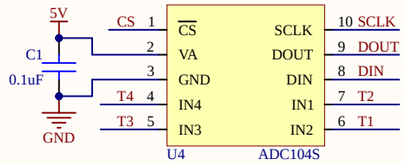


Fiducials

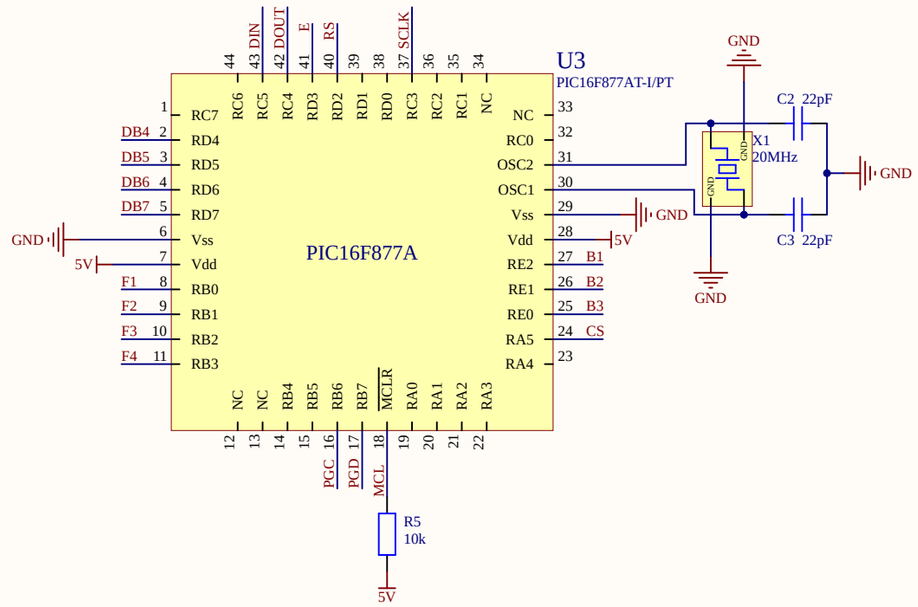
Temperature Sensors



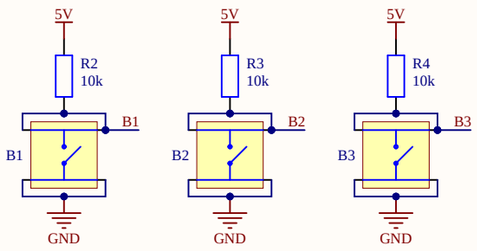
ADC



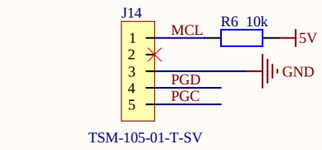
Controller



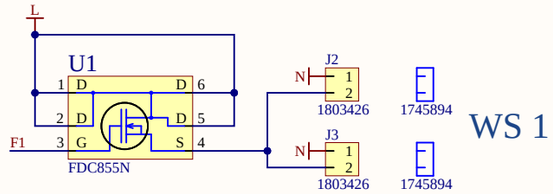
Interface Buttons



OnBoard Programming



Fans Actuators



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DOC: REVISION HISTORY

V1I1 - Nov/2022
First Release

CLOCKS (CPU & PCIE)

Page Content:
[04] - DOC REVISION HISTORY.SchDoc

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