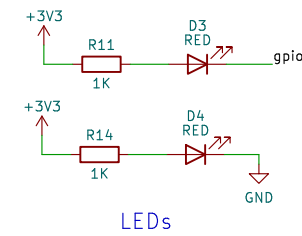
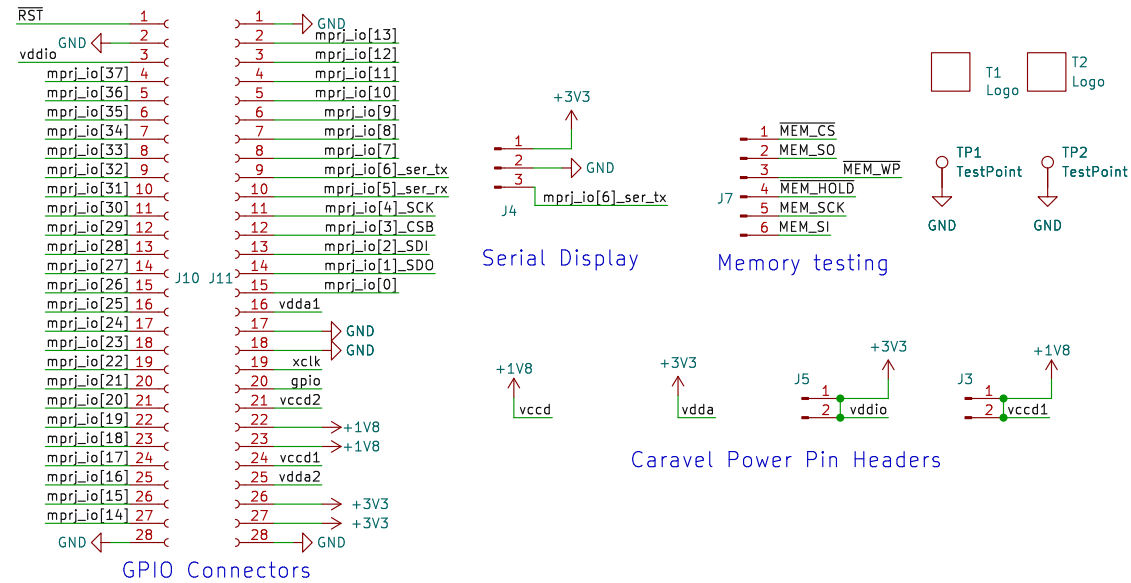
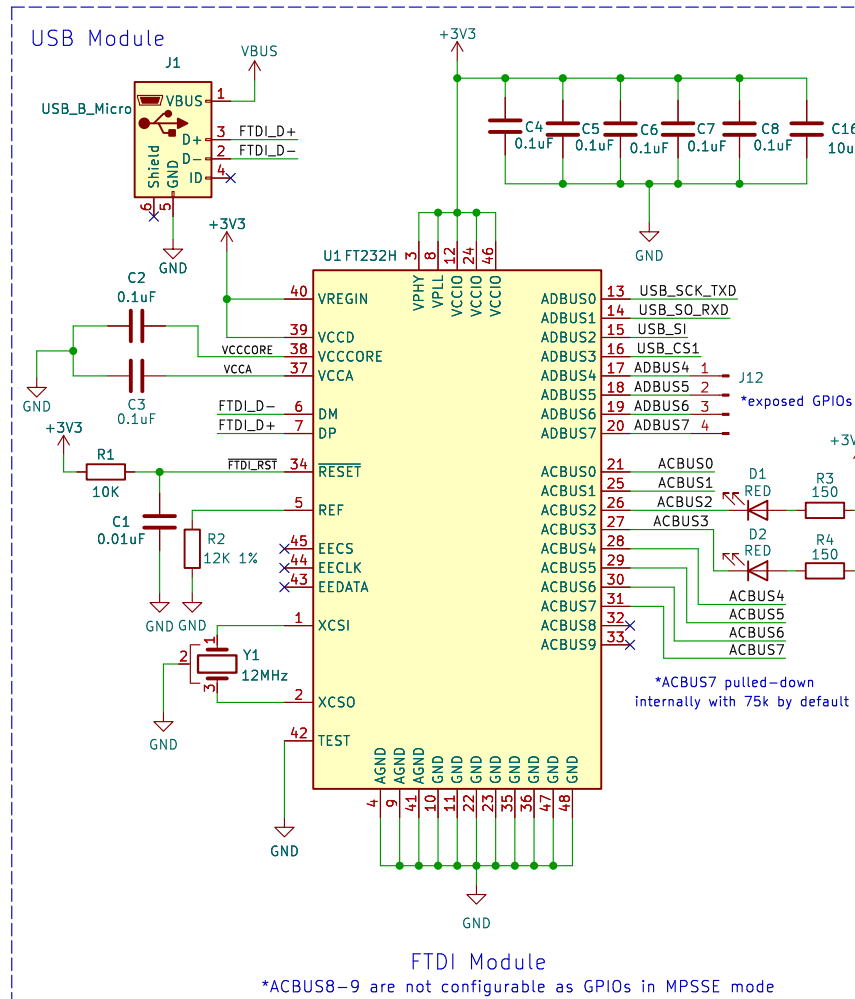


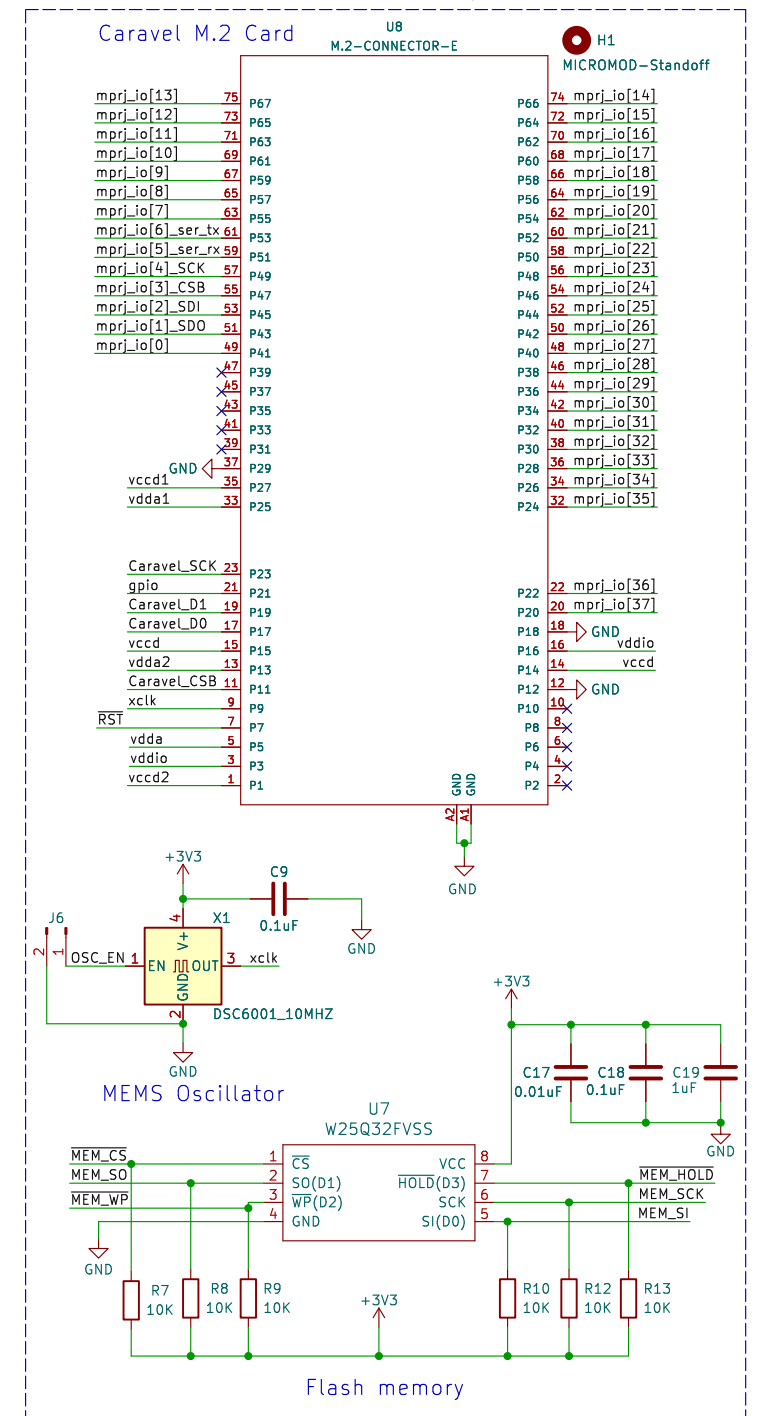
Pin Headers



USB to Serial

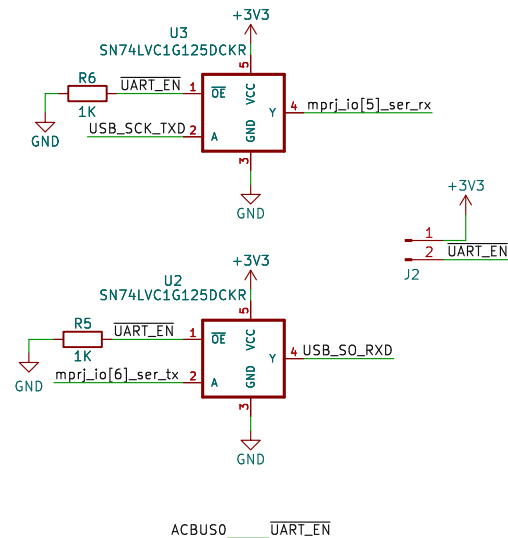


M.2 Connector + Peripherals



Interfaces

*enabled by default and should be disabled in SPI mode

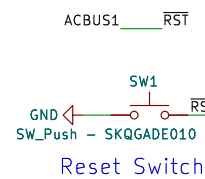


Flash memory interface with Caravel

FTDI SPI interface with HKSPI

Buffers to enable FTDI UART interface with Caravel
*required since the FTDI UART ports are shared with the SPI

Reset



Power

