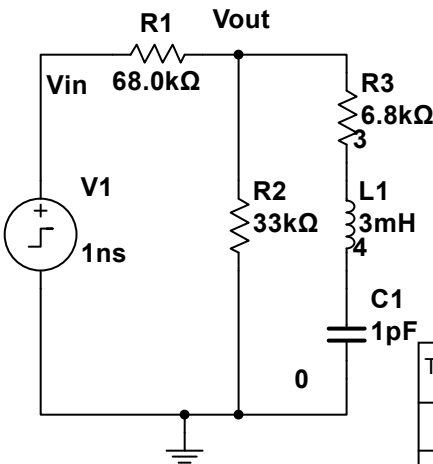



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A	<div><div></div><div></div><div><table><tr><td colspan="3">Title: Homework 4 RLC Circuit Transient Simulation</td></tr><tr><td colspan="3">Circuit2</td></tr><tr><td>Designed by: Caleb Alexander</td><td>Document N:</td><td>Revision:</td></tr><tr><td>Checked by:</td><td>Date: 6/10/2018</td><td>Size: A</td></tr><tr><td>Approved by:</td><td>Sheet 1 of 1</td><td></td></tr></table></div></div>										Title: Homework 4 RLC Circuit Transient Simulation			Circuit2			Designed by: Caleb Alexander	Document N:	Revision:	Checked by:	Date: 6/10/2018	Size: A	Approved by:	Sheet 1 of 1		A
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