



FEATURES

- UL62368-1 recognised
- Typical efficiency from 83%
- Wide temperature performance at full 3 Watt load, -40°C to 85°C
- Industry standard pinout
- 3kVDC isolation (1 minute) 'Hi Pot Test'
- 5V & 12V inputs
- 5V, 9V, 12V & 15V outputs
- No external components required
- No electrolytic or tantalum capacitors
- Pin compatible with MEV1, NMK & NMV series

PRODUCT OVERVIEW

The MEV3 series is a new range of high performance 3W DC-DC converters, offering 3W of available output power in a previously rated 2W package capable of operation over the full industrial temperature range of -40°C to 85°C. Available in an industry standard SIP package, with a pin compatible power upgrade path from the 1W NMV/MEV and 2W NMK series¹, they are ideally suited for providing local supplies on control system boards with the added benefit of 3kVDC galvanic isolation.

SELECTION GUIDE

Order Code	Nominal Input Voltage	Output Voltage	Output Current	Input Current at Rated Load	Load Regulation (Typ)	Load Regulation (Max)	Ripple & Noise (Typ) ¹	Ripple & Noise (Max) ¹	Efficiency (Min.)	Efficiency (Typ.)	Isolation Capacitance	MTTF ²
	V	V	mA	mA	%		mVp-p		%	%	pF	kHrs
MEV3S0505SC	5	5	600	700	6.8	8	39	50	80	83	28	6029
MEV3S0509SC	5	9	333	680	5.5	7	24	35	85.3	87	43	5163
MEV3S0512SC	5	12	250	700	5	7	20	35	81.5	85.5	32	5995
MEV3S0515SC	5	15	200	700	5	6	18	30	85	87.5	40	5426
MEV3S1205SC	12	5	600	300	4.5	6	34	50	81.5	84	34	6063
MEV3S1209SC	12	9	333	280	3.6	5	22	35	85.5	87.5	50	6056
MEV3S1212SC	12	12	250	280	3.2	4.5	16	30	86	88	63	6572
MEV3S1215SC	12	15	200	280	2.8	4	13	30	87	89	70	5754

INPUT CHARACTERISTICS

Parameter	Conditions	Min.	Typ.	Max.	Units
Voltage range	Continuous operation, 5V input types	4.5	5	5.5	V
	Continuous operation, 12V input types	10.8	12	13.2	
Reflected ripple current	5V input types		8	18	mA p-p
	12V input types		6.5	15	

GENERAL CHARACTERISTICS

Parameter	Conditions	Min.	Typ.	Max.	Units
Switching frequency			60		kHz

OUTPUT CHARACTERISTICS

Parameter	Conditions	Min.	Typ.	Max.	Units
Rated Power	T _A = -40°C to 85°C			3	W
Voltage Set Point Accuracy	See tolerance envelope				
Line regulation	High V _{IN} to low V _{IN}		1.01	1.1	%/%

ISOLATION CHARACTERISTICS

Parameter	Conditions	Min.	Typ.	Max.	Units
Isolation test voltage	Flash tested for 1 minute	3000			VDC
Resistance	Viso = 1000VDC	10			GΩ

TEMPERATURE CHARACTERISTICS

Parameter	Conditions	Min.	Typ.	Max.	Units
Specification	All output types, see safety approval section for UL temperature specification	-40		85	°C
Storage		-50		125	
Case Temperature rise above ambient	MEV3S0505SC			37	
	All other types			35	
Cooling	Free air convection				

ABSOLUTE MAXIMUM RATINGS

Lead temperature 1mm from case for 10 seconds	260°C
Wave Solder	Wave Solder profile not to exceed the profile recommended in IEC 61760-1 Section 6.1.3. Please refer to application notes for further information.
Input voltage V _{IN} , MEV05 types	7V
Input voltage V _{IN} , MEV12 types	15V



1. See Ripple & Noise characterisation method.
 2. Calculated using MIL-HDBK-217F FN2 with nominal input voltage at full load.
- All specifications typical at T_A = 25°C, nominal input voltage and rated output current unless otherwise specified.

TECHNICAL NOTES

ISOLATION VOLTAGE

'Hi Pot Test', 'Flash Tested', 'Withstand Voltage', 'Proof Voltage', 'Dielectric Withstand Voltage' & 'Isolation Test Voltage' are all terms that relate to the same thing, a test voltage, applied for a specified time, across a component designed to provide electrical isolation, to verify the integrity of that isolation.

Murata Power Solutions MEV3 series of DC-DC converters are all 100% production tested at their stated isolation voltage. This is 3kVDC for 1 minute.

A question commonly asked is, "What is the continuous voltage that can be applied across the part in normal operation?"

The MEV3 series has been recognised by Underwriters Laboratory for functional insulation. Both input and output should normally be maintained within SELV limits i.e. less than 42.4V peak, or 60VDC. The isolation test voltage represents a measure of immunity to transient voltages and the part should never be used as an element of a safety isolation system. The part could be expected to function correctly with several hundred volts offset applied continuously across the isolation barrier; but then the circuitry on both sides of the barrier must be regarded as operating at an unsafe voltage and further isolation/insulation systems must form a barrier between these circuits and any user-accessible circuitry according to safety standard requirements.

REPEATED HIGH-VOLTAGE ISOLATION TESTING

It is well known that repeated high-voltage isolation testing of a barrier component can actually degrade isolation capability, to a lesser or greater degree depending on materials, construction and environment. The MEV3 series has toroidal isolation transformers, with no additional insulation between primary and secondary windings of enamelled wire. While parts can be expected to withstand several times the stated test voltage, the isolation capability does depend on the wire insulation. Any material, including this enamel (typically polyurethane) is susceptible to eventual chemical degradation when subject to very high applied voltages thus implying that the number of tests should be strictly limited. We therefore strongly advise against repeated high voltage isolation testing, but if it is absolutely required, that the voltage be reduced by 20% from specified test voltage.

This consideration equally applies to agency recognised parts rated for better than functional isolation where the wire enamel insulation is always supplemented by a further insulation system of physical spacing or barriers.

SAFETY APPROVAL

UL62368-1

The MEV3 series has been recognised by Underwriters Laboratory (UL) to UL62368-1 for functional insulation in a maximum ambient temperature of 85°C and/or case temperature limit of 130°C. Case temperature measured on the face opposite the pins. File number E151252 applies.

FUSING

The MEV3 series of converters are not internally fused so to meet the requirements of UL62368-1 an input line fuse should always be used. An anti-surge 2.5A should be used for MEV3S05xxSC models, and an anti-surge 1A should be used for MEV3S12xxSC models. All fuses should be UL approved and rated to at least the maximum allowable DC input voltage.

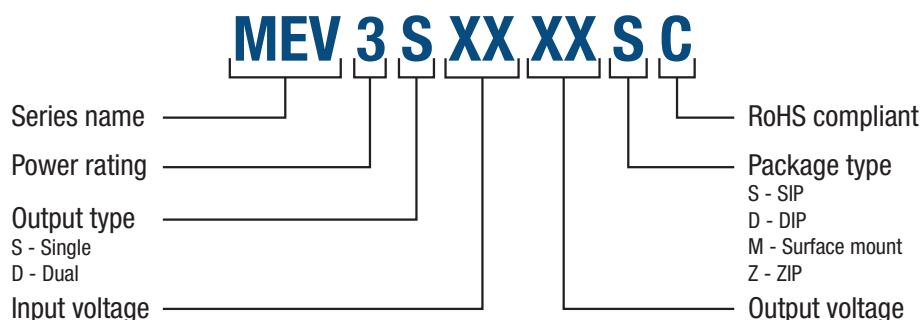
RoHS COMPLIANT INFORMATION



This series is compatible with RoHS soldering systems with a peak wave solder temperature of 260°C for 10 seconds. Please refer to [application notes](#) for further information. The pin termination finish on the SIP package type is Tin Plate, Hot Dipped over Matte Tin with Nickel Preplate. They are backward compatible with Sn/Pb soldering systems.

For further information, please visit www.murata.com/en-global/products/power/rohs

PART NUMBER STRUCTURE



ENVIRONMENTAL VALIDATION TESTING

The following tests have been conducted on this product series, please contact Murata if further information about the tests is required.

Test	Standard	Condition
Temperature cycling	MIL-STD-883.1010, Condition B	10 cycles in a dual zone chamber from -55°C to 125°C. 15 mins dwell at each (inclusive of ramps).
Shock	MIL-STD-883.2002, Condition A	500g 1.0ms half sine, 5 shocks in each direction of 3 mutually perpendicular axes.
Bump	IEC Class 4M5 of ETS 300 019-2-4	Shock Spectrum Type II, 6mS duration, 250m/s ² 500 bumps in 6 directions.
Solderability	IPC/ECA J-STD-002, Test A and A1	SnPb (Test A) For leaded solderability the parts are conditioned in a steam ager for 8 hours ±15 min. at a temperature of 93±3°C. Dipped in solder at 245°C ±5°C for 5 +0/-0.5 seconds. Pb-free (Test A1) For lead free solderability the parts are conditioned in a steam ager for 8 hours ± 15 min. at a temperature of 93±3°C. Dipped in solder at 255°C ±5°C for 5 +0/-0.5 seconds.
Solder heat	JEDEC JESD22-B106	The test sample is subjected to a molten solder bath at 260°C ±5°C for 10 (+2/-0) seconds (96SC tin/silver/copper).
Solvent cleaning	Resistance to cleaning agents	Solvent – Novec 71IPA & Topklean EL-20A. Pulsed ultrasonic immersion 45°C - 60°C.
Solvent resistance	MIL-STD-883K Method 2015	Separate samples subjected to IPA.
ESD	JEDEC JESD22-A114	HBM at 8.0kV.

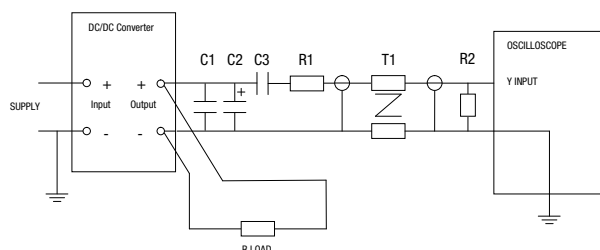
CHARACTERISATION TEST METHODS

Ripple & Noise Characterisation Method

Ripple and noise measurements are performed with the following test configuration.

C1	1 μ F X7R multilayer ceramic capacitor, voltage rating to be a minimum of 3 times the output voltage of the DC-DC converter
C2	10 μ F tantalum capacitor, voltage rating to be a minimum of 1.5 times the output voltage of the DC-DC converter with an ESR of less than 100m Ω at 100 kHz
C3	100nF multilayer ceramic capacitor, general purpose
R1	450 Ω resistor, carbon film, $\pm 1\%$ tolerance
R2	50 Ω BNC termination
T1	3T of the coax cable through a ferrite toroid
RLOAD	Resistive load to the maximum power rating of the DC-DC converter. Connections should be made via twisted wires
Measured values are multiplied by 10 to obtain the specified values.	

Differential Mode Noise Test Schematic



APPLICATION NOTES

Minimum load

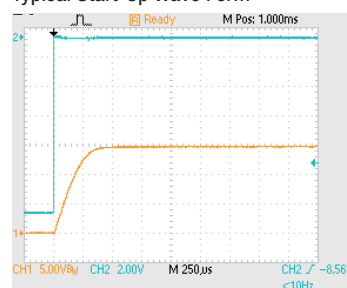
The minimum load to meet datasheet specification is 10% of the full rated load across the specified input voltage range. Lower than 10% minimum loading will result in an increase in output voltage, which may rise to typically double the specified output voltage if the output load falls to less than 5%.

Capacitive loading and start up

Typical start up times for this series, with a typical input voltage rise time of 2.2 μ s and output capacitance of 10 μ F, are shown in the table below. The product series will start into a capacitance of 47 μ F with an increased start time, however, the maximum recommended output capacitance is 10 μ F.

	Start-up time	
	μ s	
MEV3S0505SC	165	
MEV3S0509SC	300	
MEV3S0512SC	650	
MEV3S0515SC	1200	
MEV3S1205SC	75	
MEV3S1209SC	200	
MEV3S1212SC	350	
MEV3S1215SC	410	

Typical Start-Up Wave Form



APPLICATION NOTES (Continued)

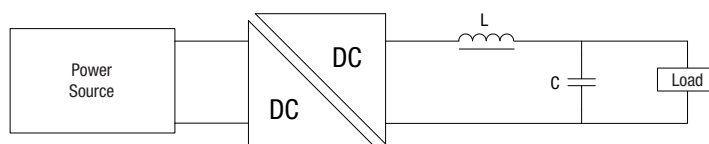
Output Ripple Reduction

By using the values of inductance and capacitance stated, the output ripple at the rated load is lowered to 5mV p-p max.

Component selection

Capacitor: It is required that the ESR (Equivalent Series Resistance) should be as low as possible, ceramic types are recommended. The voltage rating should be at least twice (except for 15V output), the rated output voltage of the DC-DC converter.

Inductor: The rated current of the inductor should not be less than that of the output of the DC-DC converter. At the rated current, the DC resistance of the inductor should be such that the voltage drop across the inductor is <2% of the rated voltage of the DC-DC converter.

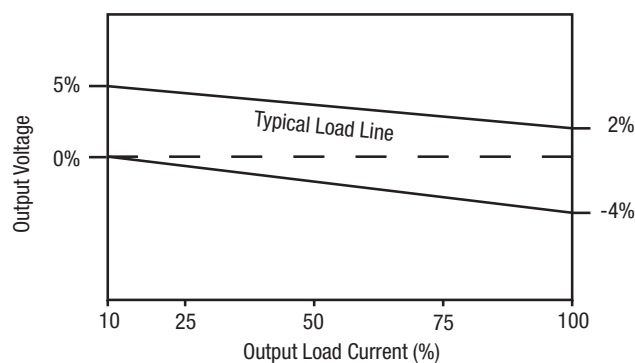


	Inductor			Capacitor
	L, μ H	SMD	Through Hole	C, μ F
MEV3S0505SC	10	84103C	11R103C	4.7
MEV3S0509SC	22	84223C	11R223C	2.2
MEV3S0512SC	47	84473C	11R473C	1
MEV3S0515SC	47	84473C	11R473C	1
MEV3S1205SC	10	84103C	11R103C	4.7
MEV3S1209SC	22	84223C	11R223C	2.2
MEV3S1212SC	47	84473C	11R473C	1
MEV3S1215SC	47	84473C	11R473C	1

OUTPUT VOLTAGE TOLERANCE ENVELOPE

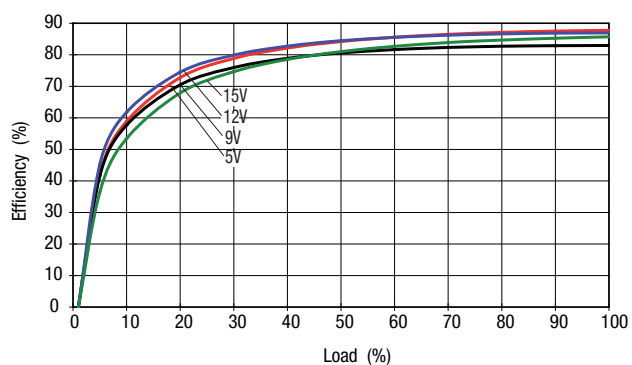
The voltage tolerance envelope shows typical load regulation characteristics for this product series. The tolerance envelope is the maximum output voltage variation due to changes in output loading.

All versions

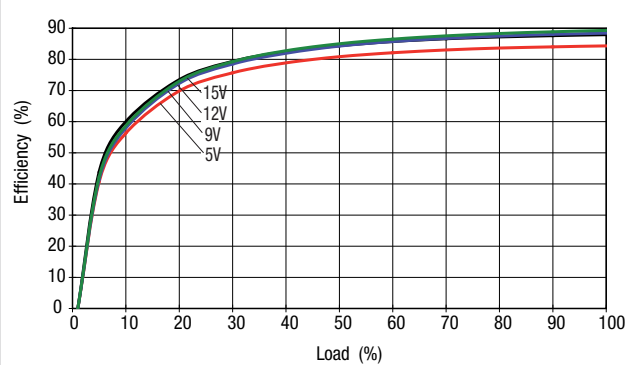


EFFICIENCY VS LOAD

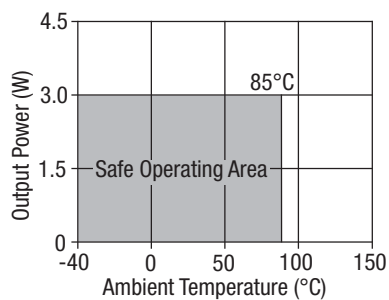
MEV3S05XXSC Input Voltage



MEV3S12XXSC Input Voltage



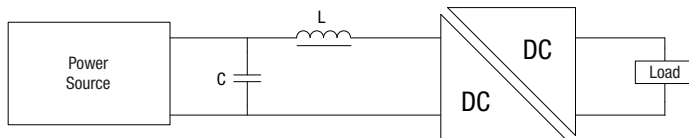
TEMPERATURE DERATING GRAPH



EMC FILTERING AND SPECTRA

FILTERING

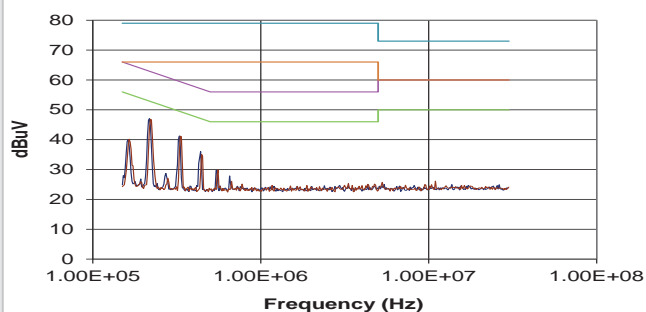
A input capacitor and inductor is required to meet EN 55022 Curve B, Quasi-Peak EMC limit, as shown in the following plots.



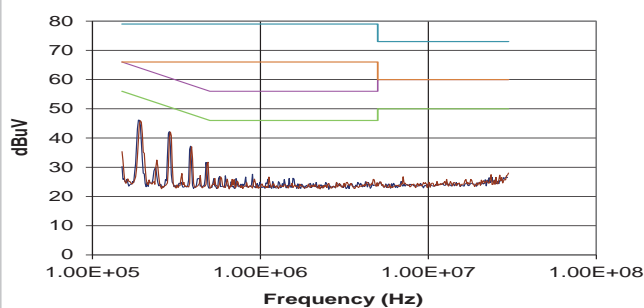
C 2.2 μ F ceramic capacitor

L 10 μ H inductor (TH - 11R103C or SMT - 82103C)

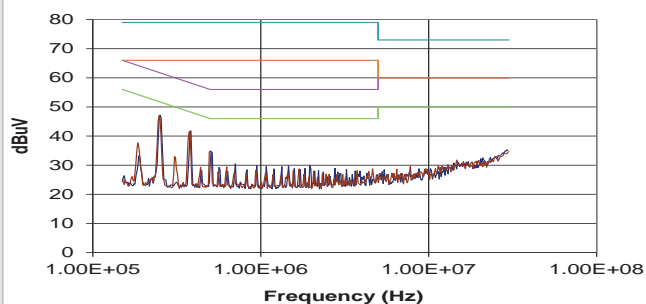
MEV 3S0505SC



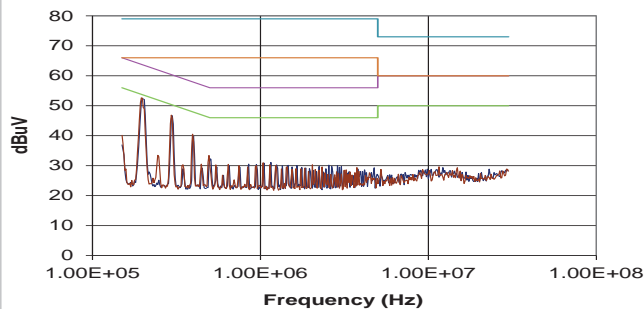
MEV3S0509SC



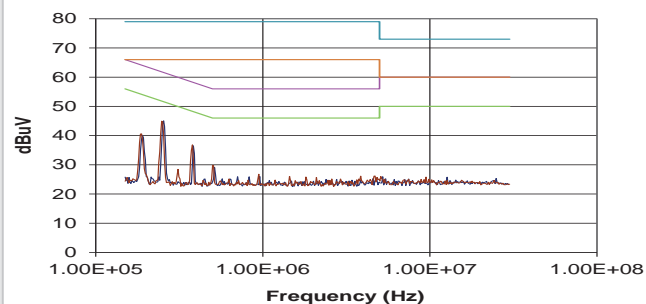
MEV3S0512SC



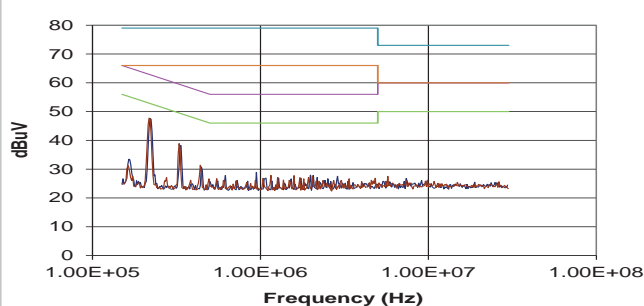
MEV3S0515SC



MEV3S1205SC

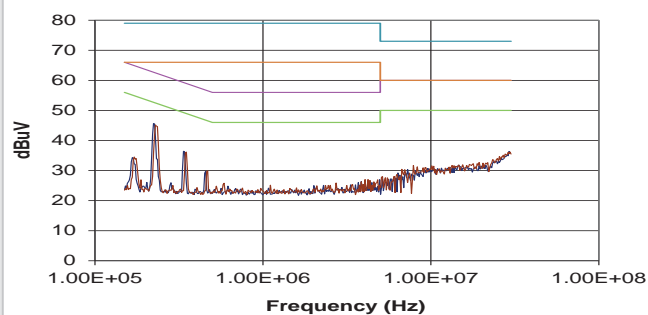


MEV3S1209SC

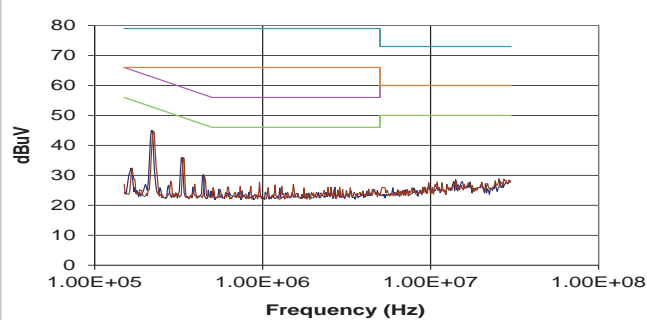


EMC FILTERING AND SPECTRA (Continued)

MEV3S1212SC

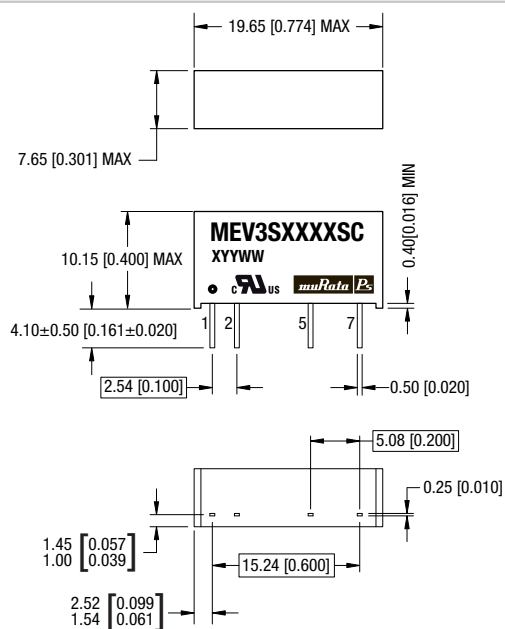


MEV3S1215SC



PACKAGE SPECIFICATIONS

MECHANICAL DIMENSIONS



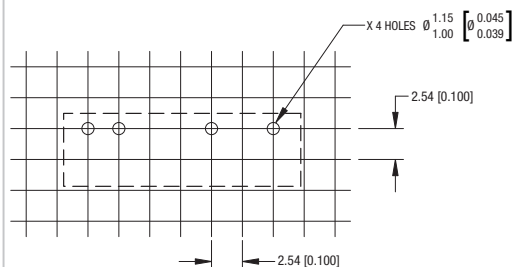
Unless otherwise stated all dimensions in mm (inches) $\pm 0.05\text{mm}$ (0.002").
Controlling dimension is mm.
All pins on a 2.54mm (0.100") pitch and within 0.25mm (0.010") of true position.

Weight: 2.9g

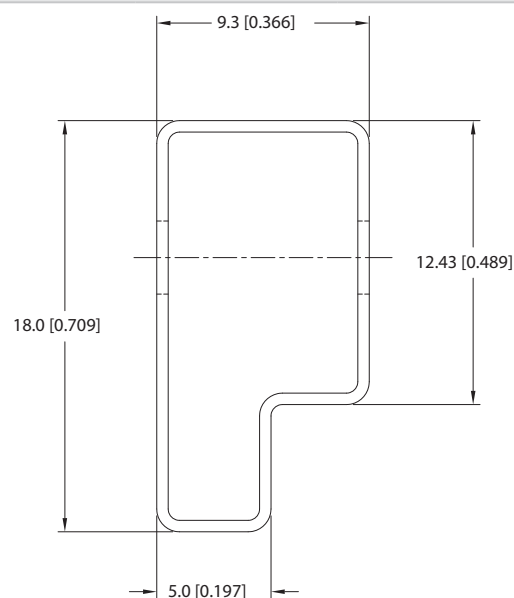
PIN CONNECTIONS

Pin	Function
1	+VIN
2	-VIN
5	-VOUT
7	+VOUT

RECOMMENDED FOOTPRINT DETAILS



TUBE OUTLINE DIMENSIONS



Unless otherwise specified all dimensions in mm (inches) $\pm 0.05\text{mm}$ [0.002].
Tube Length : 520mm [20.472] ± 2.0 [0.079].

Tube Quantity : 25

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Refer to: <https://www.murata.com/en-eu/products/power/requirements>

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