

## 5 Module Schematics

This is the reference design of the module.

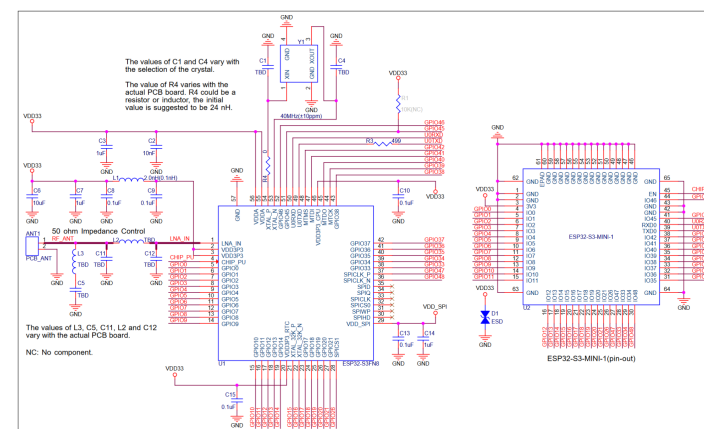
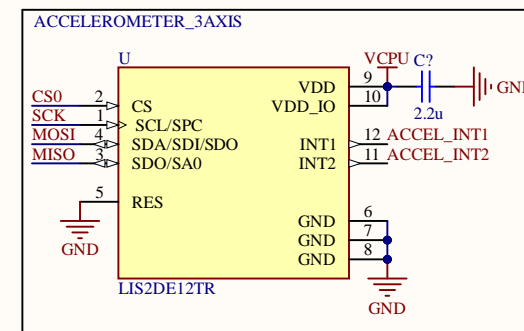
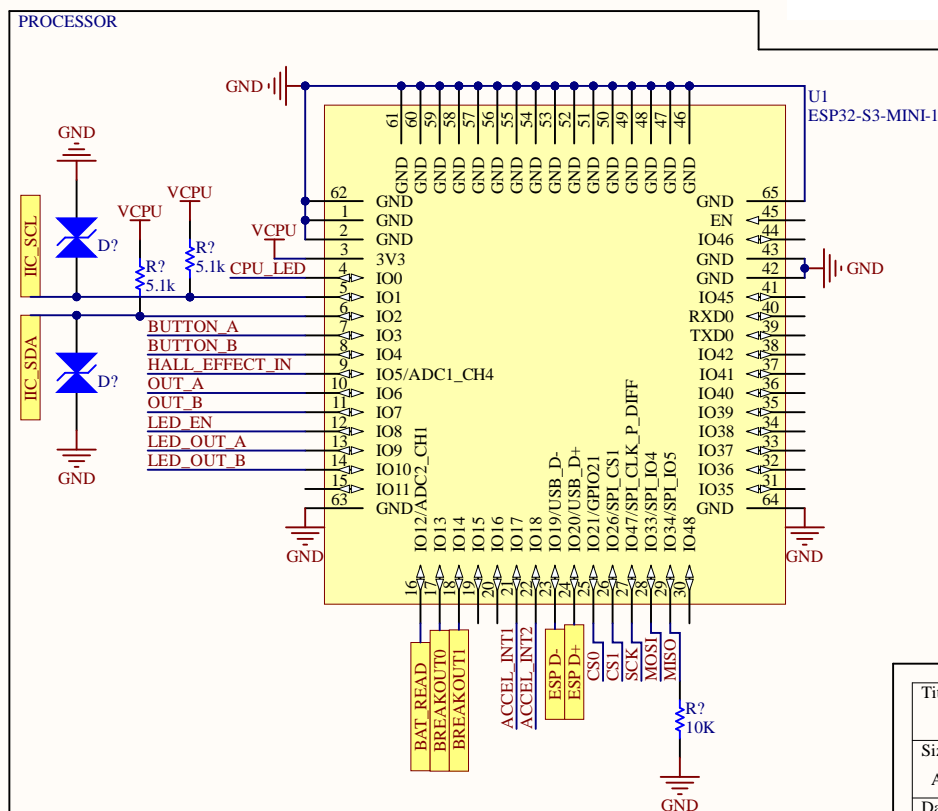
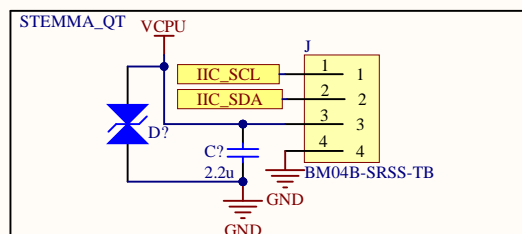
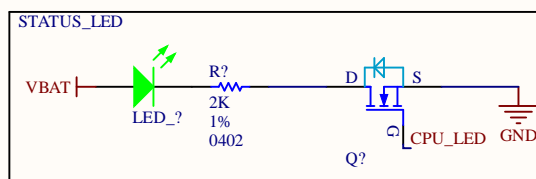
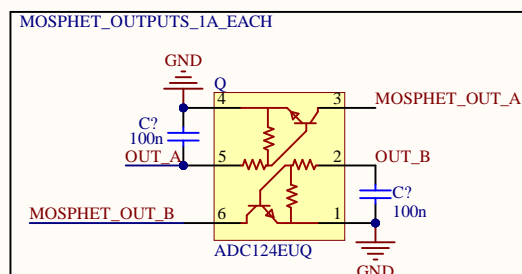
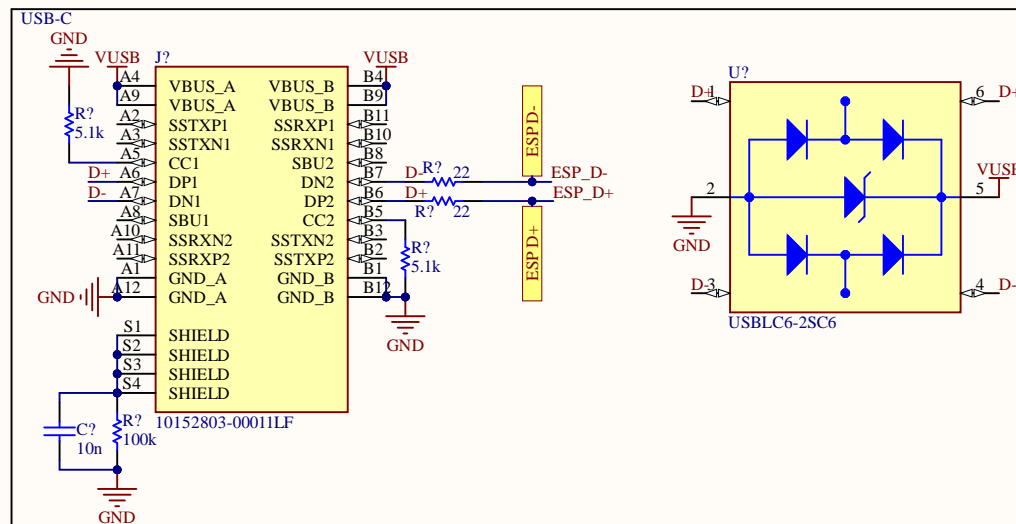
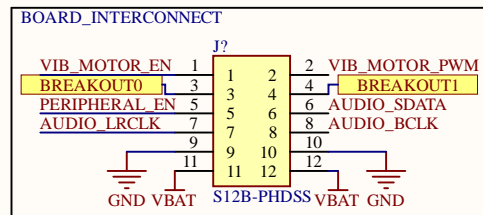
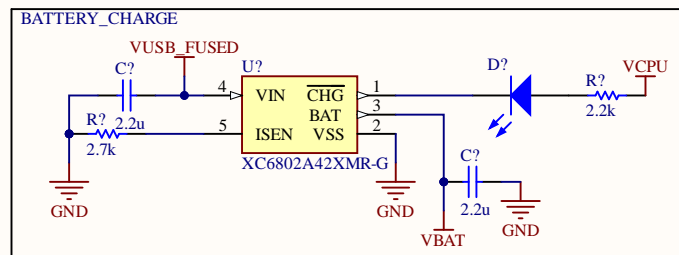
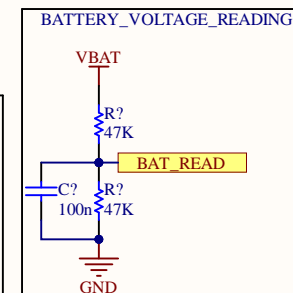
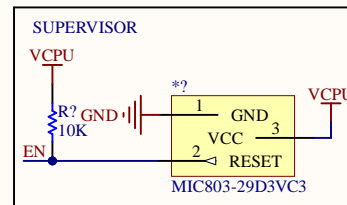
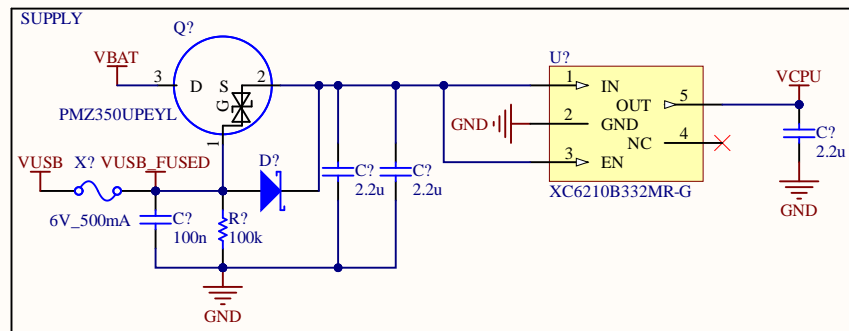


Figure 5: ESP32-S3-MINI-1 Schematics



<div> <div>Title</div> <div>Calimotion 7.0 CPU and Sensors</div> </div>		
Size	Number	Revision
A4		1.0
Date:	6-17-2024	Sheet 1 of 2
File:	C:\Users\...\Calimotion 7.SchDoc	Drawn By: Marc-Olivier Beaulieu



Title			<b>Calimotion 7.0 Power Delivery</b>		
Size	Number			Revision	
A4				1.0	
Date:	6-17-2024			Sheet 2 of 2	
File:	C:\Users\...\Power_Delivery.SchDoc			Drawn By: Marc-Olivier Beaulieu	