

## 5 Module Schematics

This is the reference design of the module.

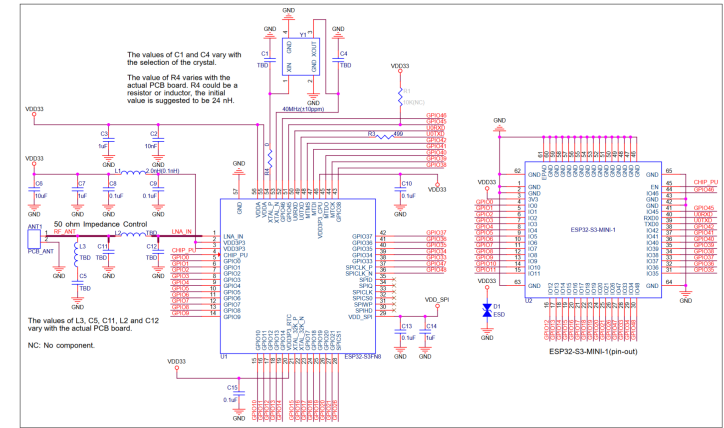
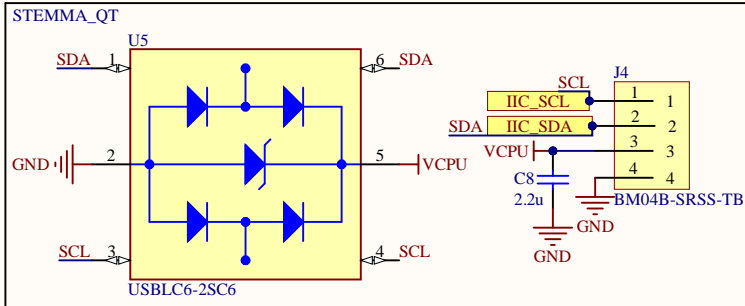
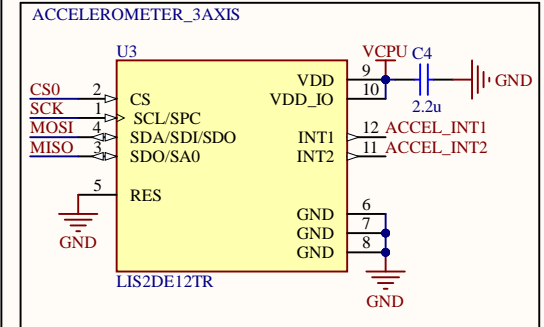
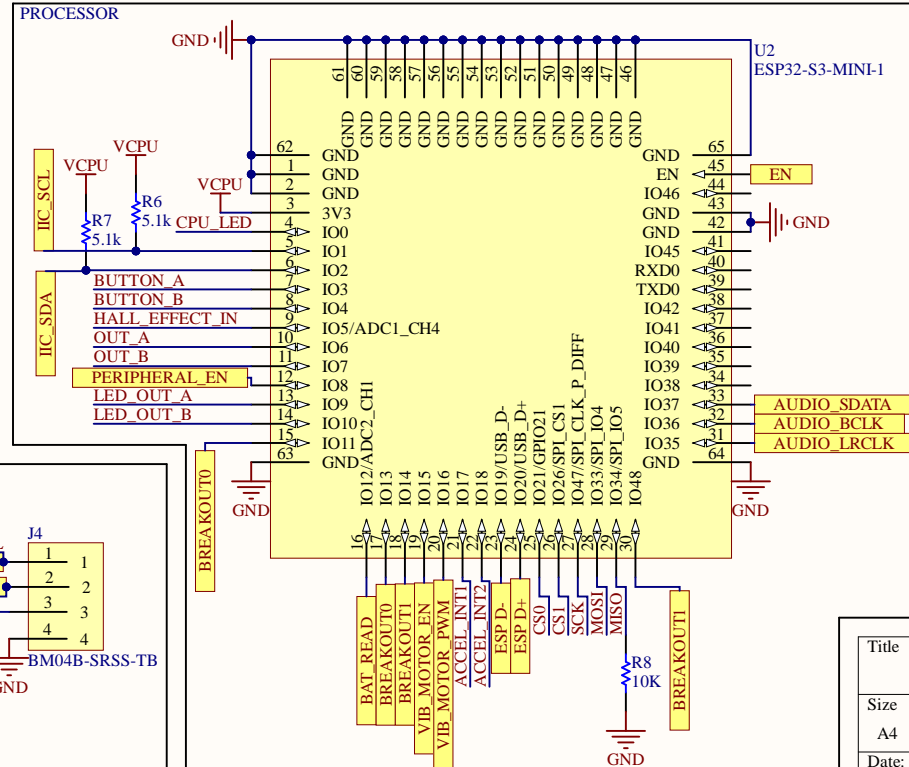
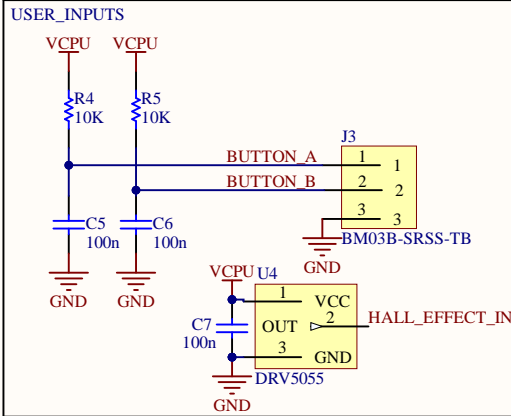
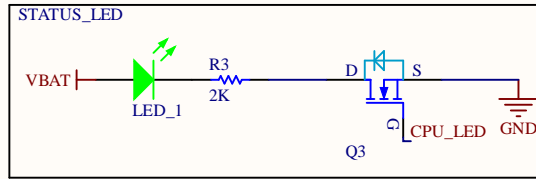
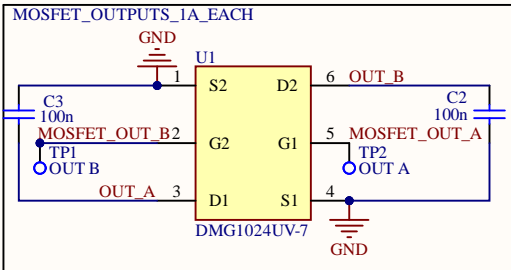


Figure 5: ESP32-S3-MINI-1 Schematics



## Calimotion 7.0 CPU and Sensors

Title	Calimotion 7.0 CPU and Sensors		
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Revision	1.0	Date:	6-19-2024
File:	C:\Users\...\Calimotion 7.0.SchDoc	Sheet 1 of 2	
Drawn By:	Marc-Olivier Beaulieu		

