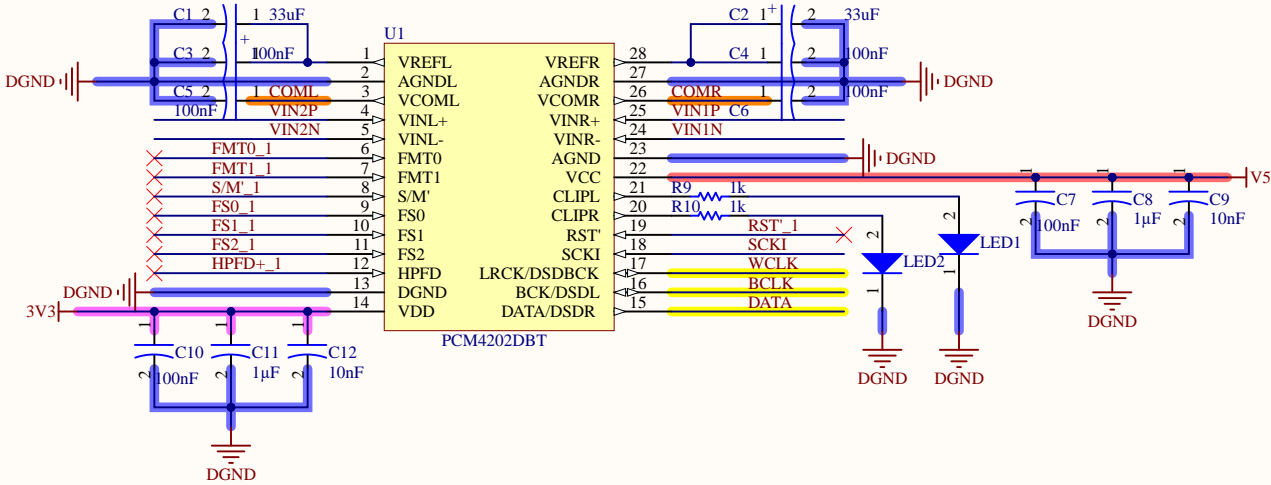
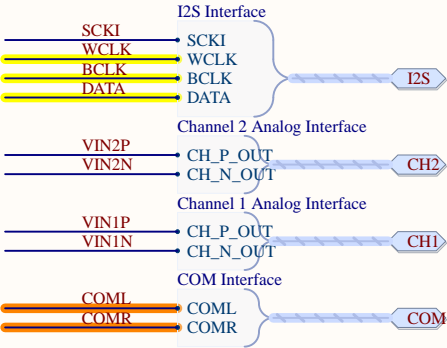
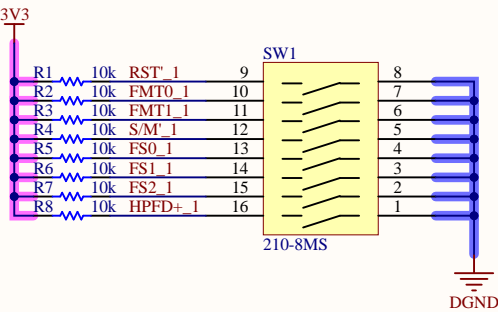


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| Project | | | 31/10/2020 | |
| Author | Organisation | Sheet | Revision | |
| Jean Callus | RATON | 1 | 0 | |

ADC

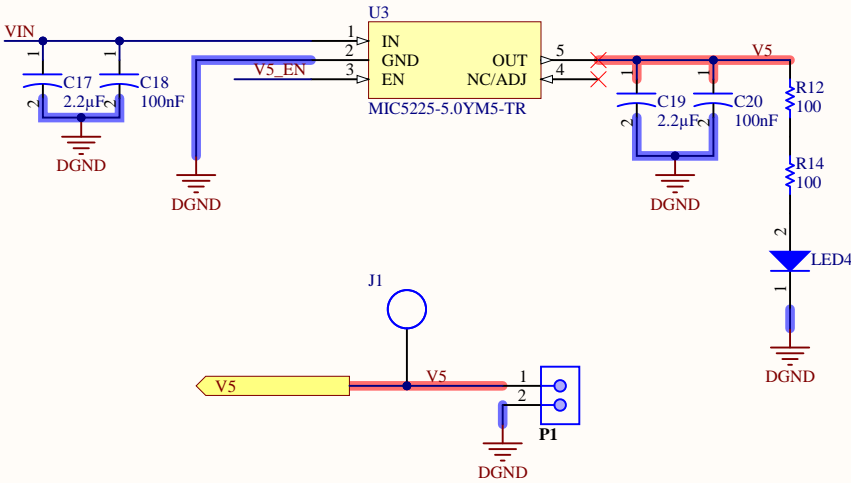


ADC Hardware Control

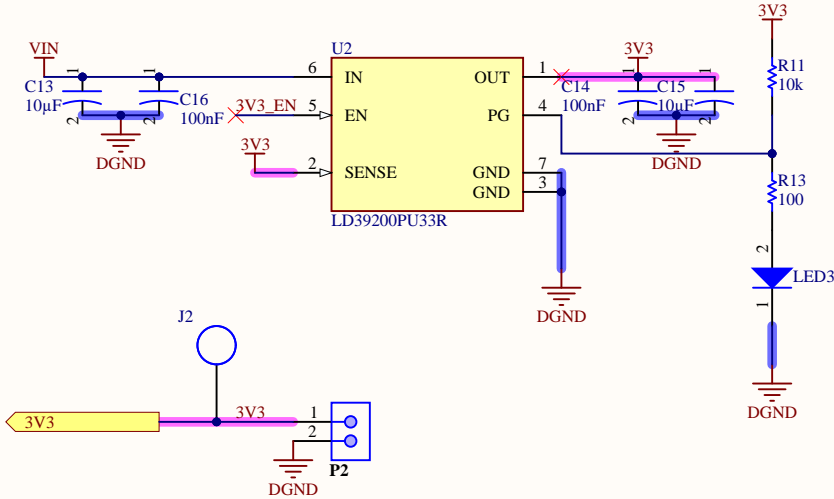


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| Project | | | 31/10/2020 | |
| Author | Organisation | Sheet | Revision | |
| Jean Callus | RATON | 2 | 0 | |

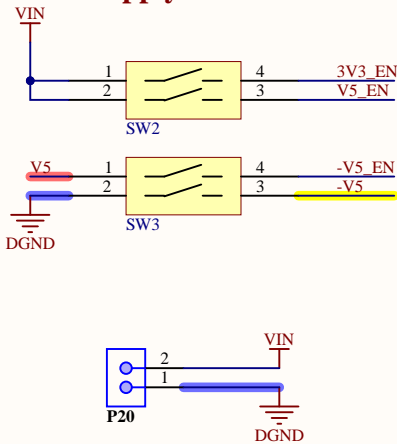
V5 Power Supply



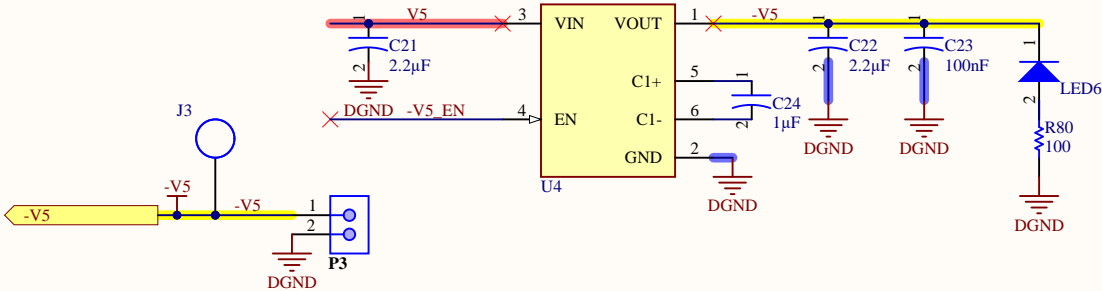
3V3 Power Supply



Supply Control



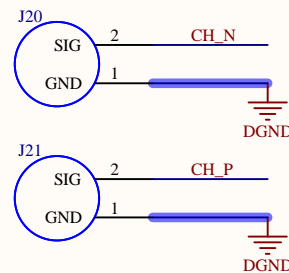
-V5 Power Supply



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| Project | | | 31/10/2020 | |
| Author | Organisation | Sheet | Revision | |
| Jean Callus | RATON | 3 | 0 | |

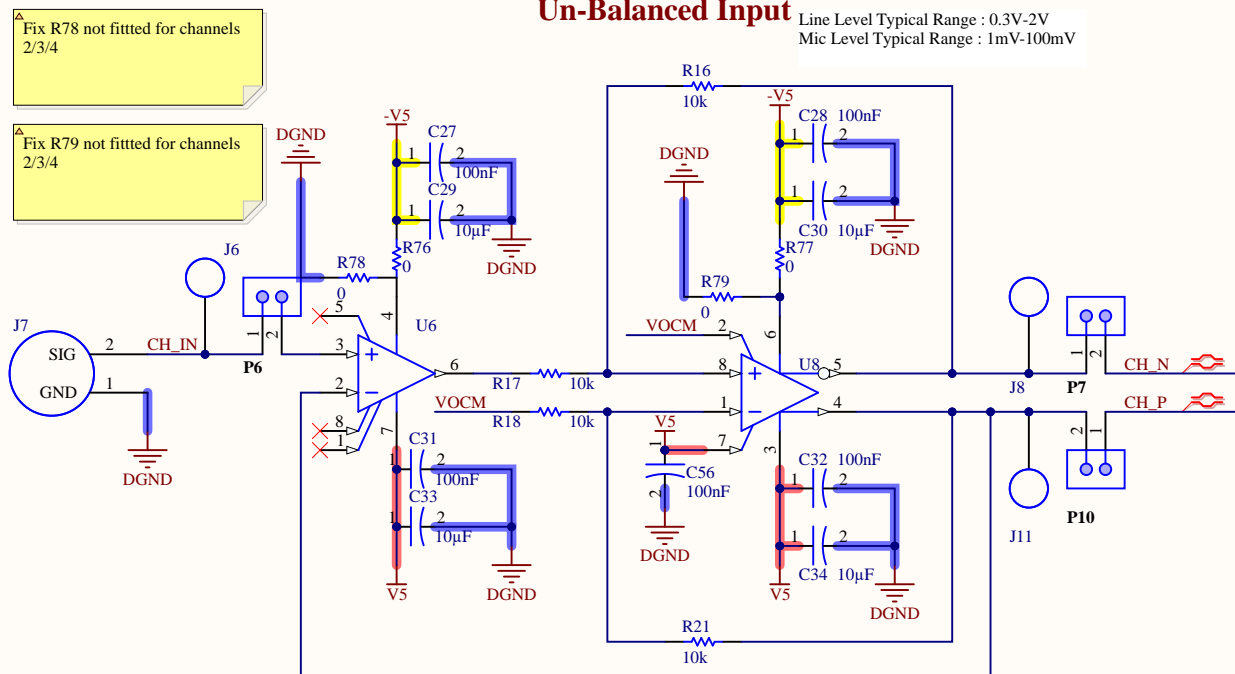
Analog Front End

Balanced Input

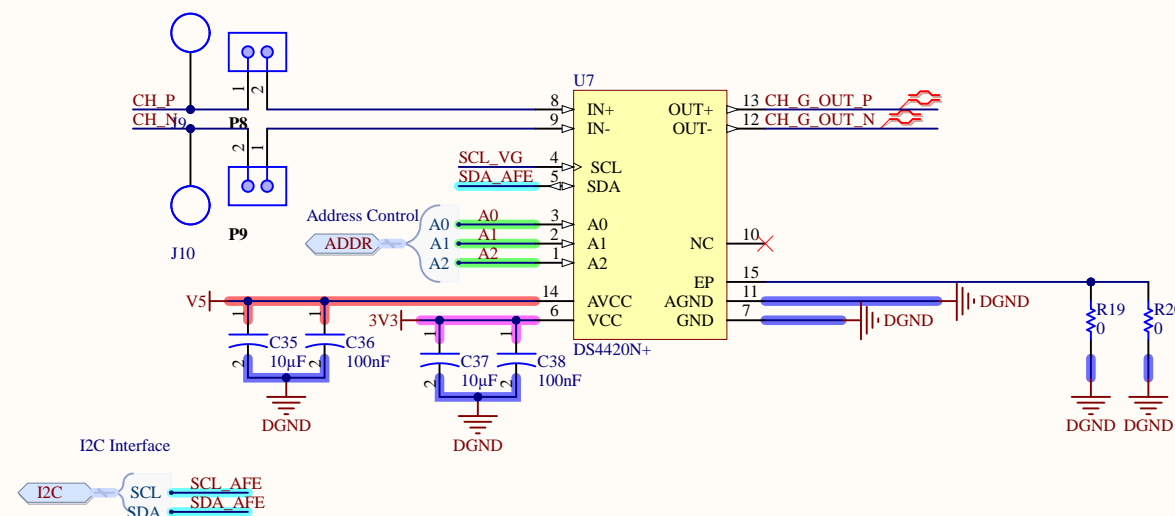


Un-Balanced Input

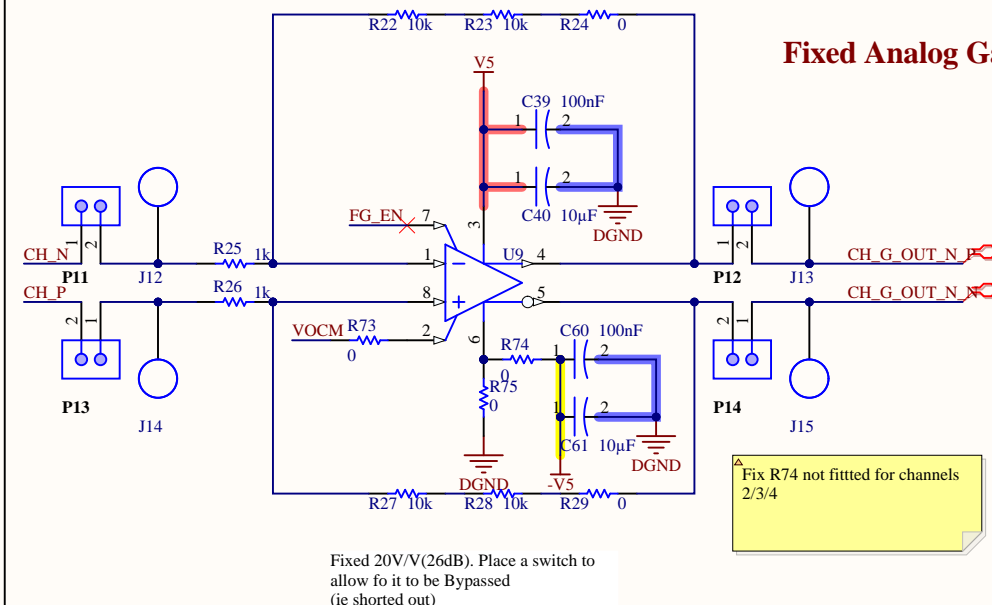
Line Level Typical Range : 0.3V-2V
Mic Level Typical Range : 1mV-100mV



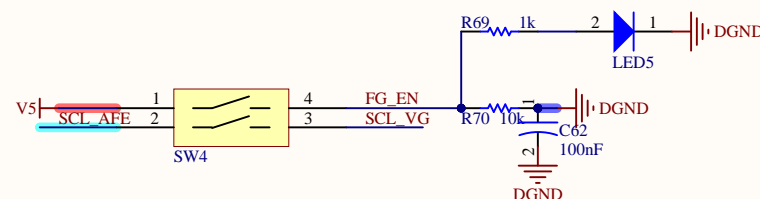
Programmable Variable Gain



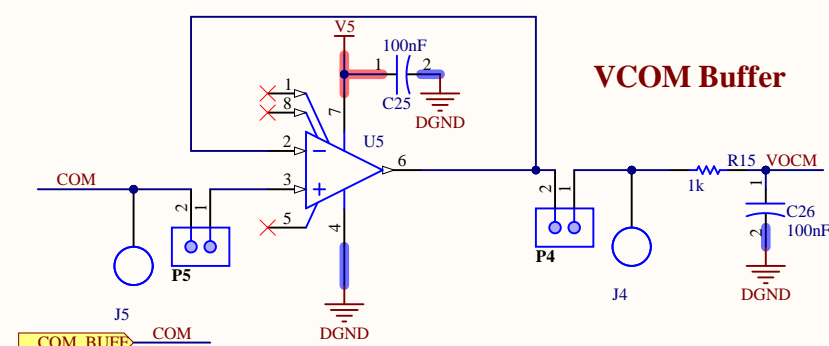
Fixed Analog Gain (Optional)



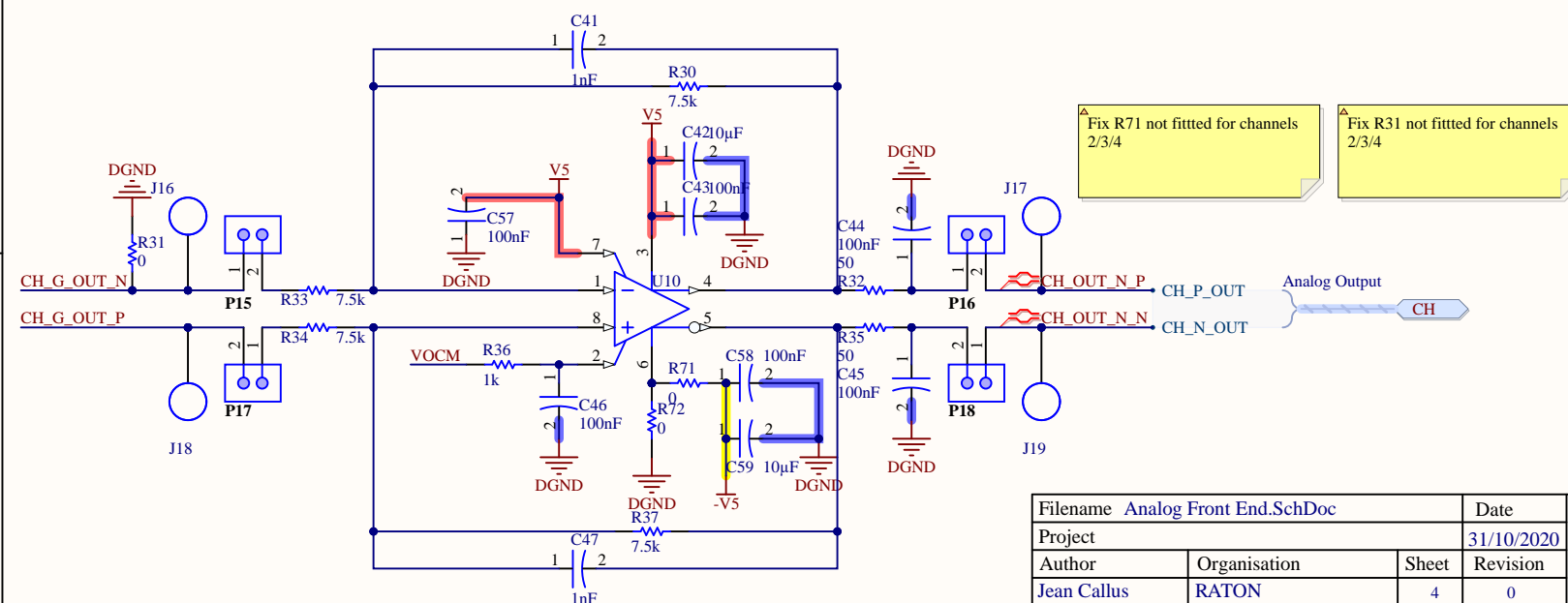
Gain Select



VCOM Buffer



ADC Channel Driver & Anti Aliasing Filter



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|--|--------------|-------|----------------------------|---|
| Filename Analog Front End.SchDoc | | | Date | Cannot open file C:\Users\mical\Downloads\download.png |
| Project | | | 31/10/2020 | |
| Author | Organisation | Sheet | Revision | |
| Jean Callus | RATON | 4 | 0 | |