

CS152A Schedule – Fall 2014

Week	Starting	Mon. Lab	Wed. Lab	Tue. Lab	Thur. Lab
0	9/29	N/A	N/A	N/A	Introduction
1	10/6	Introduction	Verilog Tutorial	Verilog Tutorial	FPGA Tutorial
2	10/13	FPGA Tutorial	Lab 1	Lab 1	Lab 1
3	10/20	Lab 1	Lab 1	Lab 1	Lab 2 introduction Lab 1 demo
4	10/27	Lab 2 introduction Lab 1 demo	Lab 2 Lab 1 report due	Lab 2	Lab 2 Lab 1 report due
5	11/3	Lab 2	Lab 2	Lab 2	Lab 3 introduction Lab 2 demo
6	11/10	Lab 3 introduction Lab 2 demo	Lab 3 Lab 2 report due	Vet. Day	Lab 3 Lab 2 report due
7	11/17	Lab 3	Lab 3	Lab 3	Lab 3
8	11/24	Final Lab introduction Lab 3 demo	Final Lab Lab 3 report due Final lab proposal due	Final Lab introduction Lab 3 demo	Thanksgiving Lab 3 report due Final lab proposal due
9	12/1	Final Lab	Final Lab	Final Lab	Final Lab
10	12/8	Final Lab Final lab demo	Final Lab Final lab report due	Final Lab Final lab demo	Final Lab Final lab report due