Computer Boot

x86

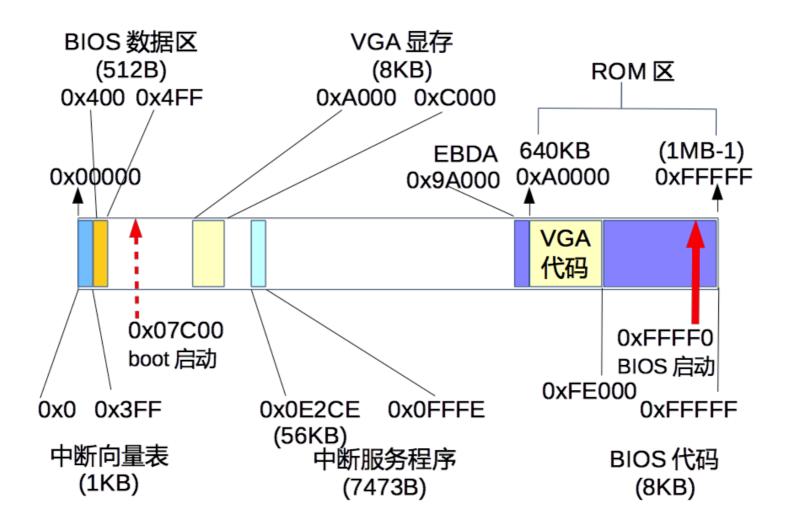
Environment

- Linux: ubuntu 18.04 LTS
- GCC
 - sudo apt-get -y install build-essential libelf-dev binutils-dev
- Bochs
 - sudo apt-get install bochs
 - sudo apt-get install bochs-x

Booting The Computer

- an IBM-compatible personal computer's x86 CPU executes
- Power On, real mode
- the instruction located at reset vector (the physical memory address FFFF0h on 16-bit x86 processors and FFFFFF0h on 32-bit and 64-bit x86 processors, i.e. BIOS entry point
- BIOS: POST, power-on self-test
- BIOS: goes through a pre-configured list of non-volatile storage devices ("boot device sequence") until it finds one that is bootable
- BIOS:load the bootstrap (i.e. MBR, Master Boot Record) from bootable storage device
- MBR:load the OS Kernel
- OS Kernel: OS services and shell

Memory Layout



bootstrap

0x000~0x002 <A jump instruction to 0xttt>
0x003~... Disk parameters(used by BIOS)
0xttt~0x1fd Bootstrap program
0x1ff~0x1fe 0xaa55



Bochs x86 emulator, http://bochs.sourceforge.net/





Plex86/Bochs UGABios (PCI) current-cvs 08 Jul 2014 This UGA/UBE Bios is released under the GNU LGPL

Please visit :

- . http://bochs.sourceforge.net
- . http://www.nongnu.org/vgabios

NO Bochs UBE Support available!

```
Bochs BIOS - build: 03/17/16
```

\$Revision: 12898 \$ \$Date: 2016-03-17 18:14:27 +0100 (Do, 17. M|ñr 2016) \$

Options: apmbios pcibios pnpbios eltorito rombios32

ataO master: Generic 1234 ATA-6 Hard-Disk (4 MBytes)

Press F12 for boot menu.

Booting from Hard Disk...

Loading...

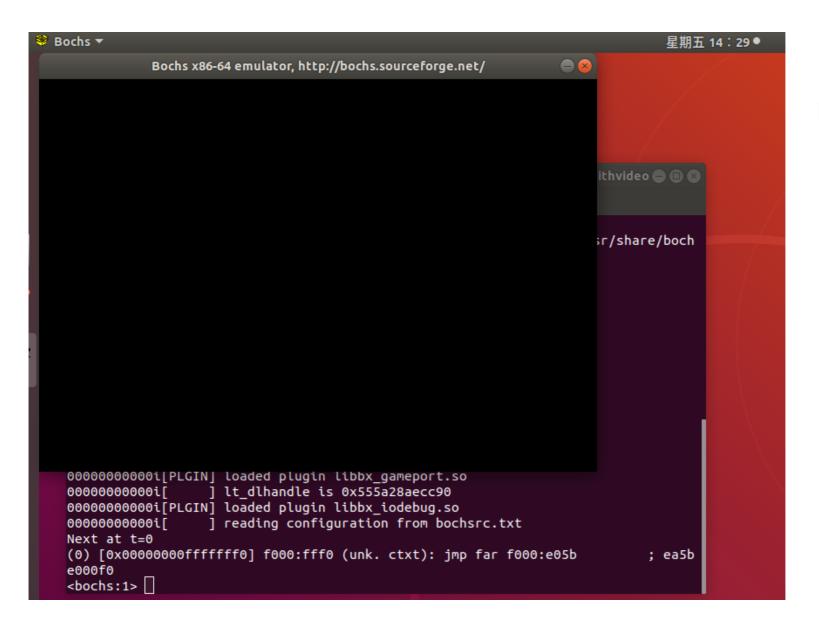
```
rw-r--r-- 1 albert albert 5120000 11月
                                       9 13:58 bochs.img
                              166 11月
rw-r--r-- 1 albert albert
                                       9 14:36 bochs.log
                            33269 9月 19 2017 bochsrc.txt
rwxrwx--- 1 albert albert
                              512 11月 9 13:58 boot
rwxr-xr-x 1 albert albert
                            1072 11月 9 13:58 boot.o
rw-r--r-- 1 albert albert
                             1016 11月 9 13:58 boot.out
rwxr-xr-x 1 albert albert
                              831 9月
                                     19 2017 boot.S
rwxrwx--- 1 albert albert
                              709 9月
rwxrwx--- 1 albert albert
                                      19
                                         2017 Makefile
                              202 9月
rwxrwx--- 1 albert albert
                                     29 22:11 Readme.txt
                            21682 9月
                                         2017 Screenshot2017-09-19-09-34-32.png
rwxrwx--- 1 albert albert
                                      19
rwxrwx--- 1 albert albert
                              399 9月
                                          2017 sign.pl
                                      19
                              533 9月
-rwxrwx--- 1 albert albert
                                          2017 snapshot.txt
```

```
🔚 Makefile🔣
    CC
             := qcc -pipe
  2 AS
             := as
  3 AR
             := ar
  4 LD
        := ld
  5 OBJCOPY := objcopy
  6 OBJDUMP := objdump
  7 NM
             := nm
  8 LDFLAGS := -m elf i386
  9
 10 all: image
 11
    boot objs := boot.o
 12 boot.o: boot.S
 13
         $(CC) -nostdinc -m32 -Os -c -o $@ $<
 14 boot: $(boot objs)
 15
        $(LD) $(LDFLAGS) -N -e start -Ttext 0x7C00 -o $@.out $^
 16
        $(OBJCOPY) -S -O binary $0.out $0
 17
        perl sign.pl $0
 18 bochs.img: boot
 19
        dd if=/dev/zero of=./.bochs.img~ count=10000 2>/dev/null
 20
        dd if=./boot of=./.bochs.img~ conv=notrunc 2>/dev/null
 21
        mv ./.bochs.img~ ./bochs.img
 22 image: bochs.img
 23 bochs: image
 2.4
        bochs -f bochsrc.txt
 25 run: bochs
 26 # For deleting the build
 2.7
   clean:
 28
        rm *.o *.out *.asm boot *.log -fr
 29
        rm bochs.img -fr
 30 .PHONY: clean
 31
```

```
📙 boot. S🔀
     #the first prog:boot
      #directly write to video memory 0xb8000~0xbffff
                                                                  boot.S
  4
  5
      .globl start
  6
      start:
       .code16
                                    # Assemble for 16-bit mode
  8
       cli
                                   # Disable interrupts
  9
       cld
                                    # String operations increment
 10
 11
              %ax,%ax
                                   # Segment number zero
       xorw
 12
              %ax,%ds
                                   # initiate Data Segment
       movw
 13
       movw %ax, %es
                                   # Extra Segment
 14
                                   # Stack Segment
       movw %ax,%ss
 15
 16
               $0xb800,%ax
                                 #text display address
       movw
 17
              %ax,%es
       movw
 18
             $msq1,%si
                              #source addr:msq1
       movw
 19
       movw $0xb88,%di
                            #dest addr:0xb8000+0xb88
 20
       movw
              $0x14,%cx
                                #count of msq1
 21
        rep movsb
                                 #print msq1
 22
 23
      spin: #loop forever
 2.4
        jmp spin
 25
 26
     msq1:
                          #Loading...
 27
        .byte 'L', 0xc, 'o', 0x9, 'a', 0xc, 'd', 0x9, 'i', 0xc, 'n', 0x9, 'q', 0xc
 28
        .byte '.', 0x9, '.', 0xc, '.', 0x9
 29
 30
        .org 510
 31
        .word 0xAA55
```

```
🔚 boot. S🔀 🛭
  4
     #
  5
     .qlobal start
                                                                 boot.S
  6
     start:
       .code16
                                   # Assemble for 16-bit mode
  8
       cli
                                   # Disable interrupts
  9
       movw %cs, %ax
 10
       movw %ax,%ds
                                   # initiate Data Segment
       movw %ax,%es
 11
                                  # Extra Segment
      movw %ax,%ss
 12
                                  # Stack Segment
 13
 14
     #qet current cursor pos
 15
       movb
               $0x03, %ah
                                       # read cursor pos
 16
       xor
           %bh, %bh
 17
       int
               $0x10
 18
 19
     #pring msg1
 2.0
                $19, %cx
       movw
 21
                $0x000c, %bx
                                       # page 0, attribute 0x0c (red)
       movw
 22
                         \#0x07 mormal, 0x0c red
 23
            $msq1, %bp
       movw
 2.4
                $0x1301, %ax
       movw
                                        # write string, move cursor
 25
       int
                $0x10
 26
 27
     spin: #loop forever
 28
       jmp spin
 29
 30
     msq1:
                        #Loading System...
 31
       .ascii "I am booting system..."
 32
       .byte 13,10
 33
 34
       .org 510
 35
       .word 0xAA55
```

make run



Press "c"

Bochs x86-64 emulator, http://bochs.sourceforge.net/







```
lex86/Bochs UGABios (PCI) current-cvs 08 Apr 2016
This UGA/UBE Bios is released under the GNU LGPL
Please visit :
. http://bochs.sourceforge.net
. http://www.nongnu.org/vgabios
NO Bochs UBE Support available!
Bochs BIOS - build: 09/02/12
$Revision: 11318 $ $Date: 2012-08-06 19:59:54 +0200 (Mo, 06. Aug 2012) $
Options: apmbios pcibios pnpbios eltorito rombios32
ata0 master: Generic 1234 ATA-6 Hard-Disk (   4 MBytes)
Press F12 for boot menu.
Booting from Hard Disk...
Loading System...
```

End