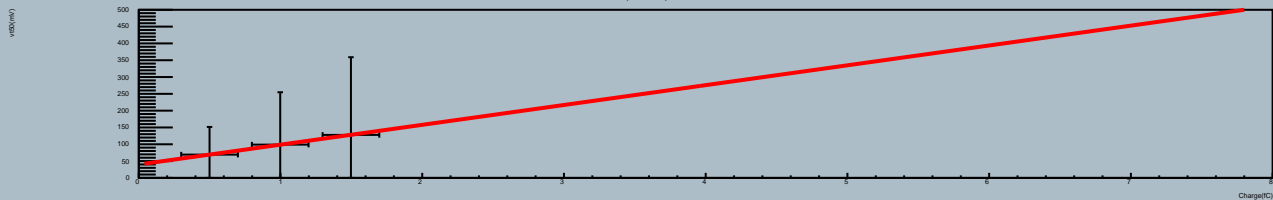
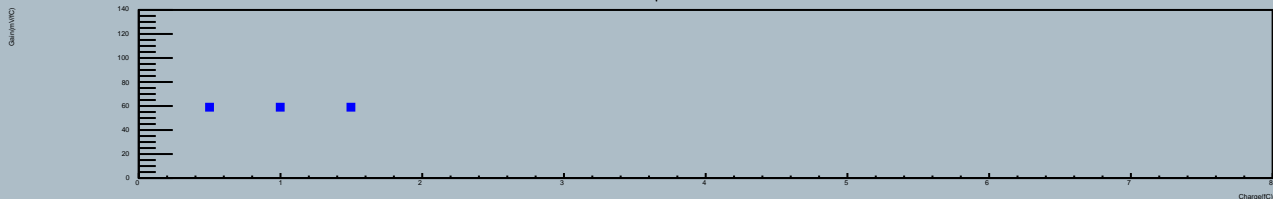


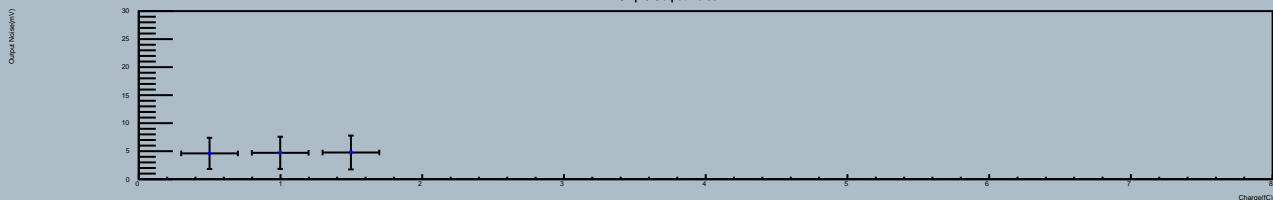
Chip 0 Response Curve



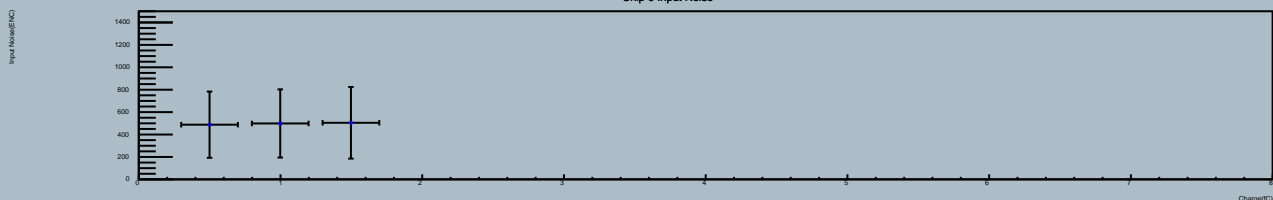
Chip 0 Gain

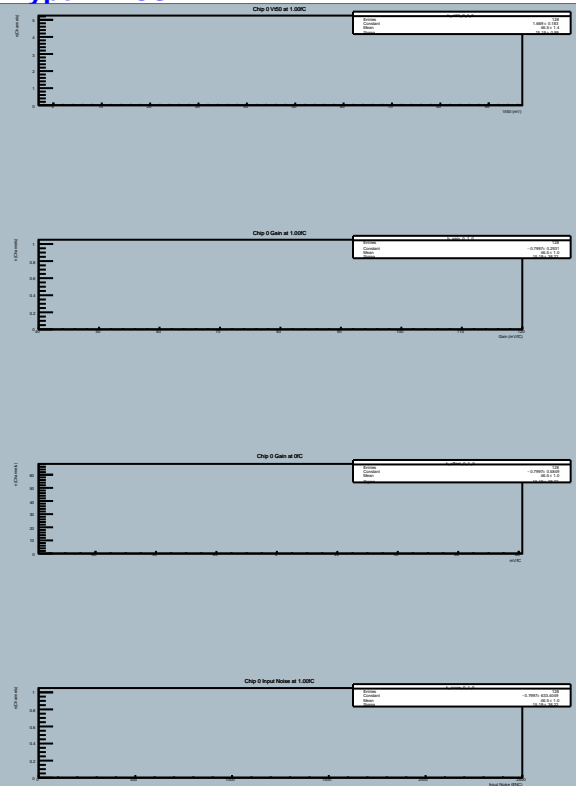
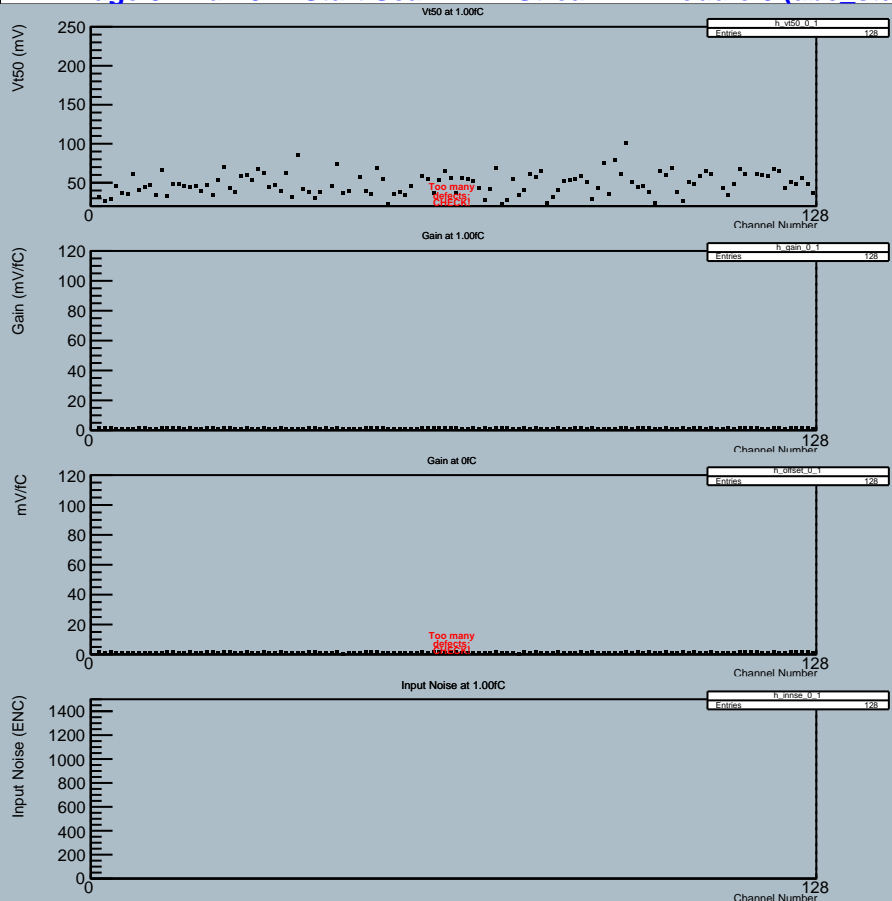


Chip 0 Output Noise

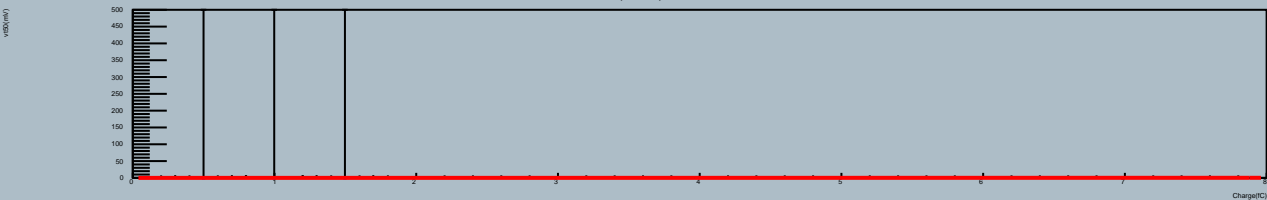


Chip 0 Input Noise

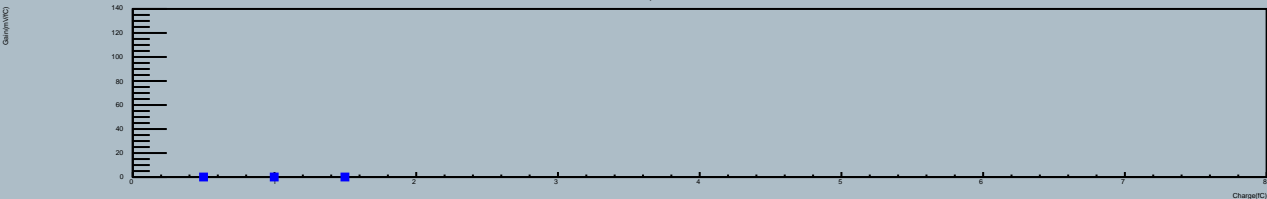




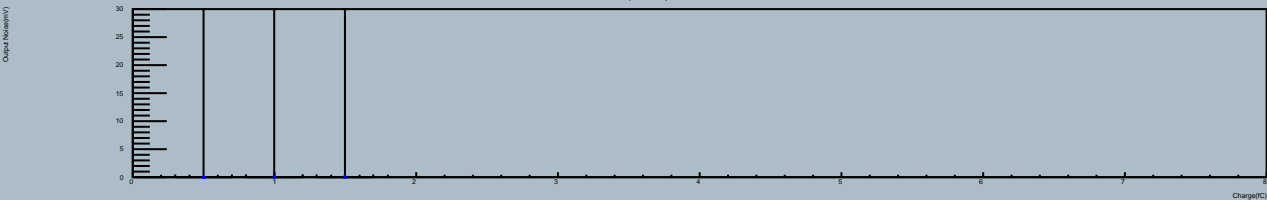
Chip 0 Response Curve



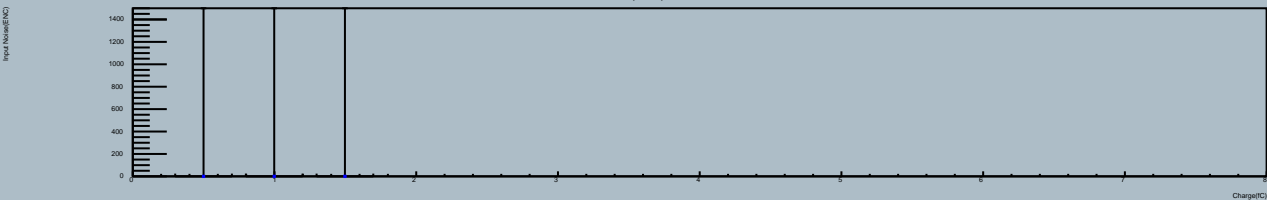
Chip 0 Gain



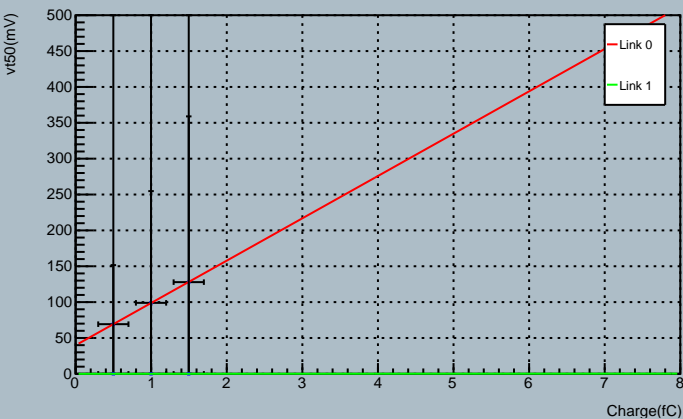
Chip 0 Output Noise



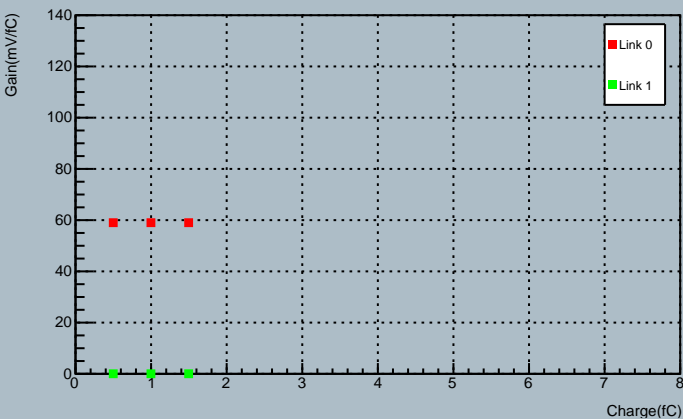
Chip 0 Input Noise



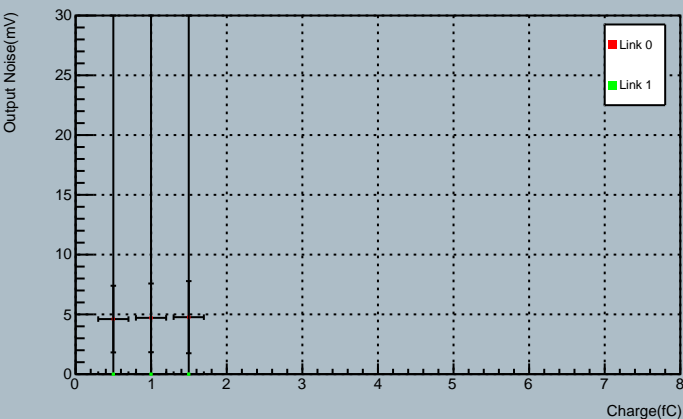
Chip 0 Response Curve



Chip 0 Gain



Chip 0 Output Noise



Chip 0 Input Noise

