Figure 1: EXT timestamp vs TID
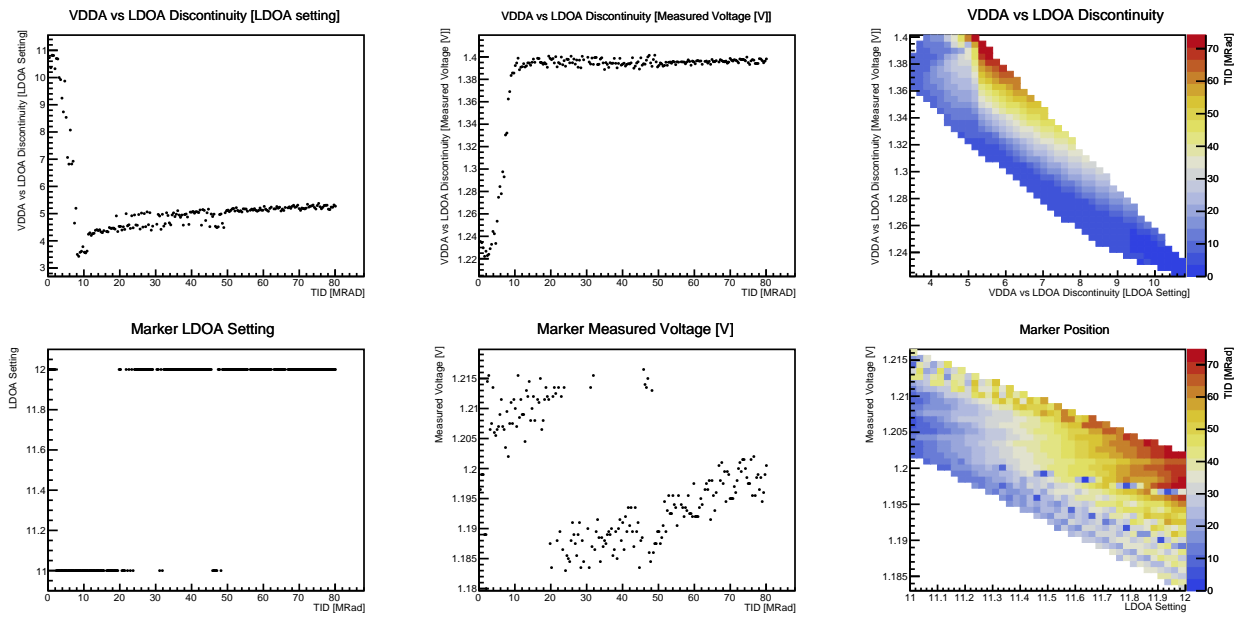
Figure 2: EXT subpad 1 chip 8
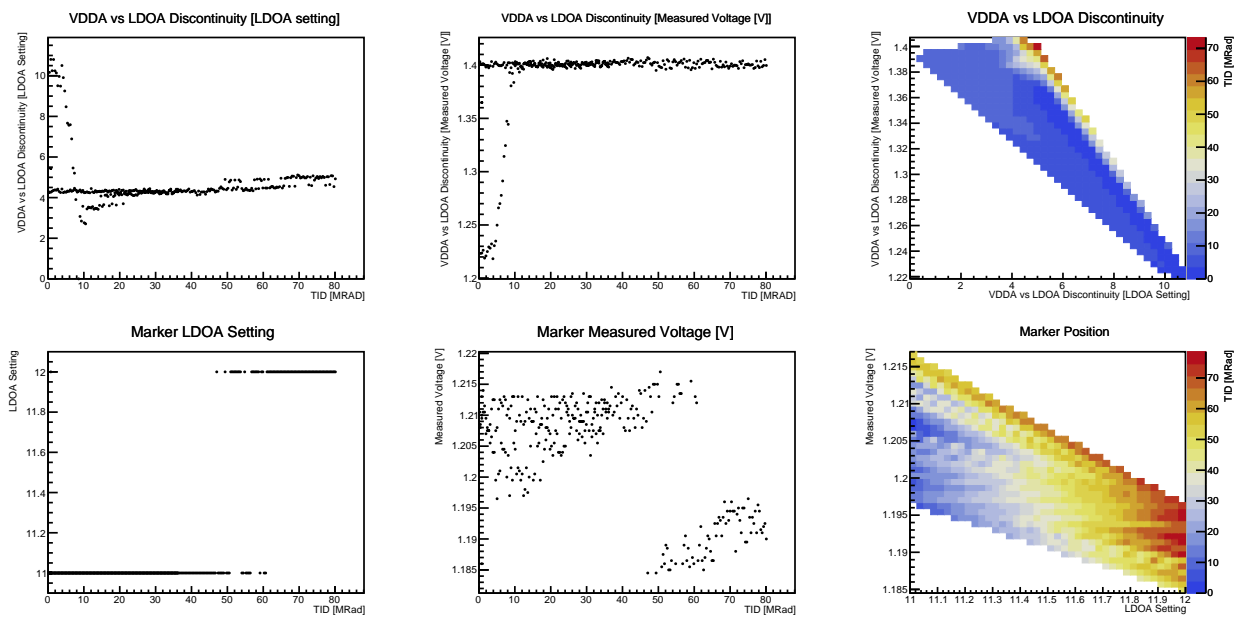

Figure 3: EXT subpad 1 chip 9
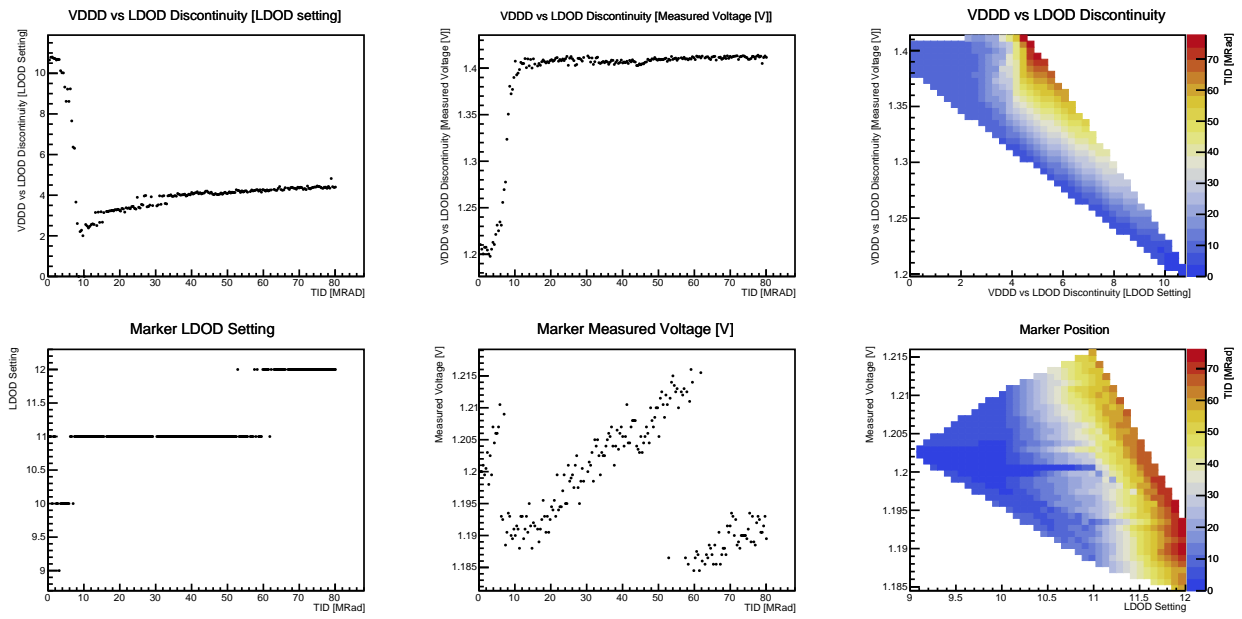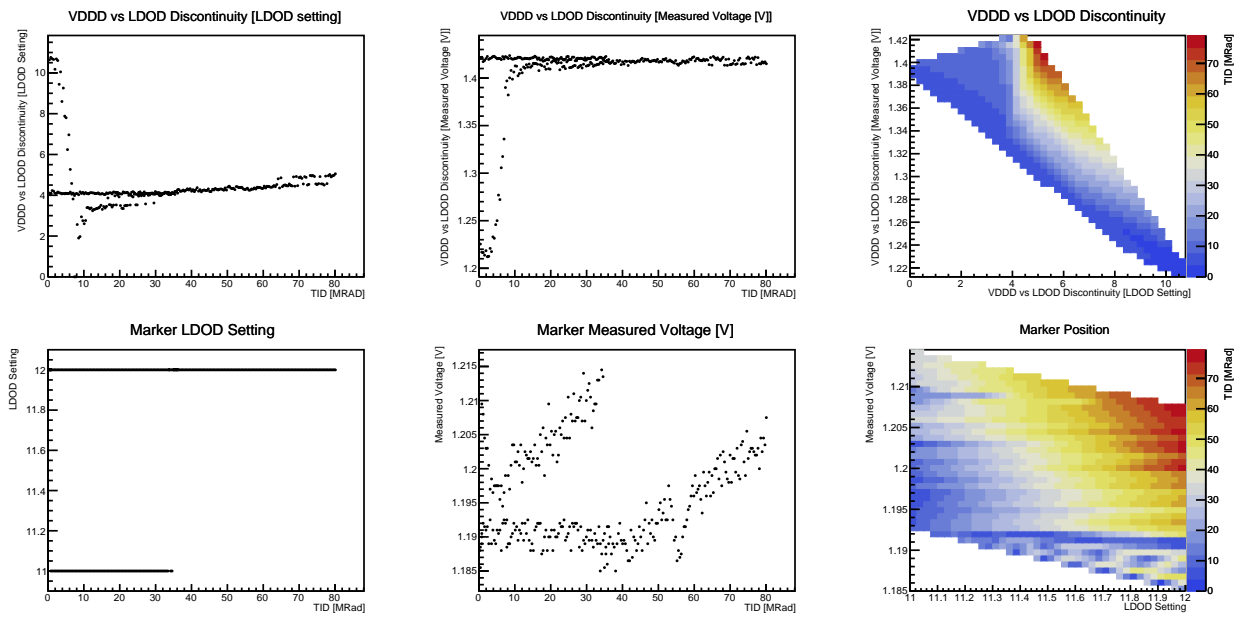
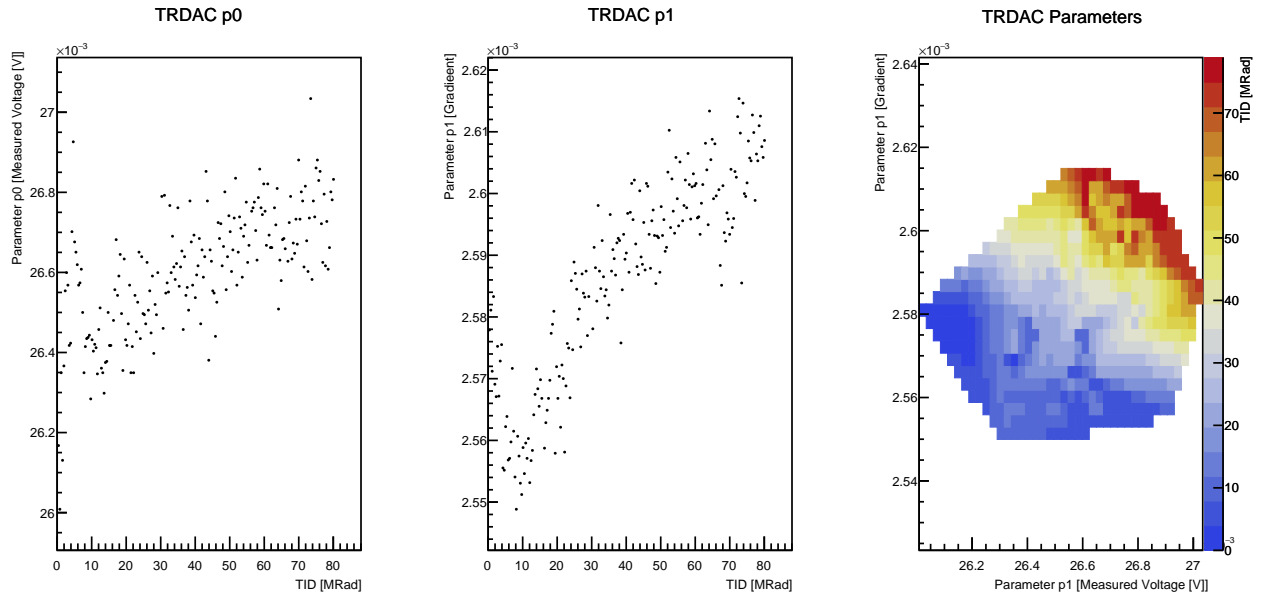Figure 4: EXT subpad 2 chip 8

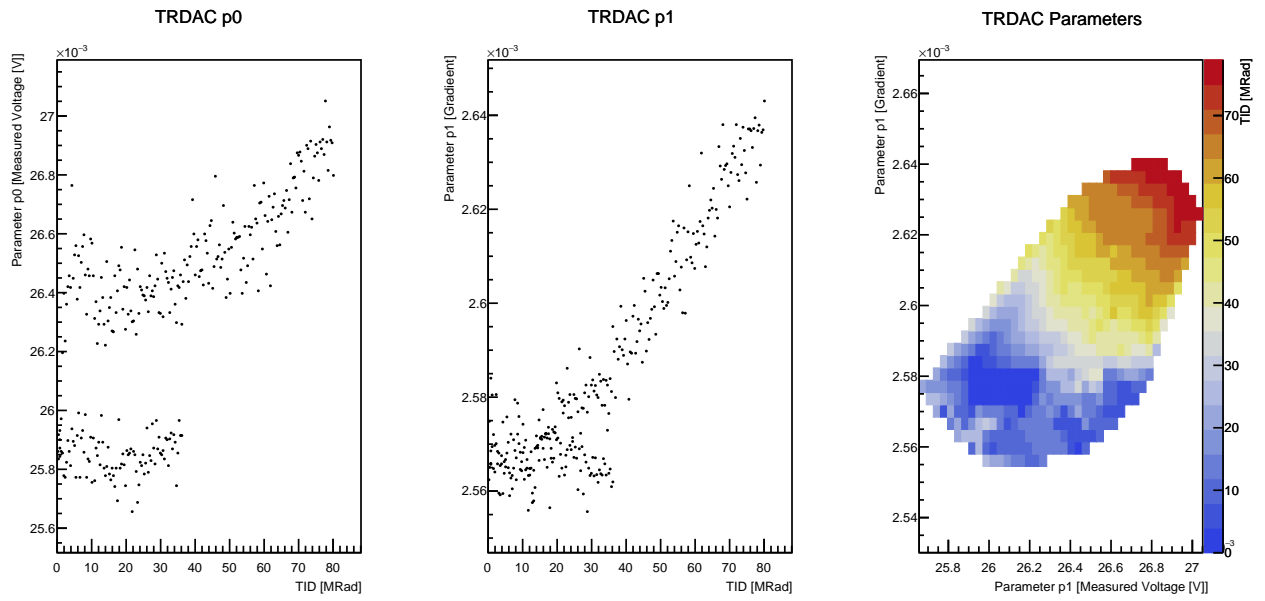Figure 5: EXT subpad 2 chip 9

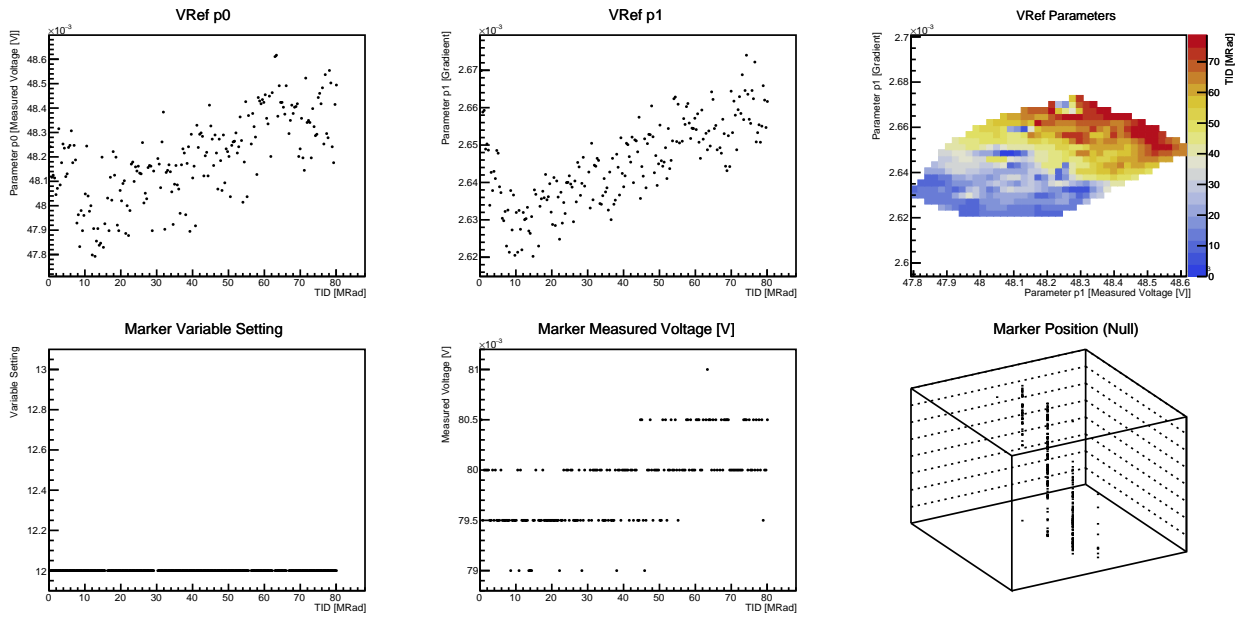Figure 6: EXT subpad 3 chip 8

Figure 7: EXT subpad 3 chip 9
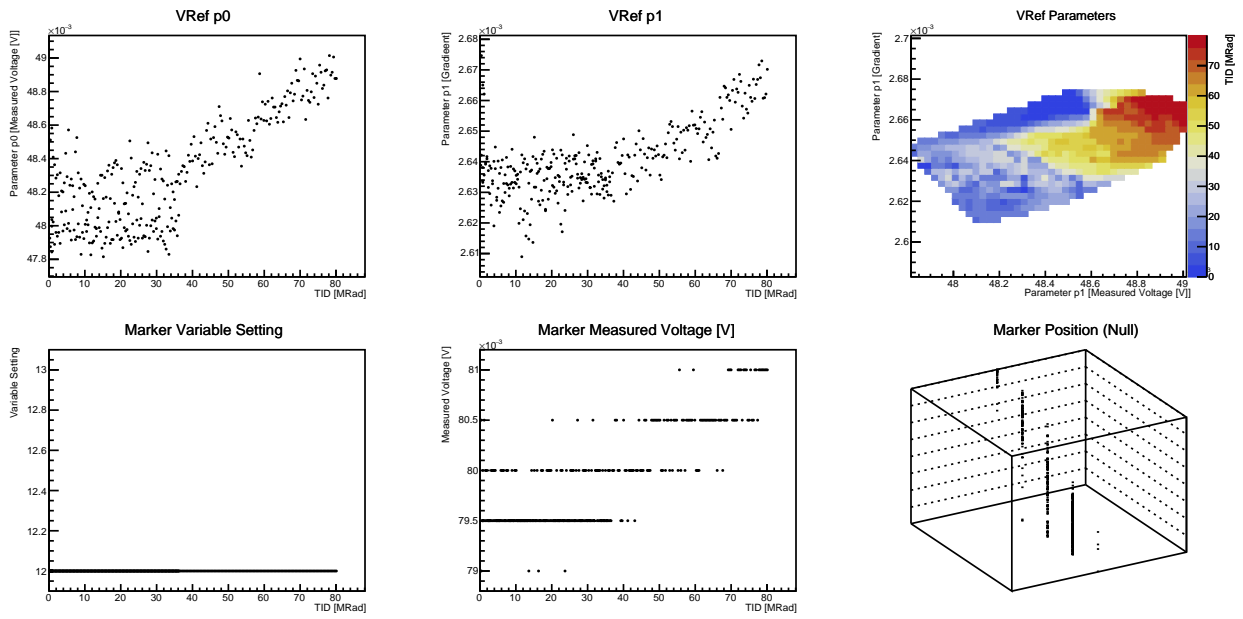
Figure 8: EXT subpad 4 chip 8
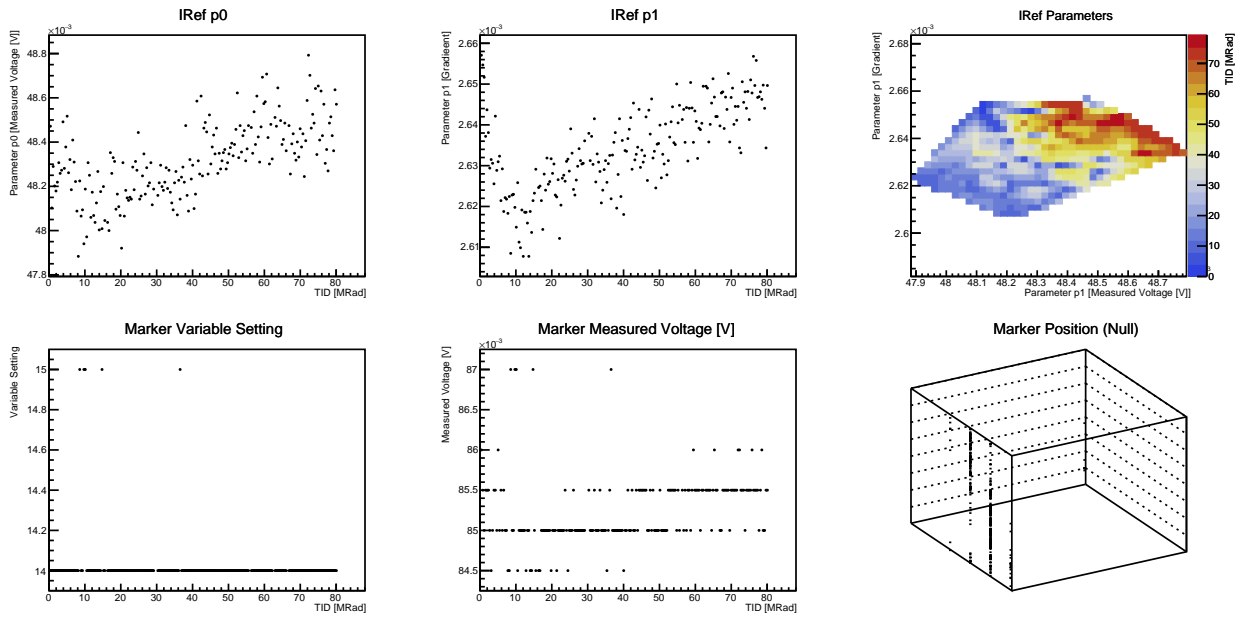
Figure 9: EXT subpad 4 chip 9
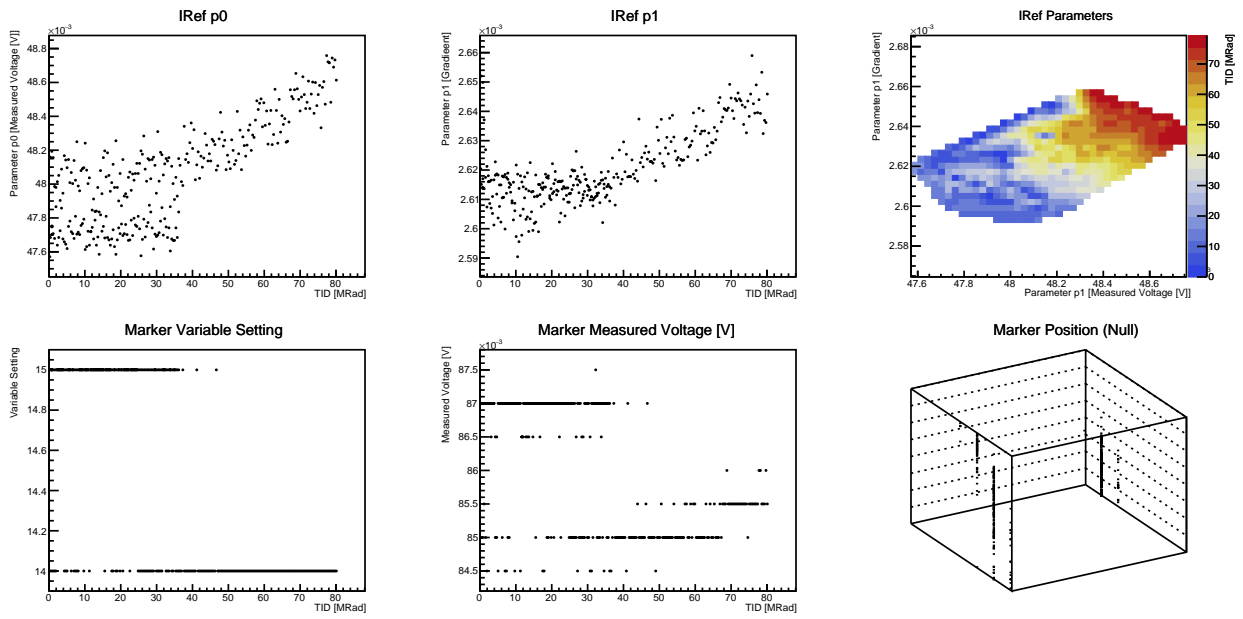
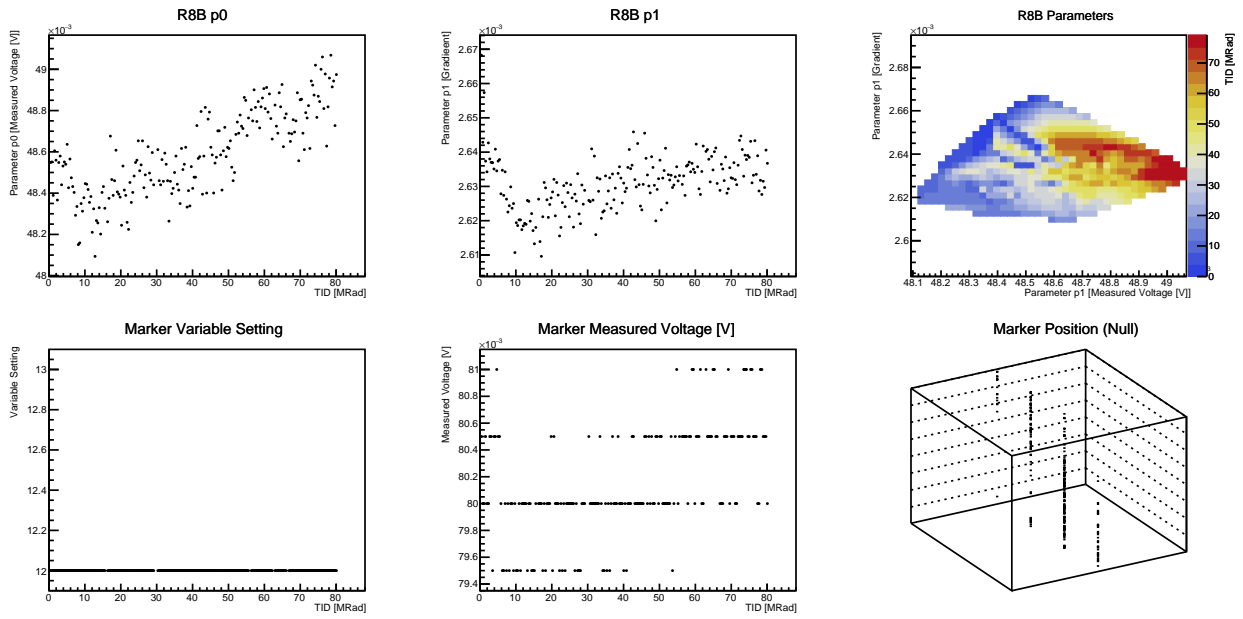Figure 10: EXT subpad 5 chip 8

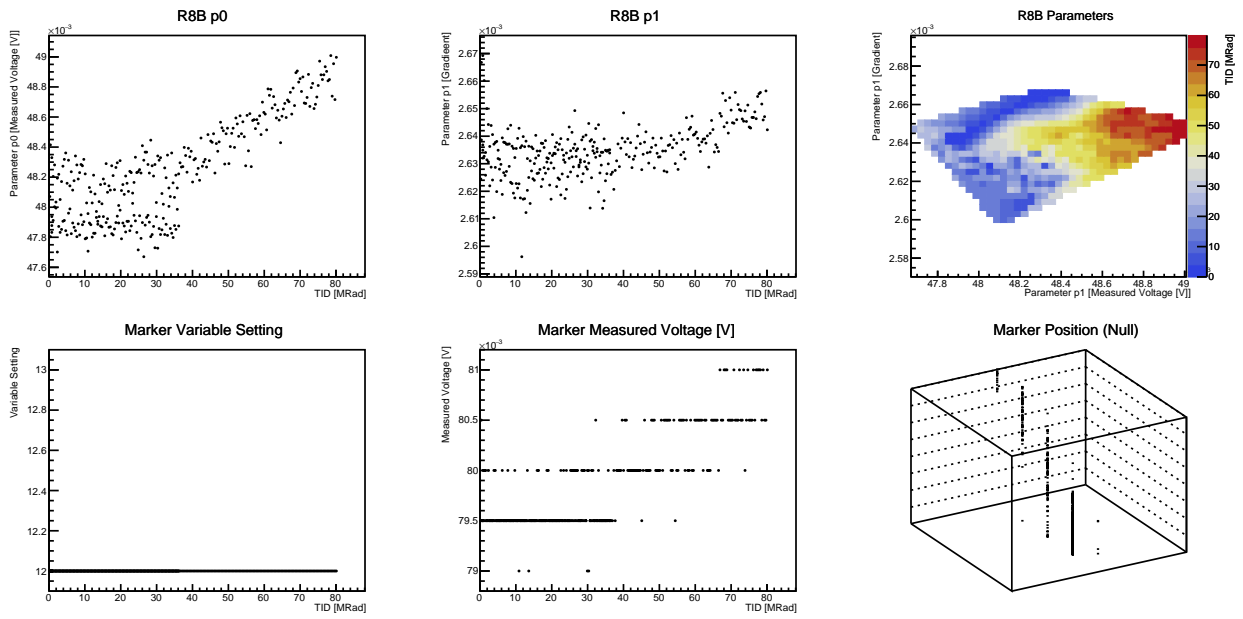Figure 11: EXT subpad 5 chip 9

Figure 12: EXT subpad 6 chip 8

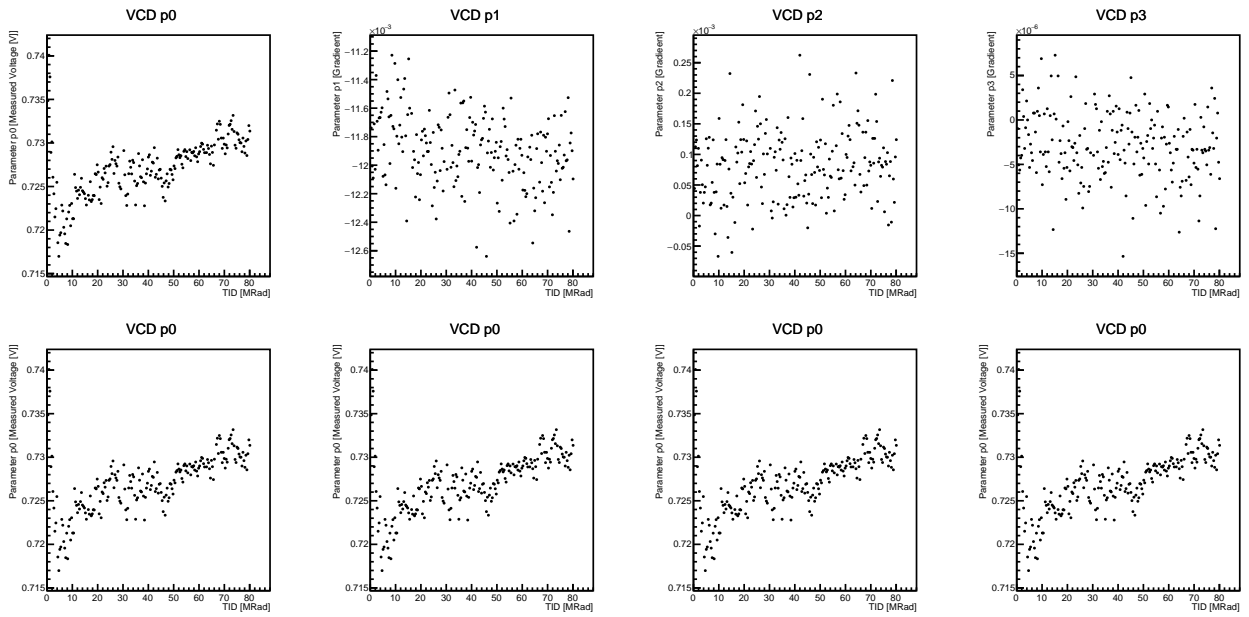Figure 13: EXT subpad 6 chip 9
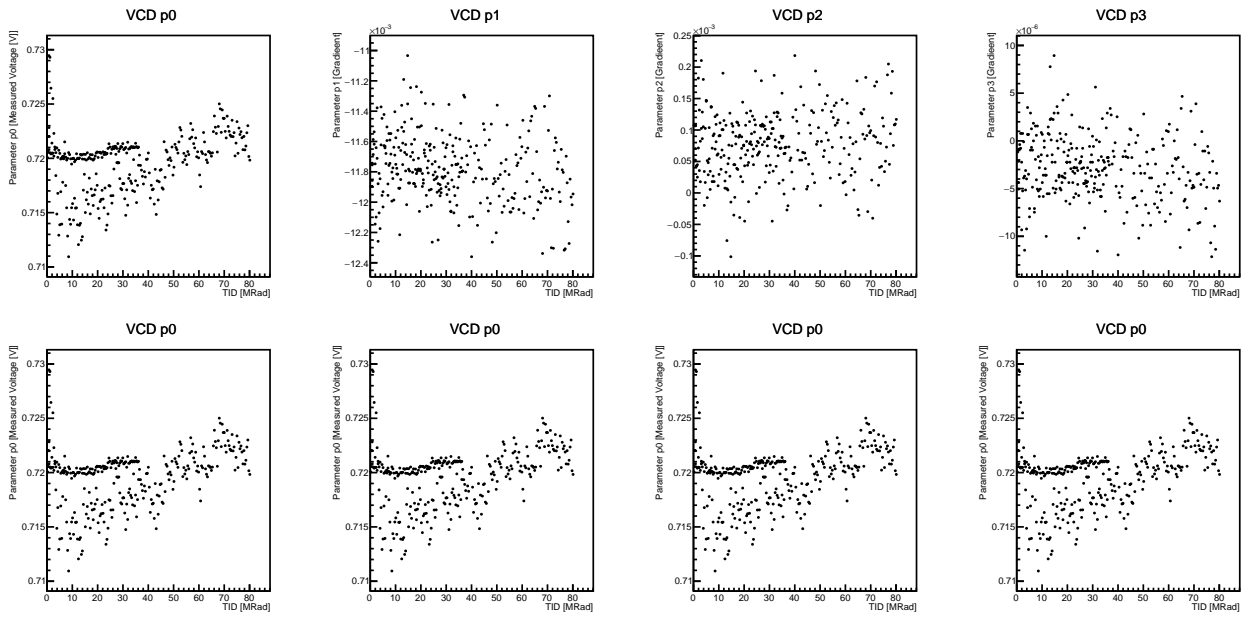
Figure 14: EXT subpad 7 chip 8
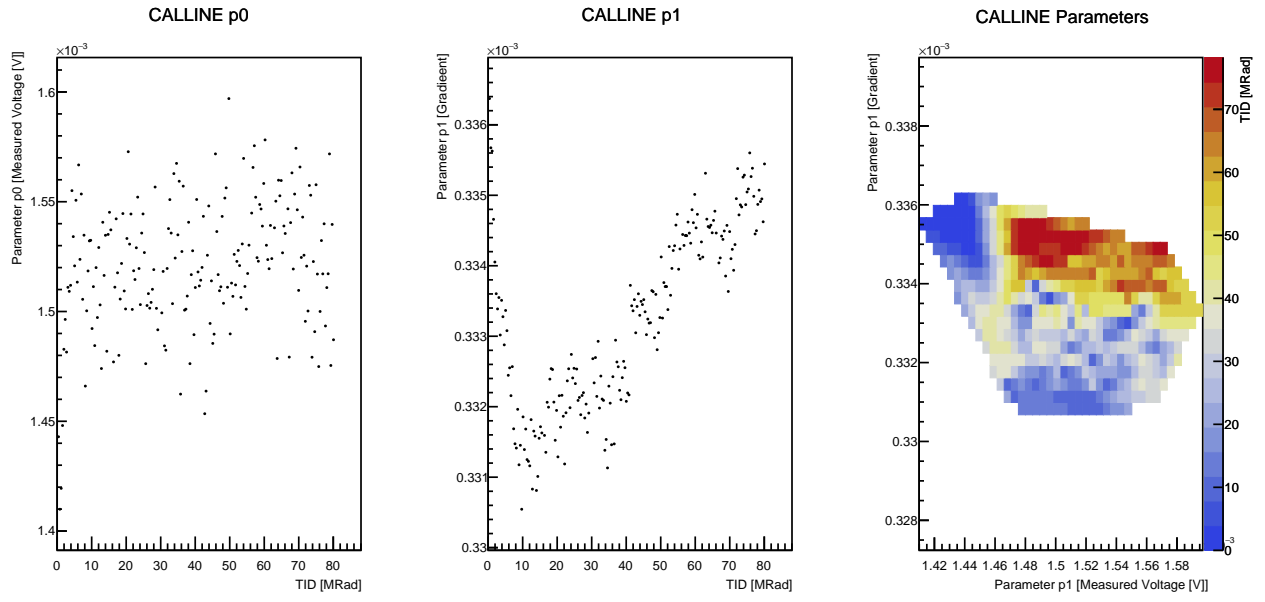

Figure 15: EXT subpad 7 chip 9
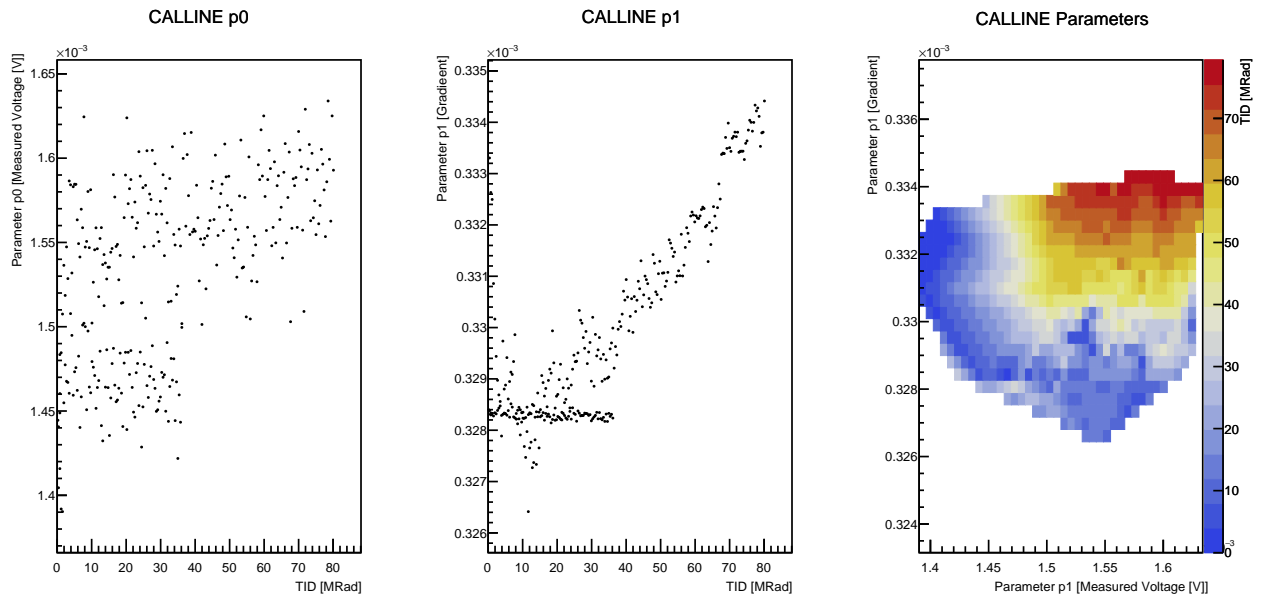
Figure 16: EXT subpad 8 chip 8
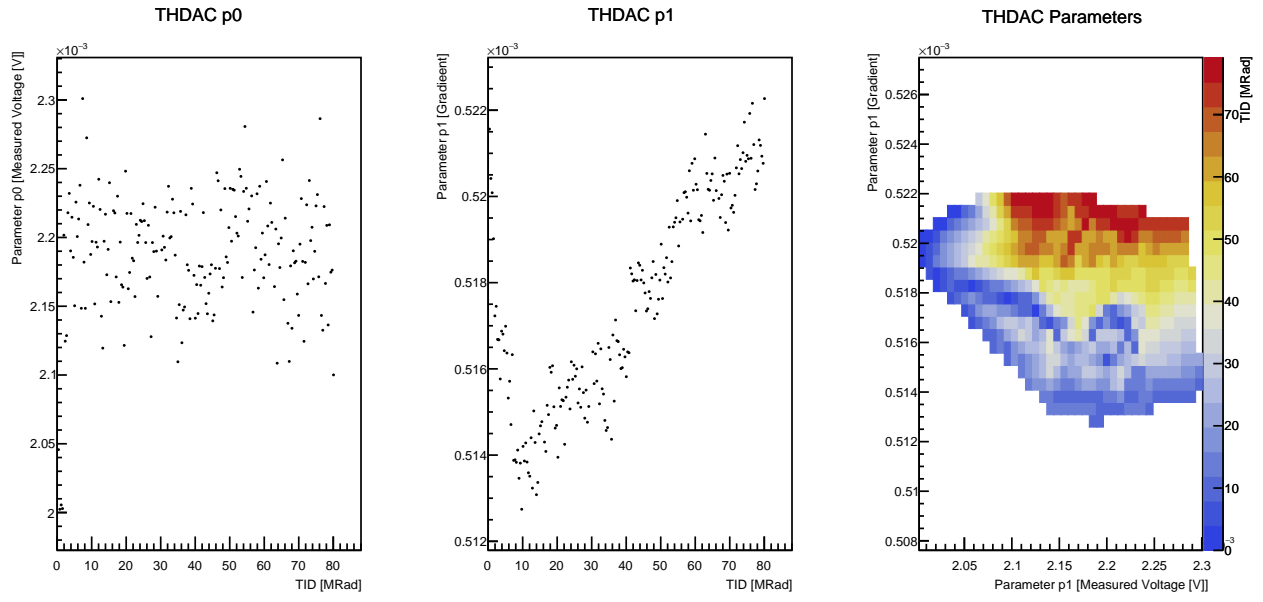

Figure 17: EXT subpad 8 chip 9
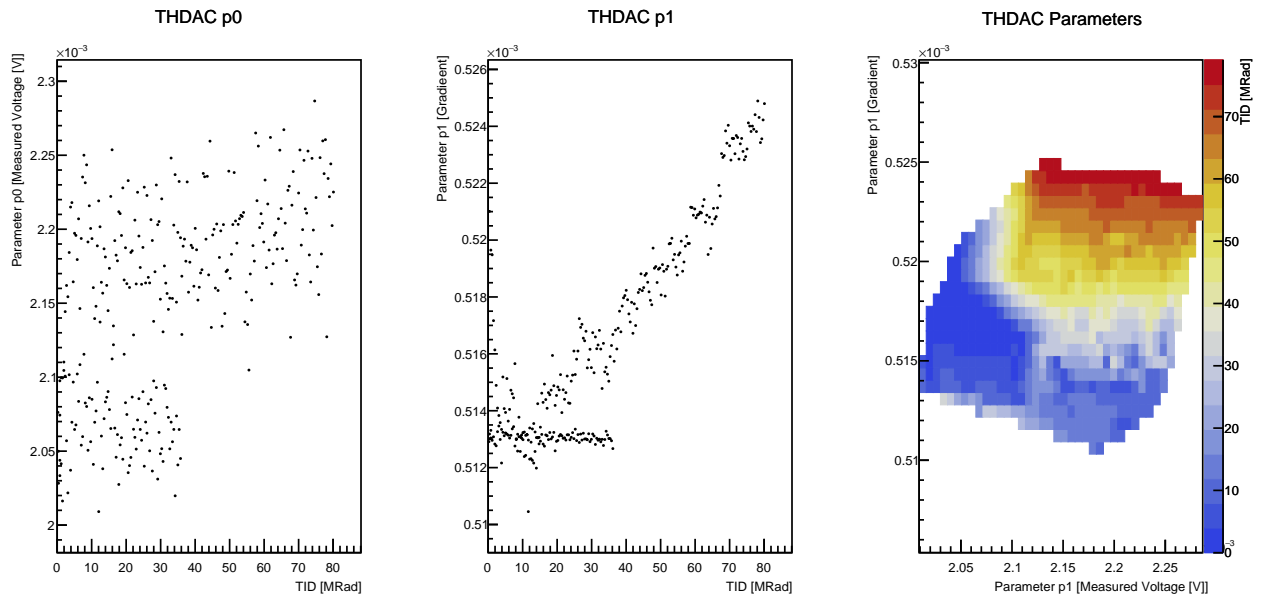
Figure 18: EXT subpad 9 chip 8
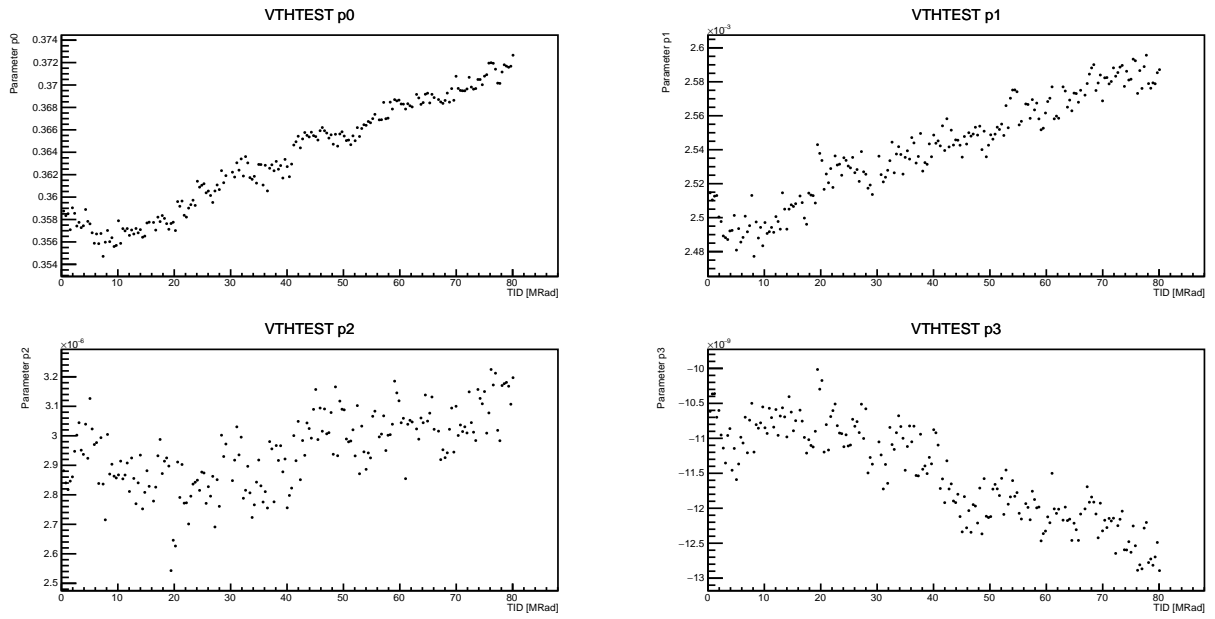

Figure 19: EXT subpad 9 chip 9
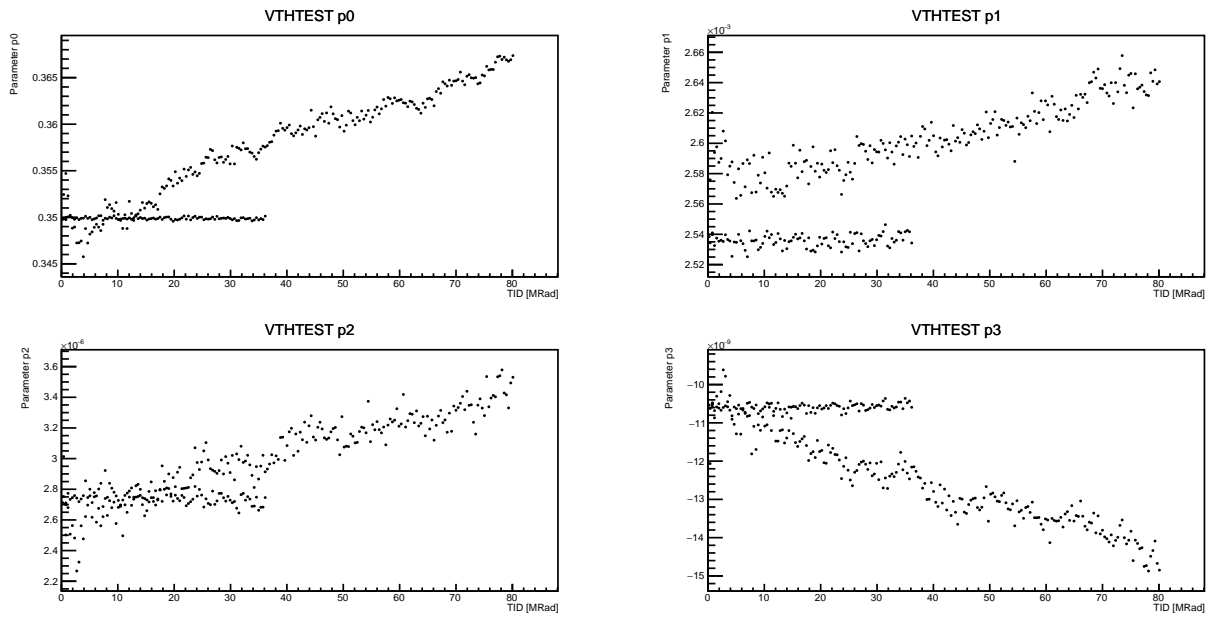
Figure 20: EXT subpad 10 chip 8
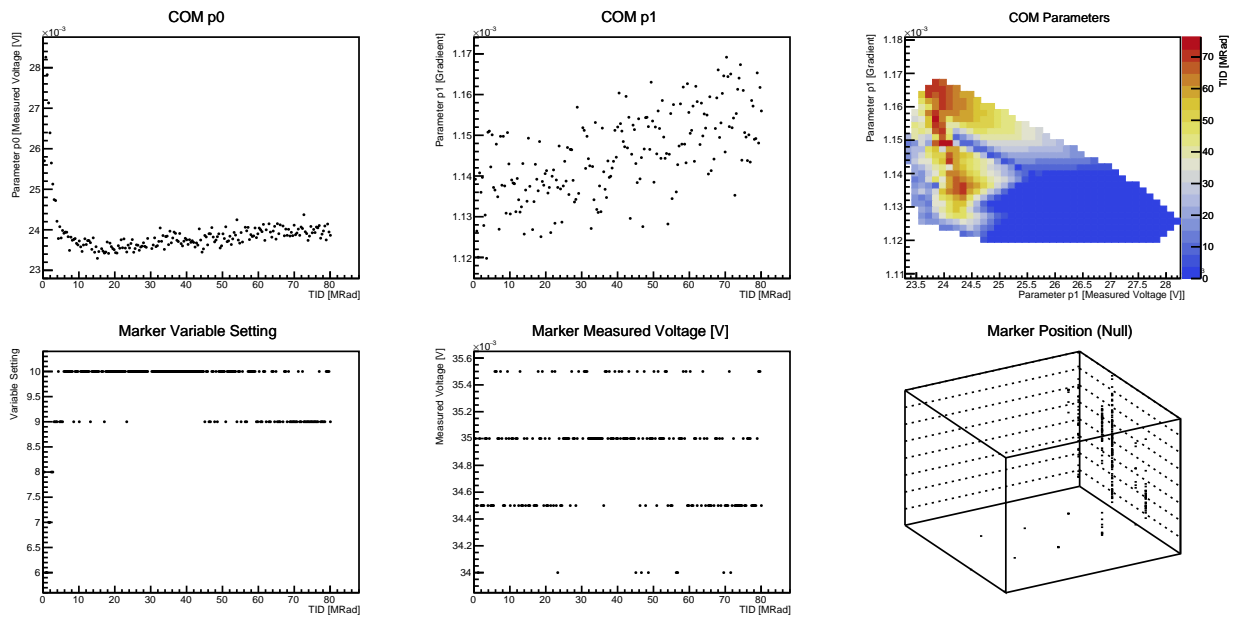

Figure 21: EXT subpad 10 chip 9
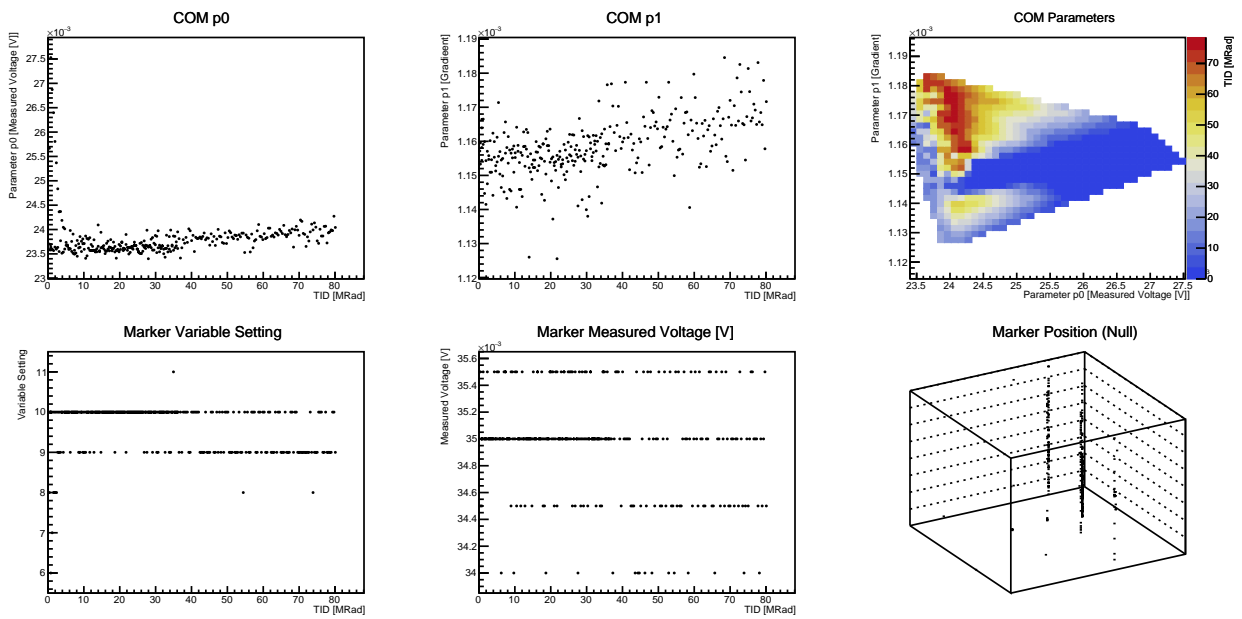
11

Figure 22: EXT subpad 11 chip 8
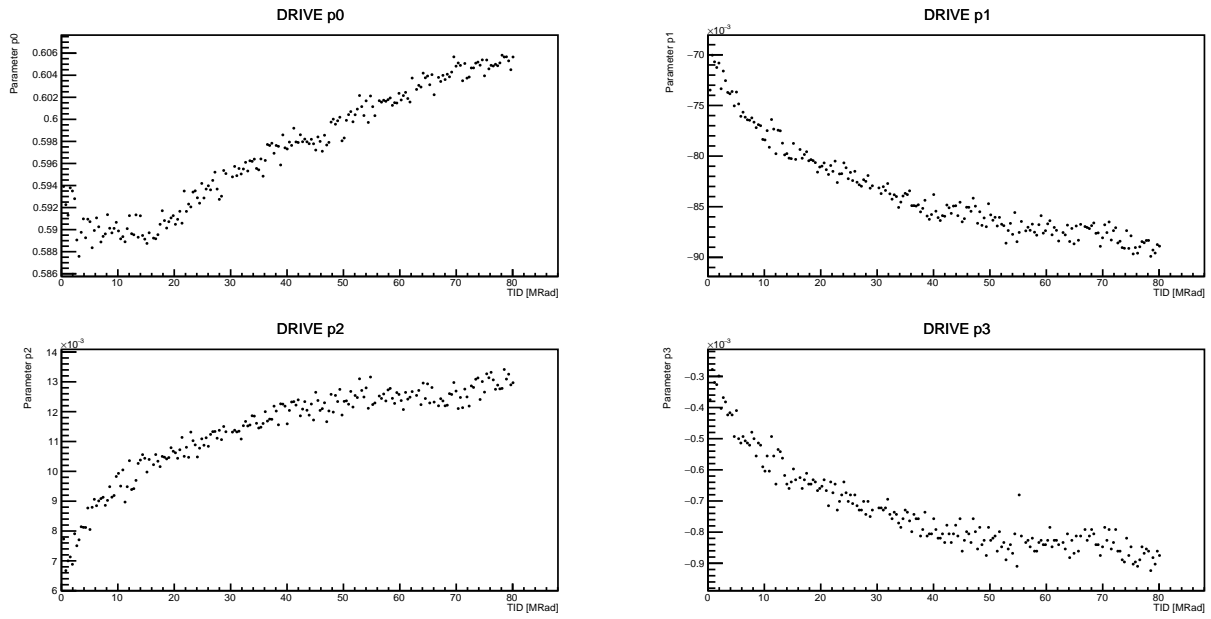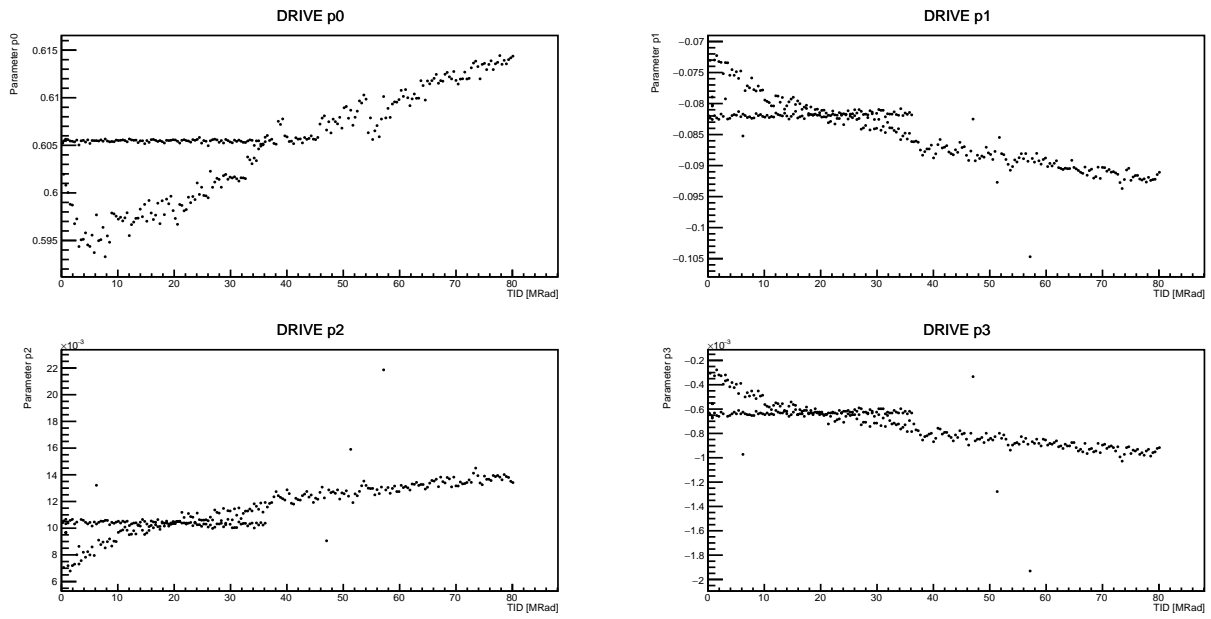

Figure 23: EXT subpad 11 chip 9
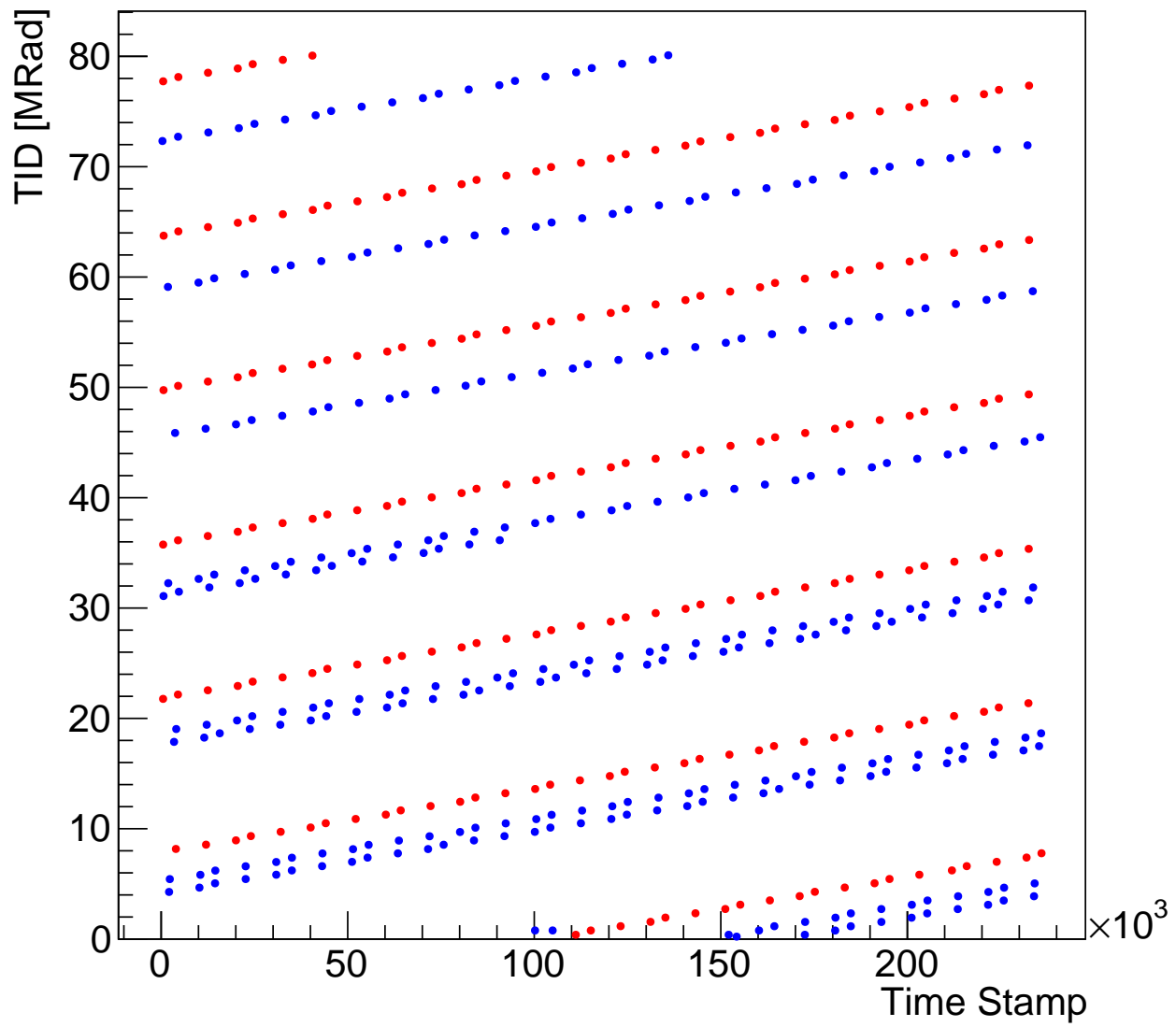
Figure 24: EXT subpad 12 chip 8

Figure 25: EXT subpad 12 chip 9

13

Figure 26: INT timestamp vs TID