

# intcon\_wrapper\_bd\_0

	o_ifu_arready
clk_i_wrapper	o_ifu_nid[2:0]
rst_ni_wrapper	o_ifu_rdata[63:0]
i_ifu_arid[2:0]	o_ifu_resp[1:0]
i_ifu_araddr[31:0]	o_ifu_rlast
i_ifu_arlen[7:0]	o_ifu_rvalid
i_ifu_arsize[2:0]	o_lsu_arready
i_ifu_arburst[1:0]	o_lsu_arready
i_ifu_arlock	o_lsu_wready
i_ifu_arcache[3:0]	o_lsu_bid[3:0]
i_ifu_arprot[2:0]	o_lsu_bresp[1:0]
i_ifu_arregion[3:0]	o_lsu_bvalid
i_ifu_arqos[3:0]	o_lsu_nid[3:0]
i_ifu_arvalid	o_lsu_rdata[63:0]
i_ifu_rready	o_lsu_resp[1:0]
i_lsu_awid[3:0]	o_lsu_rlast
i_lsu_awaddr[31:0]	o_lsu_rvalid
i_lsu_awlen[7:0]	o_sb_arready
i_lsu_awsized[2:0]	o_sb_arready
i_lsu_awburst[1:0]	o_sb_wready
i_lsu_awlock	o_sb_bid[0:0]
i_lsu_awcache[3:0]	o_sb_bresp[1:0]
i_lsu_awprot[2:0]	o_sb_bvalid
i_lsu_awregion[3:0]	o_sb_nid[0:0]
i_lsu_awqos[3:0]	o_sb_rdata[63:0]
i_lsu_awvalid	o_sb_resp[1:0]
i_lsu_wdata[63:0]	o_sb_rlast
i_lsu_wstrb[7:0]	o_sb_rvalid
i_lsu_wlast	o_ram_awid[5:0]
i_lsu_wvalid	o_ram_awaddr[31:0]
i_lsu_arid[3:0]	o_ram_awlen[7:0]
i_lsu_araddr[31:0]	o_ram_awsized[2:0]
i_lsu_arlen[7:0]	o_ram_awburst[1:0]
i_lsu_arsized[2:0]	o_ram_awlock
i_lsu_arburst[1:0]	o_ram_awcache[3:0]
i_lsu_arlock	o_ram_awprot[2:0]
i_lsu_arcache[3:0]	o_ram_awregion[3:0]
i_lsu_arprot[2:0]	o_ram_awqos[3:0]
i_lsu_arregion[3:0]	o_ram_awvalid
i_lsu_arqos[3:0]	o_ram_arid[5:0]
i_lsu_arvalid	o_ram_araddr[31:0]
i_lsu_bready	o_ram_arlen[7:0]
i_lsu_rready	o_ram_arsized[2:0]
i_sb_awid[0:0]	o_ram_arburst[1:0]
i_sb_awaddr[31:0]	o_ram_arlock
i_sb_awlen[7:0]	o_ram_arcache[3:0]
i_sb_awsized[2:0]	o_ram_arprot[2:0]
i_sb_awburst[1:0]	o_ram_arregion[3:0]
i_sb_awlock	o_ram_arqos[3:0]
i_sb_awcache[3:0]	o_ram_arvalid
i_sb_awprot[2:0]	o_ram_wdata[63:0]
i_sb_awregion[3:0]	o_ram_wstrb[7:0]
i_sb_awqos[3:0]	o_ram_wlast
i_sb_awvalid	o_ram_wvalid
i_sb_wdata[63:0]	o_ram_bready
i_sb_wstrb[7:0]	o_ram_rready
i_sb_wlast	wb_rom_adr_o[31:0]
i_sb_wvalid	wb_rom_dat_o[31:0]
i_sb_bready	wb_rom_sel_o[3:0]
i_sb_arid[0:0]	wb_rom_we_o
i_sb_araddr[31:0]	wb_rom_cyc_o
i_sb_arlen[7:0]	wb_rom_stb_o
i_sb_arsized[2:0]	wb_spi_flash_adr_o[31:0]
i_sb_arburst[1:0]	wb_spi_flash_dat_o[31:0]
i_sb_arlock	wb_spi_flash_sel_o[3:0]
i_sb_arcache[3:0]	wb_spi_flash_we_o
i_sb_arprot[2:0]	wb_spi_flash_cyc_o
i_sb_arregion[3:0]	wb_spi_flash_stb_o
i_sb_arqos[3:0]	wb_spi_flash_cti_o[2:0]
i_sb_arvalid	wb_spi_flash_bte_o[1:0]
i_sb_rready	wb_sys_adr_o[31:0]
i_ram_arready	wb_sys_dat_o[31:0]
i_ram_arready	wb_sys_sel_o[3:0]
i_ram_wready	wb_sys_we_o
i_ram_bid[5:0]	wb_sys_cyc_o
i_ram_bresp[1:0]	wb_sys_stb_o
i_ram_bvalid	wb_sys_cti_o[2:0]
i_ram_nid[5:0]	wb_sys_bte_o[1:0]
i_ram_rdata[63:0]	wb_uart_adr_o[31:0]
i_ram_resp[1:0]	wb_uart_dat_o[31:0]
i_ram_rlast	wb_uart_sel_o[3:0]
i_ram_rvalid	wb_uart_we_o
wb_rom_dat_i[31:0]	wb_uart_cyc_o
wb_rom_ack_i	wb_uart_stb_o
wb_spi_flash_dat_i[31:0]	wb_uart_cti_o[2:0]
wb_spi_flash_ack_i	wb_uart_bte_o[1:0]
wb_spi_flash_err_i	wb_gpio_adr_o[7:0]
wb_spi_flash_rty_i	wb_gpio_dat_o[31:0]
wb_sys_dat_i[31:0]	wb_gpio_sel_o[3:0]
wb_sys_ack_i	wb_gpio_we_o
wb_sys_err_i	wb_gpio_cyc_o
wb_sys_rty_i	wb_gpio_stb_o
wb_uart_dat_i[31:0]	wb_ptc_adr_o[31:0]
wb_uart_ack_i	wb_ptc_dat_o[31:0]
wb_uart_err_i	wb_ptc_sel_o[3:0]
wb_uart_rty_i	wb_ptc_we_o
wb_gpio_dat_i[31:0]	wb_ptc_cyc_o
wb_gpio_ack_i	wb_ptc_stb_o
wb_gpio_err_i	wb_ptc_cti_o[2:0]
wb_ptc_dat_i[31:0]	wb_ptc_bte_o[1:0]
wb_ptc_ack_i	wb_spi_accel_adr_o[31:0]
wb_ptc_err_i	wb_spi_accel_dat_o[31:0]
wb_ptc_rty_i	wb_spi_accel_sel_o[3:0]
wb_spi_accel_dat_i[31:0]	wb_spi_accel_we_o
wb_spi_accel_ack_i	wb_spi_accel_cyc_o
wb_spi_accel_err_i	wb_spi_accel_stb_o
wb_spi_accel_rty_i	wb_spi_accel_cti_o[2:0]
	wb_spi_accel_bte_o[1:0]

RTL

## intcon\_wrapper\_bd\_v1\_0