Department of Electrical Engineering Jamia Millia Islamia, New Delhi.

Power Electronics -EES- 502

(B. Tech-V Sem) (Ist Sessional Test)- Sep 2016

Time One Hour

Maximum Marks: 30

Note:

i) Answer all questions.

ii) Only scientific calculator is allowed.

Question No.1: Answer the following questions

(5 x 2)

J.

Explain the difference between controlled and uncontrolled switch?

Draw the schematic and waveform of half wave RC firing Circuit?

Explain the difference between SCR and TRIAC?

TY)

A 12 phase uncontrolled rectifier is connected to load and the rms value of input phase voltage is 230V, calculate the average output voltage?

Define the terms Ripple Factor and THD?

Question No.2: Solve the following questions.

 (2×5)

A diode circuit shown in Fig. 1 with R=20 Ohm, L=10 mH . If a load current of 20 A is flowing through freewheeling diode Dm and switch S is closed at t=0, determine the expression for the current through the switch S.

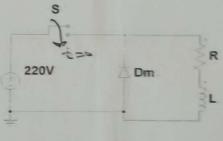
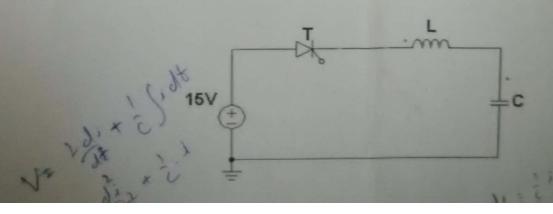


Fig. 1

II. What is commutation circuit? List types of commutation circuit? Explain working of any one with schematic and diagram.

Question No.3: Thyristor T in the figure 2 below is initially off and is triggered with a single pulse of width 10 μ s. It is given that L = $(100/\pi)\mu$ H and C = $(100/\pi)\mu$ F. Assuming latching and holding currents of the thyristor are both zero and the initial charge on C is zero, Find out the conduction time of SCR T. (1x 10)



Sep 2015

Power Electronius (EES-502)

Ith SEM
Department of Electrical Engy

Note: (1) Attempt all the questions.
(11) Calcultur is allowed.

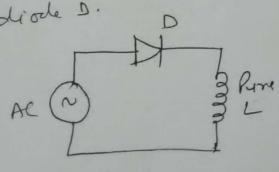
(275)

as what are the ideal characteristic of electric switch?

(b) Explain reverse record time of diode?

what is the difference between Controlled, uncontrolled and semi controlled switch?

d) In the circuit shown in fig.1, a pure inductor L is connected with diode D and inductor L is connected with diode D and Al source. Draw the current waveform of diode D.



Draw the Symbol & V-I characteristic of

F180 83

9-2 (9) Consider a phase controlled converter (5×2) Shown in Fig. 2. The thyrister is fixed at an argle of in every possitive half cycle of the input vollage, of the beak value of the instantaneons output vollege equal 230V, Calculate the firing angle d. Fig. 2. b) Explain the working of UJT Fing circuit for SCR with schemali, wavefrom? (1×10) F18.3. F18 @ 13

The & Controlled thy siste Converter is shown in fig. 3, is fed from a Sigle phone source. when the fing angle is oo, the dc output Volley of the converter is 300V, what will be the output vollage for a firing angle of 60°, assuming Continues Coduction? The Ced

Department of Electrical Engineering Jamia Millia Islamia, New Delhi. Power Electronics -EES- 502

(B. Tech- Vth Sem) (Ist Sessional Test)

Sept 2014

Time One Hour

Maximum Marks: 30

Note:

- i) Answer all questions.
- ii) Only scientific calculator is allowed.

Question No.1: Answer the following questions

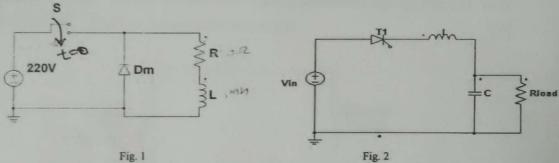
 (5×2)

- What are the ideal characteristic of an electrical switch? Explain with signal diagrams?
- П. What is reverse recovery time of diode? Explain reverse recovery time of Schottky diodes?
- III. Explain the difference between GTO and TRIAC?
- IV. A 7 phase uncontrolled rectifier is connected to load and the rms value of input phase voltage is 230V, calculate the average output voltage?
- V. Define the terms THD and Crest Factor?

Question No.2: Solve the following questions.

 (2×5)

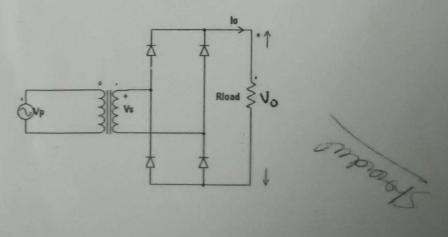
A diode circuit shown in Fig. 1 with R = 10 Ohm, L = 5 mH. If a load current of 10 A is flowing through freewheeling diode Dm and switch S is closed at t=0, determine the expression for the current through the switch S.



Identify the circuit shown in Fig. 2. Explain its working with waveforms and mathematical expression? П.

Question No.3: Given the circuit of Fig. 3, Vp = 250 Sin(wt), Transformer has 1:1 ratio, find the following: (1 x 10)

(a) - Avg Output voltage, (b)- Rms output voltage, (c)- The rectification efficiency, (d)- The Form Factor, (e)-The Ripple Factor (f)- The TUF, (g)- The PIV of any diode, (h)- The crest factor of the input current.



Department of Electrical Engineering Ist Sessional Test EES-502: Power Electronics

Vth Semester Time: 1 Hour

Sept 2013

Note: Attempt all questions.

M.M.: 15

Q.1: (A) - Define the following term with expressions:

[2]

- (a)- Total Harmonic Distortion (b)- Rectification Efficiency.
- (B) Explain the difference between general purpose, fast recovery and schottkey diodes. [3]
- Q.2: Explain the working of RC firing circuit with schematic and waveforms? What advantage it has over R firing circuit? [5]

Q.3:

(A) Draw the input voltage, current, output voltage current waveforms of the circuit shown in Fig.1. Explain for how much duration the diode D1 will conduct and why? Initial conditions are zero. [3]

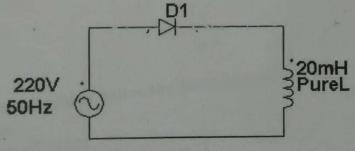


Fig.1

(B) Explain with circuit diagram, whether freewheeling diode is required in the case of full bridge diode rectifier with R-L load? [2]

Department of Electrical Engineering B.Tech. (Electrical Engg.)- V Semester

Sessional Test-1 Time: 1 Hour Power Electronics (FES-502)

Sep, 2012 Max Marks: 30

Attempt any three question.

- Q1. Describe the reverse recovery characteristic of a diode with proper diagram showing reverse recovery time. [10]
- Q2. Describe the working of RC firing circuit for thyristor with schematic and waveforms.

 What advantage it has over R firing scheme.

 [10]
- Q3. Draw the waveforms of Voltage and current across the thyristor T1 and across resistive load as shown in the schematic of Fig. 1. The typical forward breakover voltage of thyristor T1 is 800V. [10]

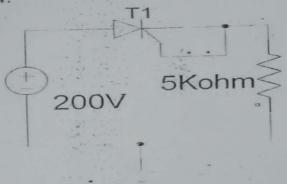
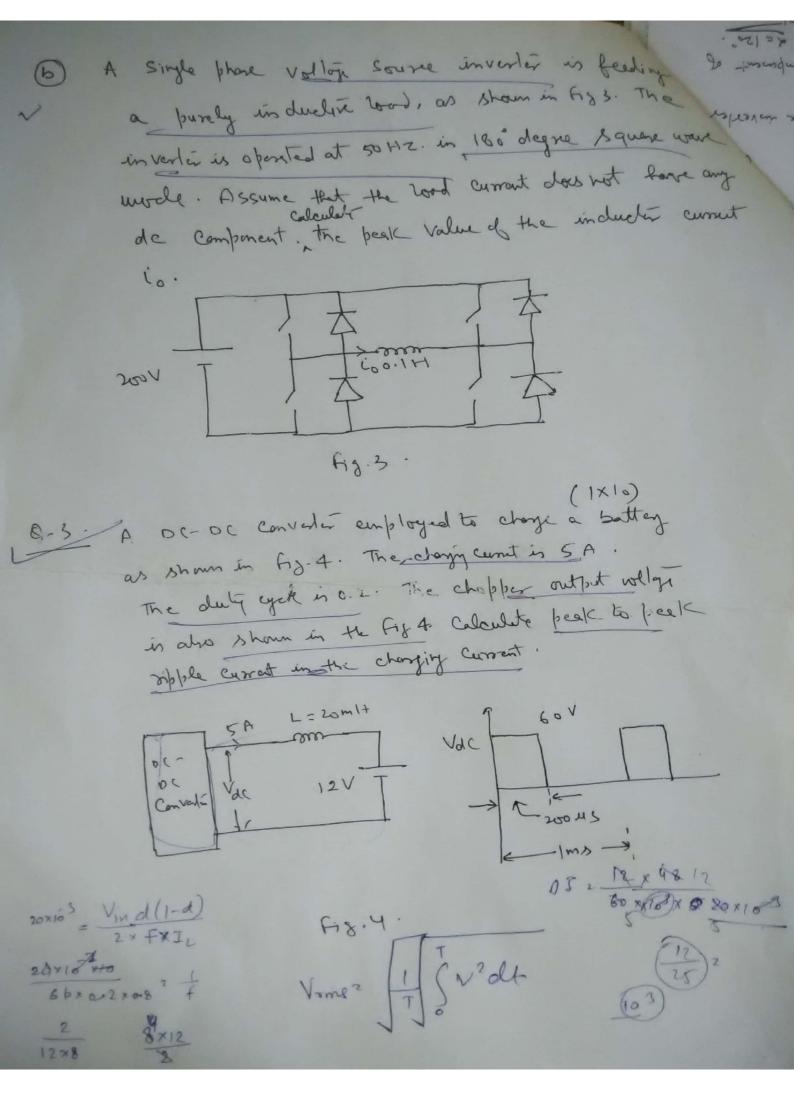


Fig. A.

- Q4. For a single phase rectifier define the following terms:
 - a)- Form Factor
 - b)- Rectification efficiency
- c)- Ripple Factor
 - d)- Crest Factor.

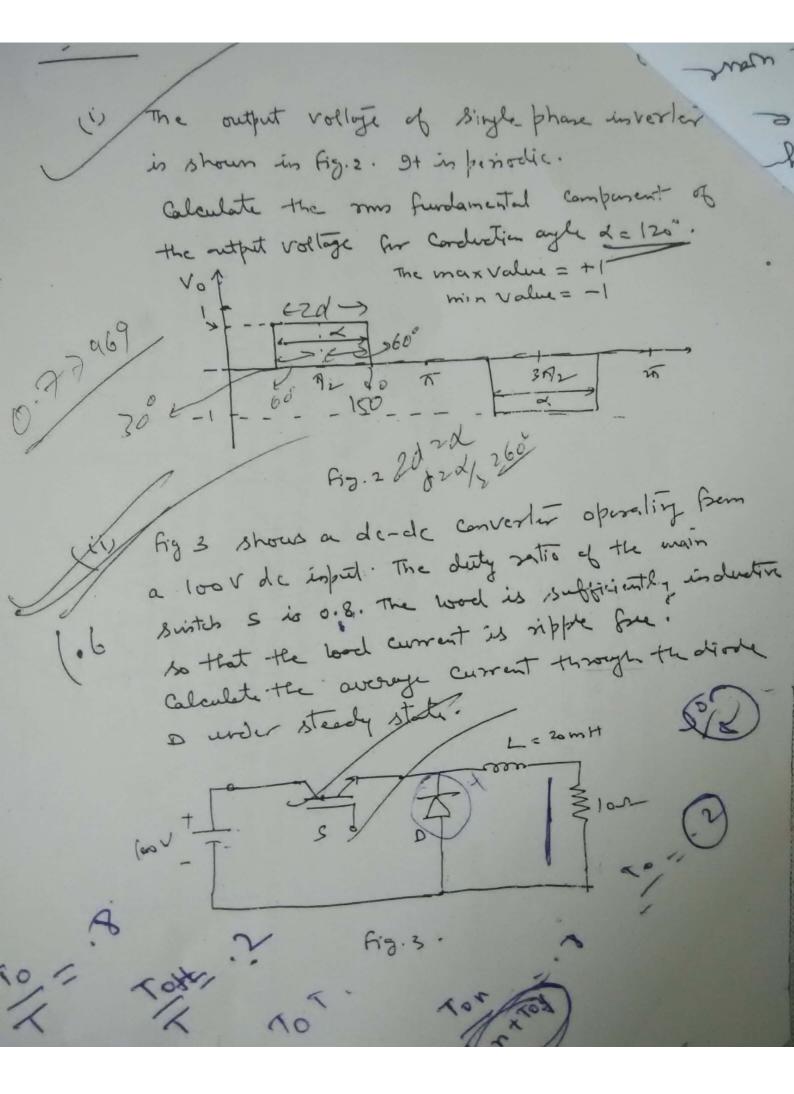
[10]

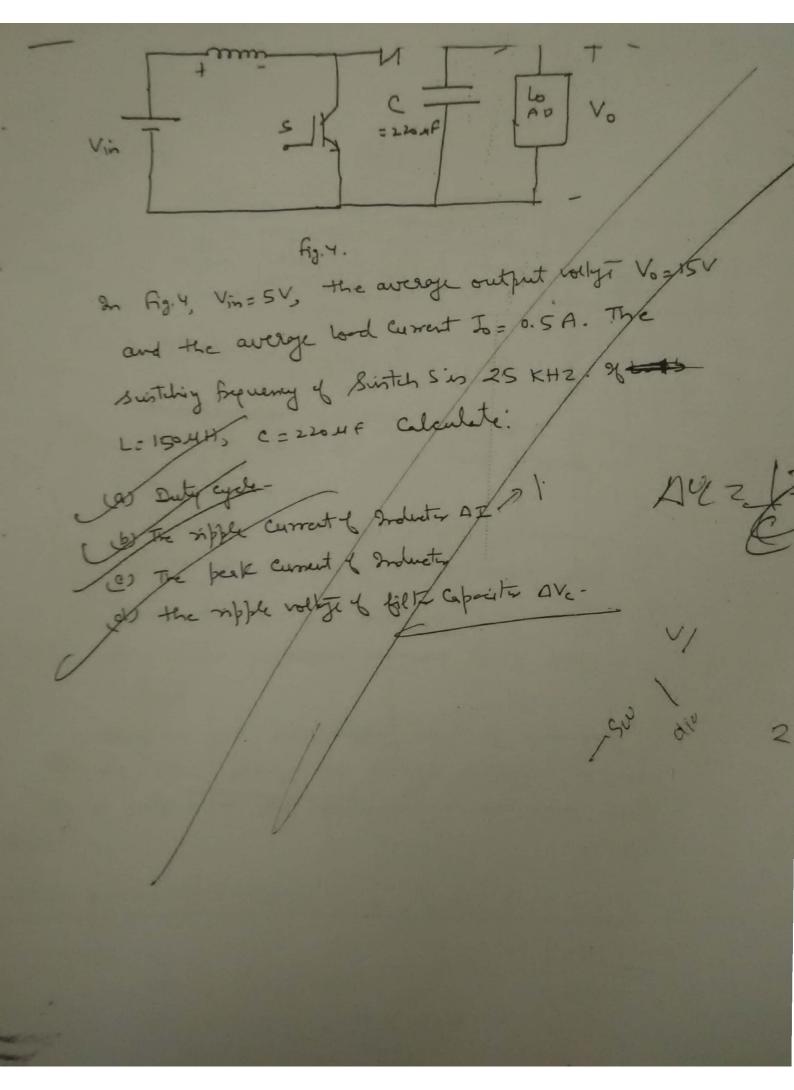
Department of Electrical Engl B. TECH - I'M SEM Dra sessionAL. Flore 2016 EES-502: POWER ELECTRONICS MM: 30 TIME: I HOW Note: Attempt All Questions (2) Draw the Schematte of Buck-Boost DC-SC consents I b) what is the difference between resurant & Ruson michig ce which method is more suitable to control duty eyes, by Varying frequency or on time of signal cd) room the gality scheme of 3-phase volling Source bridge invalit operating in 180° Corduction mode. (come only get what is the value of voz in larm of v, 24 51,52 opends at 50% duty cycle to 68.1. (5×2) In Fig. 2, a step down or-or conventor switched at 1 KHZ, with a duty cycle of 0.5. what in the peak to peak ribble load current 100V]] = 35r



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IND SESSIONAL TEST EES-502: POWER ELECTROPICS NOV 2015 Om 5 Em M.M 30 TIME: I HOUK NOTE: Attempt all questions. (225) I A single phase talk bridge mounter has input welly of 48 vgc. Investor is feeding a love of 24 m. what is the mes output velige at fundamental (ii) what is the difference between buck, bout a buck boost oc-oc Convention. (iii) Bosed on the no. of phase, which tyle of inveter is suitable for better performance and why? Explain. US Explain the working of Bingle phase Full wome inverter with R-L bond. Colculate the output noting Lig 1. Boost Buck Converte outgard ठाउँ पुरम = 50/0 fiz.1.





Department of Electrical Engineering Jamia Millia Islamia, New Delhi. Power Electronics –EES- 502 (B. Tech) – Vth Sem-(IInd Sessional Test)

Oct 2014

Time One Hour

Maximum Marks: 30

Note:

- i) Answer all questions.
- ii) Only scientific calculator is allowed.

Question No.1: Answer the following questions

(5 x 2)

I. Explain the difference between the current source and voltage source inverter?

A single phase full bridge inverter has a resistive load R = 2.4 - ohm and the dc input voltage is 48V.

Determine (a) The rms output voltage at the fundamental frequency (b)- the output power.

A single phase full bridge inverter is connected with highly industrye to determine the connect the

A single phase full bridge inverter is connected with highly inductive toad. Is it required to connect the freewheeling diode? Explain with schematic.

A MOSFET is rated for 10A, carried a periodic current as shown in Fig. 1. The ON state resistance of the MOSFET is 0.15 ohm, What is the average ON state loss in the MOSFET?

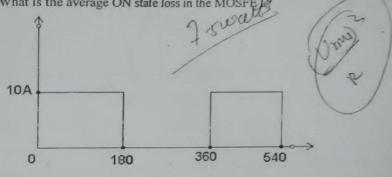
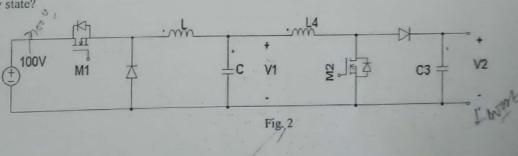


Fig. 1

In Fig 2 both the switches M1 and M2 are operating at 50% duty cycle. Find the value of V1 and V2 under steady state?

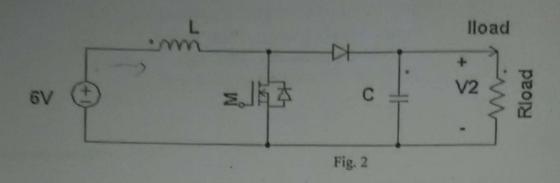


Question No.2: Solve the following questions.

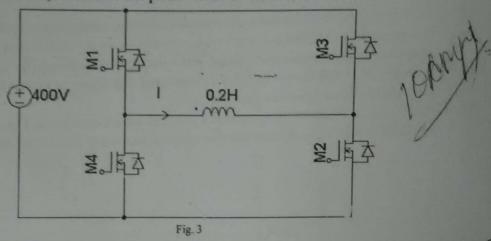
(2 x 5)

1. The DC - DC converter of Fig. 3 has the average output voltage V2 = 15V. The average load current fload = 0.5 A. The switching frequency is 20 kHz. If L= 250uH, C= 440uF, determine (a)—the duty cycle (b)—the inductor ripple current (c)—the inductor peak current (d)—the capacitor ripple voltage.

1 of 2

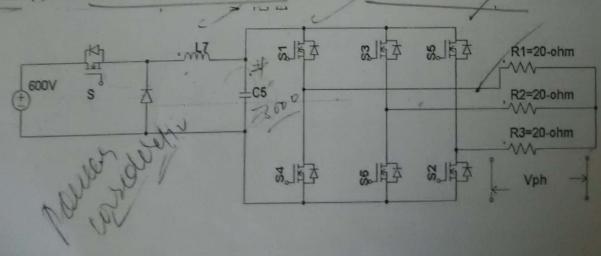


 A singe phase voltage source inverter is operated in 180 degree, 50 Hz square wave mode shown in Fig. 3. The load is highly inductive. Assume that load current does not have any dc component, calculate the peak value of inductor current I.



Question No.3:

A DC – DC converter is connected with a 3 phase inverter operating in 180 degree conduction mode, as shown in Fig. 4. The three phase balanced resistive load is connected. The duty cycle of the switch S is 50%. Determine under steady state: (a) The rms value of the load phase voltage. (b) The power consumed by the 3 phase load.



(1x10)

Department of Electrical Engineering IInd Sessional Test EES-502: Power Electronics

Vth Semester

Time: 1 Hour

Note: Attempt any three questions.

Oct 2013 M.M.: 15

Q.1: Explain the working of single phase full bridge controlled inverter with resistive load drawing schematic, waveforms and giving the equation of output voltage? [5]

- Q.2: The single phase half bridge inverter has a resistive load of R = 2.4 ohm and the dc input voltage is 48V. Calculate: [5]
 - (a) the rms output voltage at the fundamental frequency.
 - (b) The output power.
- Q.3. Explain the working of buck dc-dc converter with schematic, waveform and expressions? [5]
- Q.4: The circuit shown in Fig. (1) has an input voltage of 12V. The required average output voltage $V_a = 5V$ at R = 500 ohm and the peak to peak output ripple voltage is 20 mV. The switching frequency is 25 kHz. If the peak to peak ripple current of inductor is limited to 0.8 A, determine: [5]
 - (a)- The duty cycle
 - (b)- The inductor L
 - (c)- The Capacitor C

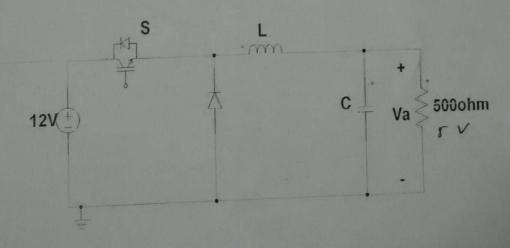


Fig (1)

B.Tech. (Electrical Engineering B.Tech. (Electrical Engg.)- V Semester Power Electronics-(EES-502)

Sessional Test-II

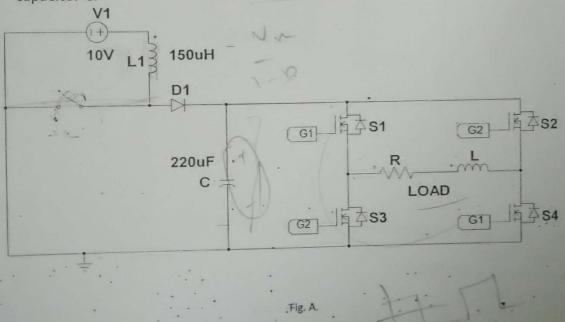
Oct-Nov, 2012

Max Marks: 30

Time: 1 Hour

Attempt any three question.

- Describe the working of 3 phase voltage source bridge inverter connected with 3 phase resistive load R- star connected in 180° conduction mode with schematic, waveforms and mathematical expressions.
 - Q2. Draw the schematic of step down converter with R-L load? Explain its working with waveforms? Derive the expression for load inductor current without making linear approximation? [10]
 - Q3. Identify the circuit shown in Fig. A. Explain its working with waveforms and expressions. If the ON time of switch Sa is 10uS and OFF time is 15uS, find out the voltage across the capacitor C. [10]



4. List the classification of DC-DC choppers? Explain any two of them with schematic and Waveforms?