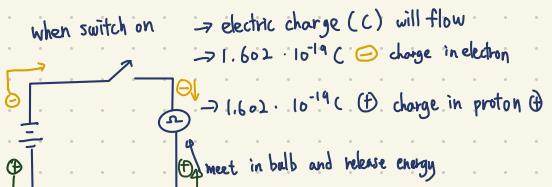


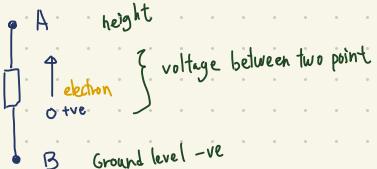


①



Energy & Voltage

\rightarrow voltage / potential difference across element
 (energy required to move a unit charge through)



②

There are two current

- \rightarrow Direct current (DC)
- \rightarrow Alternating current (AC)

$$i = \frac{dq}{dt} \rightarrow q = \int_{t_0}^t i(t) dt$$

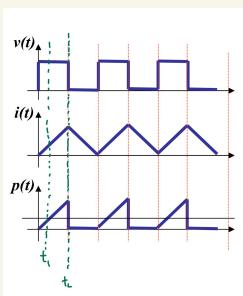
③

Direction of Current
 $(+IA = -IA)$

方向可不同
 只要 reverse the sign of current

\rightarrow Energy (provide capacity to do work) Joule (J)

\rightarrow Power (time rate expending / absorbing energy) Watt (W)



Power (W)

$$P(t) = \frac{dE}{dt} = \frac{dE}{dq} \frac{dq}{dt} = v(t) \cdot i(t)$$

Energy (J)

$$E(t) = \int_{t_0}^t P(t) dt = \int_{t_0}^t v(t) i(t) dt$$

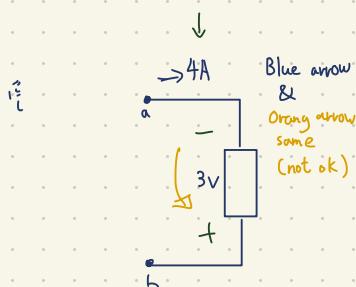
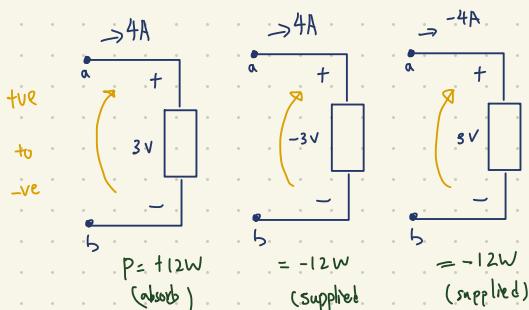
Average Power (W)

$$\text{Average} = \frac{1}{T} \int_{t_0}^{t_0+T} P(t) dt$$

Polarity of Power

→ Passive sign convention

(Active source vs Passive source)

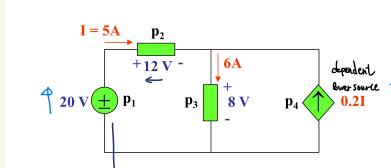


$$\begin{aligned} \textcircled{1} \text{ reverse } V &= -3V \cdot 4A = -12W \\ \textcircled{2} \text{ reverse } I &= 3V \cdot -4A = -12W \end{aligned}$$

Law of conservation energy

→ Power supplied = Power absorbed

→ Sum of power in circuit always 0 $\sum P = 0$



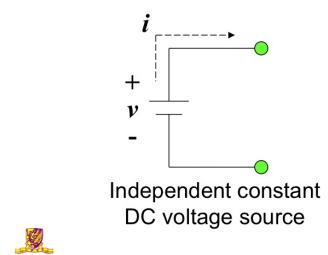
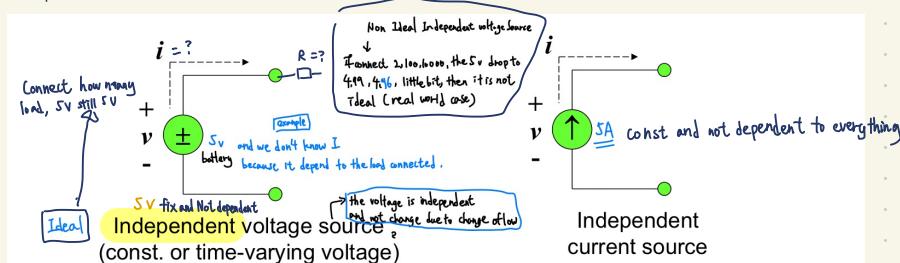
$$P_1 : 20V \cdot -5A = -100W$$

$$P_2 : 12V \cdot 5A = 60W$$

$$P_3 : 8 \cdot 6A = 48W$$

$$P_4 : 8 \cdot (0.2 \cdot 5) = 8 \cdot 0.2 \cdot 5 = -8W$$

$$P_1 + P_2 + P_3 + P_4 = 0$$



• Voltage source

- Voltage defined.
- Current depends on load connected to source.

• Current source

- Current defined.
- Voltage depends on load connected to source.

Resistor

$$\rightarrow v = iR$$

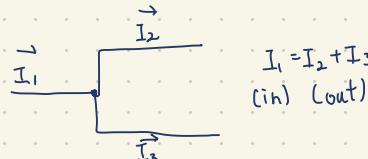
Ohm's law

- only applied in close circuit.
- Open circuit, $R=\infty, i=0$
- short circuit, $R=0, i=\infty$

$$\begin{aligned} \rightarrow P &= vi = i^2 R = \frac{v^2}{R} \\ &= vi = v^2 G = \frac{i^2}{G} \end{aligned}$$

G for conductance

For node circuit

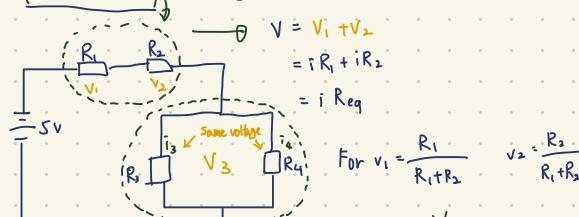


$$I_1 = I_2 + I_3$$

(in) (out)

voltage divider

① Resistor in series (假設無 R_2, R_3, V_2)



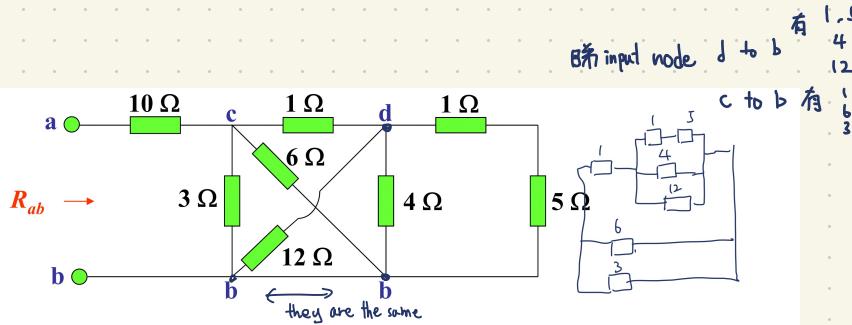
$$\text{For } V_1 = \frac{R_1}{R_1+R_2} \quad V_2 = \frac{R_2}{R_1+R_2}$$

$$V_3$$

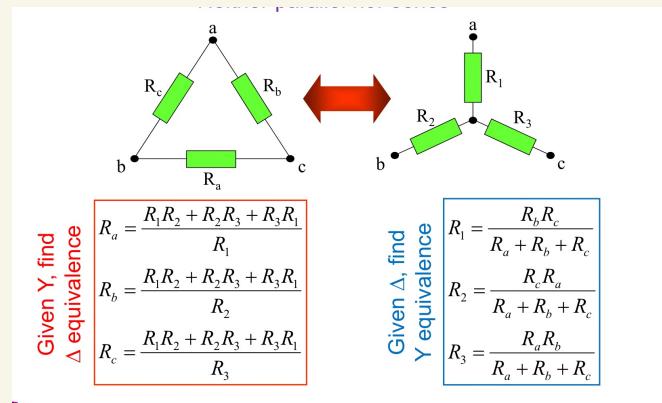
$$i_{in} = i_3 + i_4$$

$$= \frac{v}{R_3} + \frac{v}{R_4}$$

$$\frac{1}{R_{eq}} = \frac{1}{R_3} + \frac{1}{R_4}$$



有 1.5
4
12
有 1
6
3



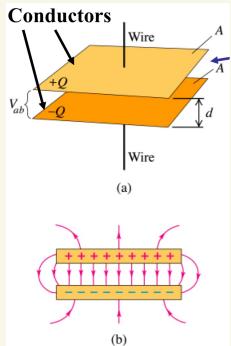
Given Δ , find Δ equivalence

$$\begin{aligned} R_a &= \frac{R_1 R_2 + R_2 R_3 + R_3 R_1}{R_1} \\ R_b &= \frac{R_1 R_2 + R_2 R_3 + R_3 R_1}{R_2} \\ R_c &= \frac{R_1 R_2 + R_2 R_3 + R_3 R_1}{R_3} \end{aligned}$$

Given Δ , find γ equivalence

$$\begin{aligned} R_1 &= \frac{R_b R_c}{R_a + R_b + R_c} \\ R_2 &= \frac{R_c R_a}{R_a + R_b + R_c} \\ R_3 &= \frac{R_a R_b}{R_a + R_b + R_c} \end{aligned}$$

Capacitor 電容 → store charges (Energy)



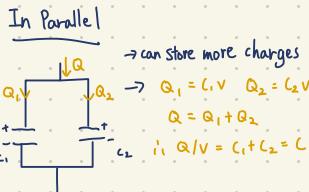
$$Q = C V$$

C capacitance in (F) Farad

$$C = \epsilon A / d$$

↑ area
↑ distance

permittivity of material / dielectric between plate



$$\frac{1}{C_1} \parallel \frac{1}{C_2} \equiv \frac{1}{C} \quad \text{assuming } C_1, C_2 \neq 0$$

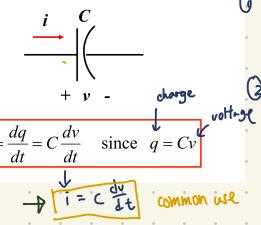
By, $V_1 = \frac{Q_1}{C_1}, V_2 = \frac{Q_2}{C_2}$

$$V = V_1 + V_2$$

$$\frac{V}{C} = \frac{1}{C_1} + \frac{1}{C_2}$$

$$\therefore \frac{1}{C} = \frac{1}{C_1} + \frac{1}{C_2}$$

$$C = \frac{C_1 C_2}{C_1 + C_2}$$



① when v is constant ($D C$),
 $i = 0$ (since capacitor behave like open circuit)

② v can't change in step because it result $i \rightarrow \infty$



$$P = V \cdot i = V \cdot C \frac{dv}{dt}$$

$$E(t) = \int_{-\infty}^t P(t') dt'$$

$$= \int_{-\infty}^t C \cdot v \frac{dv}{dt} dt$$

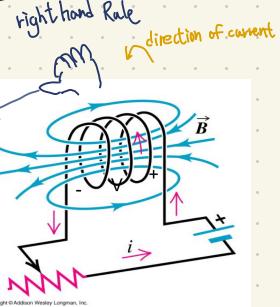
$$= C \int_{-\infty}^t v(t) dv$$

$$\text{Energy stored: } E(t) = \frac{1}{2} C \cdot V^2 = \frac{Q^2}{2C} \quad \text{assuming } v(-\infty) = 0$$

* Energy in Capacitor transfer energy faster than energy in battery. (due to chemical reaction in battery slow down)

* Energy stored in Capacitor lower than a battery

Inductor



inductance (Henry (H))

$$V = L \frac{di}{dt}$$

where $L = \frac{N^2 \mu A}{l}$

- ↑ No. of turn
- ↑ Permeability of core
- Cross-sectional area
- length of flux length



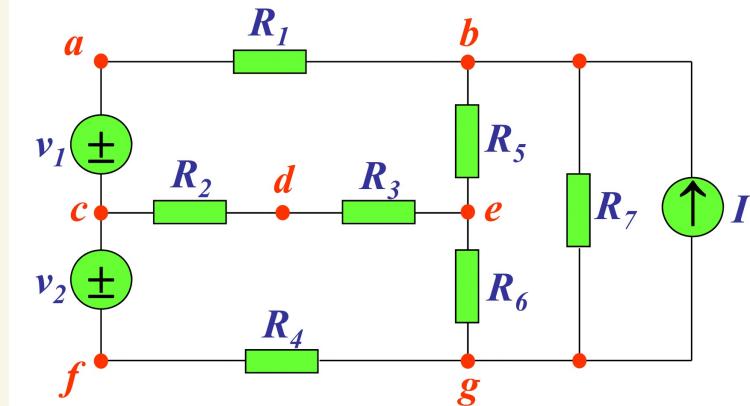
Node

node : point of two or more circuit join (a,b,c,d,e,f,g)

Essential node : point of three or more circuit element joined (b,c,e,g)

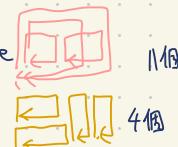
Branch : circuit element between 2 node

Essential Branch : circuit element between 2 essential node (R_6)



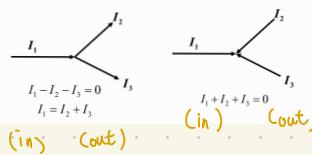
Path: 之路 that element not include more than once

Loop: closed path with no node pass more than once

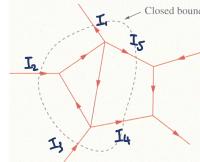


Mesh: a loop that doesn't enclose any other loop

KCL (Kirchhoff's Current Law)



$$\text{Current}_{\text{in}} = \text{Current}_{\text{out}}$$

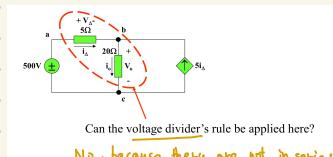


$$\sum_{N} i_n = 0$$

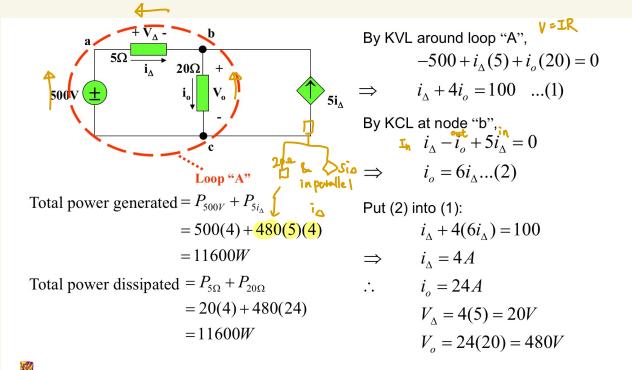
$$(\text{in}) \quad (\text{out}) \\ \text{For this case: } I_2 + I_3 = I_1 + I_4 + I_5$$

KVL (Kirchhoff's Voltage Law)

→ Algebraic sum of all voltages around a closed path / a loop

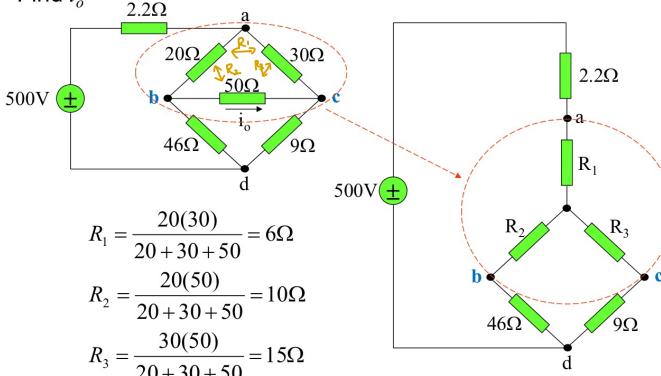


No, because they are not in series.
(即使兩路元件 in series, in series 係指所有元件有同一 current, 但 $i_o \neq i_\Delta$ in 2 resistor)

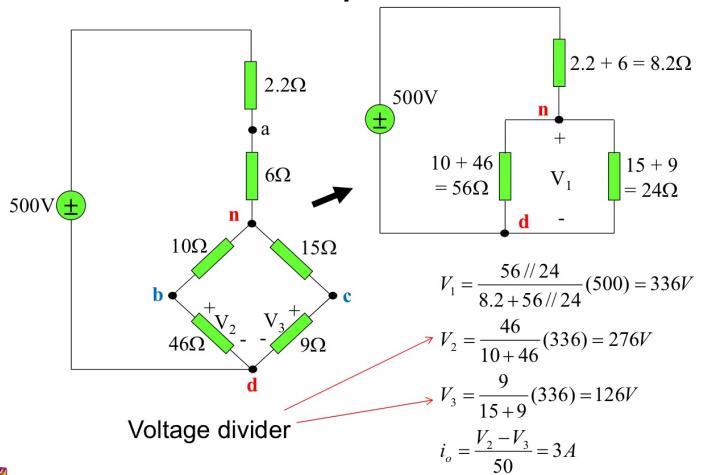


Example 3.2

Find i_o



Example 3.2

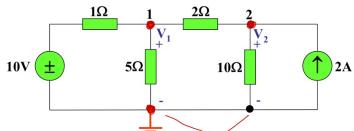


Nodal Analysis (KCL we repeatly)

Procedure

Steps:

1. Identify all **essential nodes** (•); no. of nodes = n_e (3)
2. Choose and label the **reference node** with \ominus (usually it connects most branches, but theoretically arbitrary).
3. Assign node voltage variable (with respect to the reference node) to each essential node. (V_1, V_2, \dots, V_3 no need $(B, V_2=0)$ reference node)
4. Write one **KCL** equation for each essential node to give $(n_e - 1)$ equations in total.
5. Solve the linear equations.



they are same node B_g

KCL at node 1: $\frac{(V_1 - 10)}{1} + \frac{V_1 - 0}{5} + \frac{V_1 - V_2}{2} = 0$

KCL at node 2: $\frac{V_2 - 0}{10} + \frac{V_2 - V_1}{2} - 2 = 0$

$$17V_1 - 5V_2 = 100$$

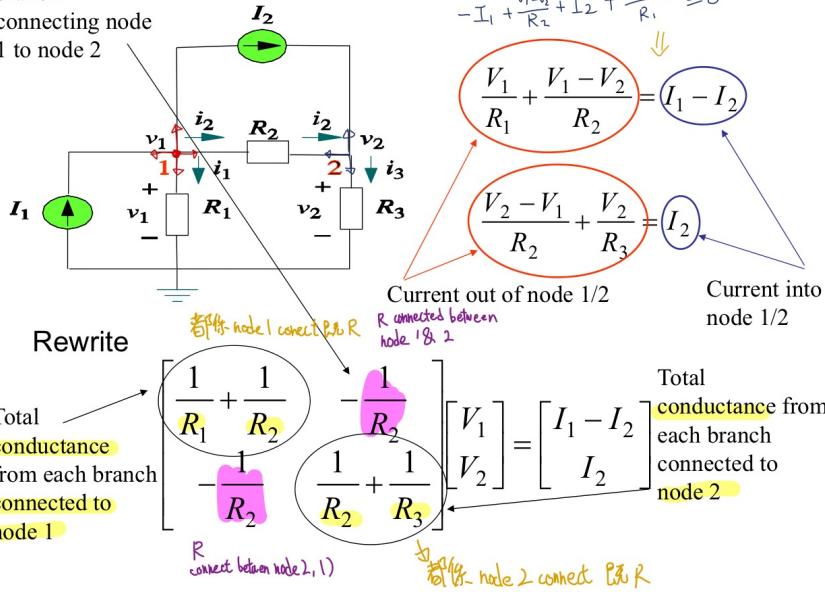
$$-5V_1 + 6V_2 = 20$$

$$\begin{pmatrix} 17 & -5 \\ -5 & 6 \end{pmatrix} \begin{pmatrix} V_1 \\ V_2 \end{pmatrix} = \begin{pmatrix} 100 \\ 20 \end{pmatrix}$$

$$V_1 = 9.09V$$

$$V_2 = 10.91V$$

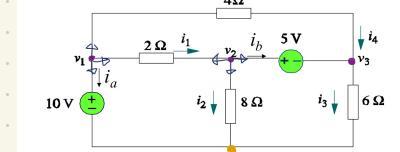
Conductance of branch connecting node 1 to node 2



Example 3.4

Important!

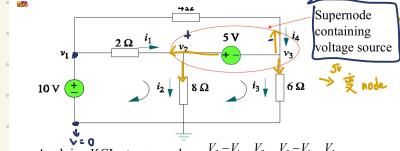
- So far, only current sources connected to essential nodes. (voltage source not connect to essential node)
- Procedure has to change slightly with voltage sources connected to essential nodes.



$$\text{Applying KCL at node 1: } i_a + \frac{V_1 - V_2}{2} + \frac{V_1 - V_3}{4} = 0$$

$$\text{Applying KCL at node 2: } \frac{V_2 - V_1}{2} + \frac{V_2 - V_4}{8} + i_b = 0$$

$$\text{Applying KCL at node 3: } \frac{V_3 - V_1}{4} - i_b + \frac{V_3 - V_4}{6} = 0$$

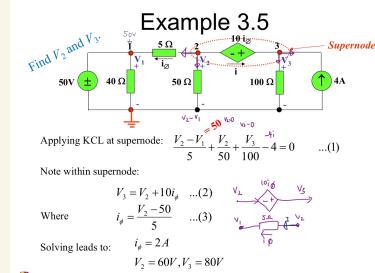


Note within supernode:

$$V_2 - V_3 = 5$$

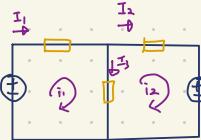
$$V_1 = 10$$

$$V_3 = 60$$



Mesh

→ loop without other loop inside (Not work with current source in mesh)



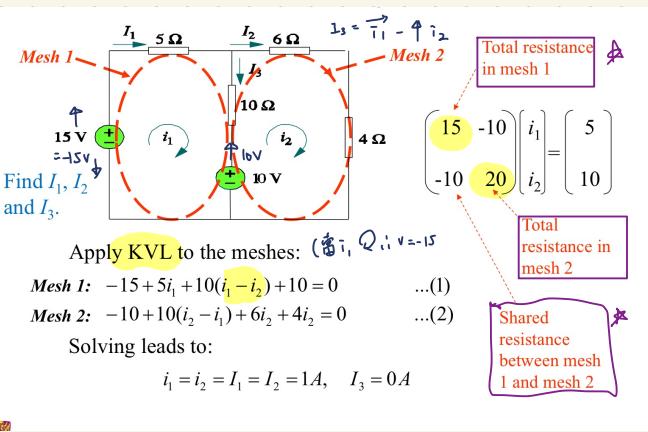
$$I_1 = i_1$$

$$I_2 = i_2$$

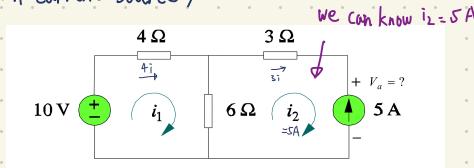
$$I_3 = i_1 - i_2$$

(work only when voltage source in each mesh) *

KVL



With current source,



$$4i_1 - 10V + 6(i_1 - i_2) = 0 \quad (1)$$

$$3i_2 + 6(i_2 - i_1) + V_a = 0 \quad (2)$$

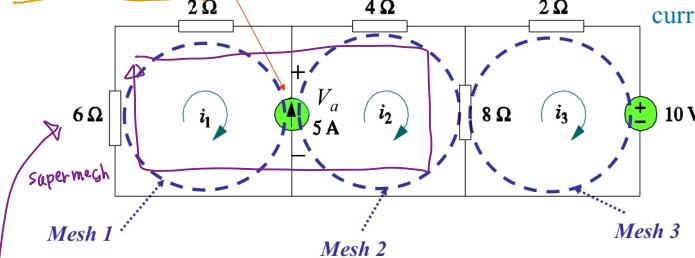
By $i_2 = 5A$,

Current source shared by two meshes *

Example 3.6

Common current source shared by two meshes

Find all mesh currents.



X Apply KVL to mesh 1: $6i_1 + 2i_1 + V_a = 0$

$$V_a = ?$$

$$-V_a + 4i_2 + 8(i_2 - i_3) = 0$$

✓ Apply KVL to mesh 3: $8(i_3 - i_2) + 2i_3 + 10 = 0 \rightarrow (3)$

✓ Current source constraint: $i_2 - i_1 = 5 \rightarrow (4)$

✓ Apply KVL to supermesh: $6i_1 + 2i_1 + 4i_2 + 8(i_2 - i_3) = 0 \rightarrow (5)$

唔用有 V_a 的方程 *

$$\rightarrow i_2 = 5 + i_1$$

$$\therefore 8i_1 + 12(5 + i_1) - 8i_3 = 0$$

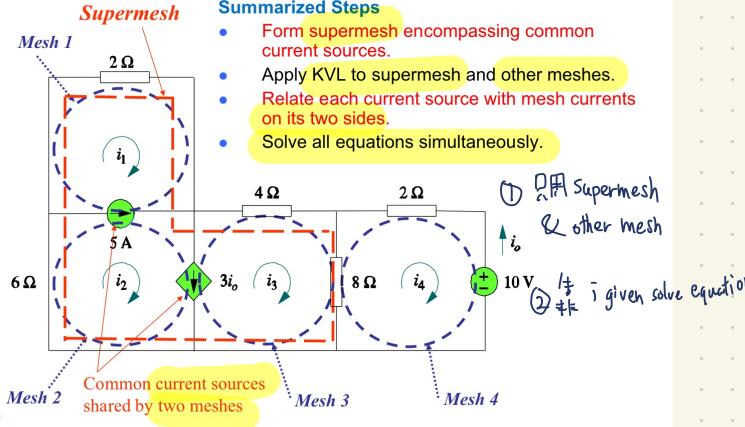
$$20i_1 - 8i_3 = 60$$

$$10i_3 - 8i_2 = 10$$

$$10i_3 - 8i_1 = 50$$

↓

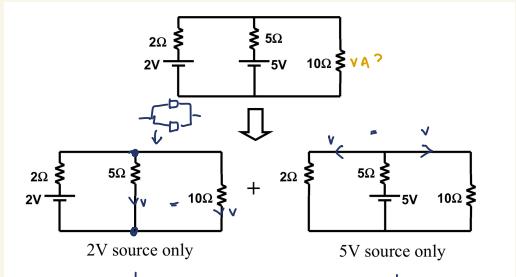
Mesh Analysis with Supermesh



Part 4

→ Linear circuit

Superposition → 分做 2 个 subcircuit



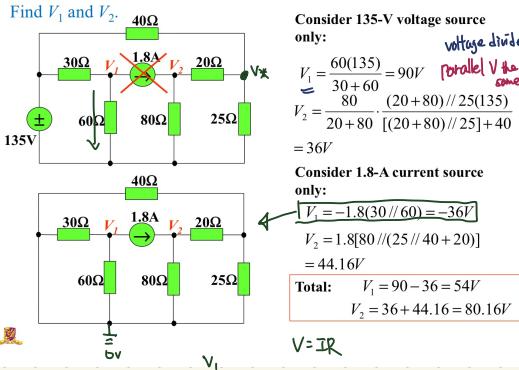
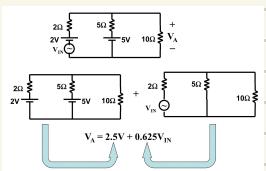
$$V_1 = \frac{10}{2+10} \cdot 2 = 1.25V$$

$$V_2 = \frac{10}{5+10} \cdot 5V = 1.25V$$

$$\therefore V_{12a} = V_1 + V_2 = 2.5V$$

→ can do longer working than nodal/mesh analyses

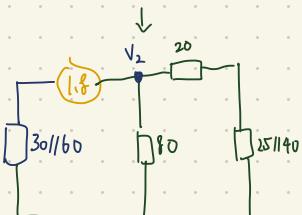
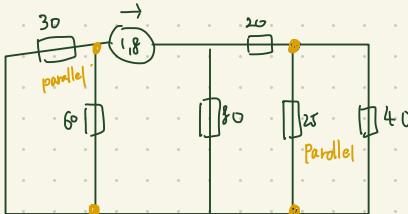
case ①: can mix AC & DC circuit



$$V_2 = V_x \cdot \frac{80}{20+80}$$

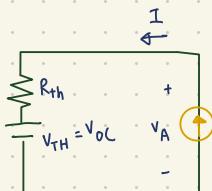
$$= \frac{100/125+40}{100/125+40} \cdot \frac{80}{20+80} \cdot 135$$

! Current same in series



$$V_2 = \frac{80}{(25//40+20)} \cdot 3\Omega/60$$

$$V_1 = -1.8 \cdot (3\Omega/60)$$



$$V_A = V_{OC} + R_{Th} \cdot I$$

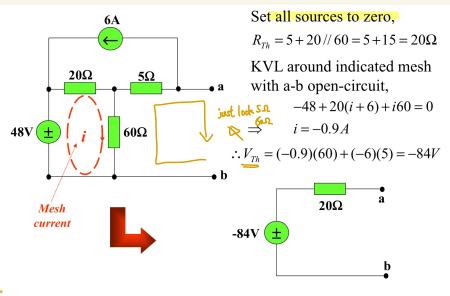
$V_{TH} = V_{OC}$ = Open circuit voltage

$$R_{Th} = V_{TH} / I_{SC} = V_{OC} / I_{SC}$$

$$R_{Th} = \frac{V_{OC} - V}{I}$$

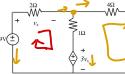
In case short-circuit current too high, need a R to

Method 1 (No dependent source)



1. Find KVL

Step 1, find V_{Th} , make an open circuit at R_L



$$2i + 3v_x = 9$$

$$2i = v_x + 3v_x = 7v_x$$

$$v_x = 7$$

Maximum power obtained only when $R_L = R_{Th}$

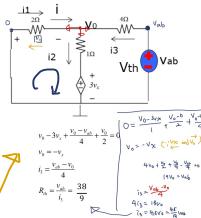
$$P_{max} = \frac{V_{Th}^2}{4R_{Th}} = 2.9W$$

$$2i - 9 + 1i + 3v_x = 0$$

$$3i + 3v_x = 9 \Rightarrow i = 1$$

$$2i = 2 \Rightarrow i = 1$$

Step 2, find R_{Th} , turn off independent source and suppose a voltage source at V_{ab}

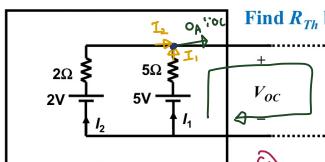


2. KCL

Method 3

Example 4.2

Open circuit condition



Find R_{Th} by using Method 3

By KCL $I_2 = -I_1$

By KVL, $2 + 2I_2 - 5 - 5I_1 = 0$

$\therefore I_1 = 5 - 2I_2$

$I_2 = \frac{3}{7}A$

① Short-circuit

$$I_1 = \frac{5}{5} = 1A$$

$$I_2 = \frac{2}{2} = 1A$$

$$I_{SC} = I_1 + I_2 = 2A$$

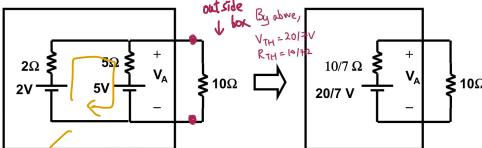
$$I_1 = -I_2 = \frac{5-2}{5+2} = \frac{3}{7}A$$

$$V_{OC} = 5 - 5 \times \frac{3}{7} = \frac{20}{7}V$$

$$\therefore R_{Th} = \frac{V_{OC}}{I_{SC}} = \frac{10}{7}\Omega$$

$$\text{Method 1: set } V = 0, R_{TH} = \frac{1}{\frac{1}{2} + \frac{1}{3}} = \frac{6}{5}\Omega$$

• Find V_A using Thevenin's equivalent circuit.



Application of voltage divider formula

$$V_a = \frac{10}{10 + \frac{10}{7}} * \frac{20}{7}V = 2.5V$$

$$2i + 5i - 2 - 5 = 0$$

$$7i = 7$$

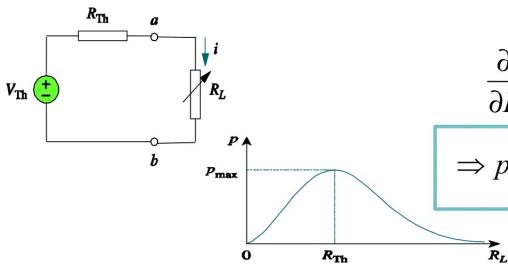
$$i = 1$$

- For Thevenin's circuit below, maximum power transfer occurs when tested with load resistance

$$R_L = R_{Th}$$

$$i = \frac{V_{Th}}{R_{Th} + R_L} \quad \text{when } R_L = R_{Th} \text{ max}$$

- Proof: Power delivered to load



Find the value of R_L for maximum power transfer in the circuit of Fig. 4.50. Find the maximum power.

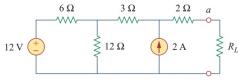
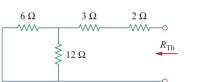


Figure 4.50
For Example 4.13.

Solution:

We need to find the Thevenin resistance R_{Th} and the Thevenin voltage V_{Th} across the terminals $a-b$. To get R_{Th} , we use the circuit in Fig. 4.51(a) and obtain

$$R_{Th} = 2 + 3 + 6 \parallel 12 = 5 + \frac{6 \times 12}{18} = 9 \Omega$$



Example 4.13

Chapter 4 Circuit Theorems

To get V_{Th} , we consider the circuit in Fig. 4.51(b). Applying mesh analysis gives

$$-12 + 18i_1 - 12i_2 = 0, \quad i_2 = -2 \text{ A}$$

Solving for i_1 , we get $i_1 = -2/3$. Applying KVL around the outer loop to get V_{Th} across terminals $a-b$, we obtain

$$-12 + 6i_1 + 3i_2 + 2(0) + V_{Th} = 0 \Rightarrow V_{Th} = 22 \text{ V}$$

For maximum power transfer,

$$R_L = R_{Th} = 9 \Omega$$

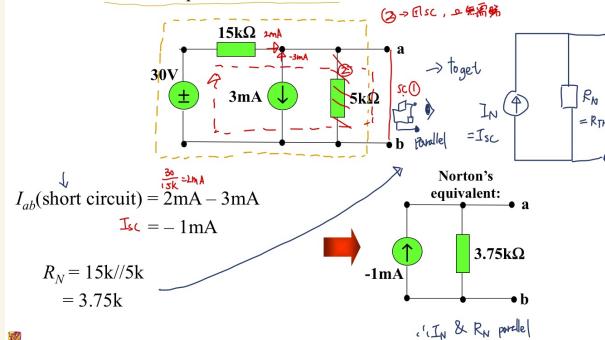
and the maximum power is

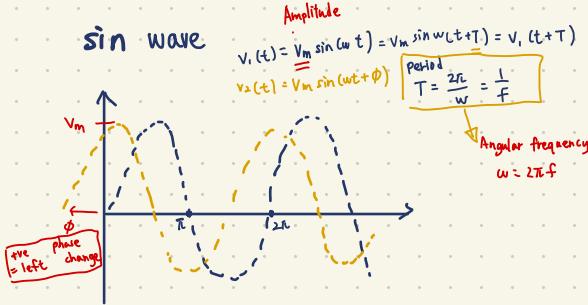
$$P_{max} = \frac{V_{Th}^2}{4R_L} = \frac{22^2}{4 \times 9} = 13.44 \text{ W}$$

Figure 4.51
For Example 4.13: (a) finding R_{Th} , (b) finding V_{Th} .

Example 4.5

Find Norton's equivalent circuit between terminals a and b.





Impedance of Resistor

Time domain: $v(t) = R \times i(t)$

Phasor domain: $V = R \times I$

Impedance: $Z_R = V/I = R$ = resistance

Impedance of Capacitor

Time domain: $i(t) = C \times \frac{d}{dt}(v(t))$

Phasor domain: $I = C \times j\omega(V)$
(Still remember $\frac{d}{dt} \rightarrow j\omega$ in phasor domain)

Impedance: $Z_C = V/I = \frac{1}{j\omega C} = -\frac{1}{\omega C}$

Reactance: $X_C = -\frac{1}{\omega C}$

V lags I by 90° or I leads V by 90° because of $-j$

Impedance of Inductor

Time domain: $v(t) = L \times \frac{d}{dt}(i(t))$

Phasor domain: $V = L \times j\omega(I)$
(Still remember $\frac{d}{dt} \rightarrow j\omega$ in phasor domain)

Impedance: $Z_L = V/I = j\omega L$

Reactance: $X_L = \omega L$

V leads I by 90° or I lags V by 90° because of $+j$

$$Z = R + jX$$

Resistance: $R = Re(Z)$

Reactance: $X = Im(Z) = \begin{cases} \text{positive} \rightarrow \text{inductive} \\ \text{negative} \rightarrow \text{capacitive} \end{cases}$

Example 5.1

Given
Find $i(t)$ given $v(t) = 15\cos(\omega_0 t)$

$$R = 15\Omega; C = 800\mu F; L = 0.2H$$

$$\omega_0 = 50rad/s$$

Then $Z_R = 15\Omega$

$$Z_C = \frac{1}{j\omega_0 C} = -j25\Omega$$

$$Z_L = j\omega_0 L = j10\Omega$$

$$R + jX = 15\Omega - j15\Omega$$

$$Z = 15\sqrt{2}\angle -45^\circ$$

If $v(t) = 15\cos(\omega_0 t) \Rightarrow V = 15\angle 0^\circ$

Then $I = \frac{V}{Z} = \frac{15}{15\sqrt{2}\angle -45^\circ} = \frac{1}{\sqrt{2}}\angle 45^\circ$

$$i(t) = \frac{1}{\sqrt{2}} \cos(\omega_0 t + \frac{\pi}{4})$$

$$= \boxed{\quad}$$

(In Series)

Example 5.1

Given $v_1(t)$ Find $i(t)$ given $v(t) = 15\cos(\omega_0 t)$

$v_1(t)$ C $v(t)$ L $0.2H$

$i(t)$ R 15Ω $v_3(t)$

magnitude = $15\sqrt{2}$

phase = -45°

Example 5.2

Determine $v_s(t)$, $i_L(t)$ and $i_C(t)$

Then $I_s = 3\angle -30^\circ$

\Updownarrow

$i_s(t) = 30 \cos(5000t)$

\Downarrow

$I_R = 3S = \left(\frac{1}{R}\right) = \frac{1}{5} \Downarrow$

$I_R = 3V_s$

\Downarrow

$I_L = \frac{1}{j\omega_0 L} = -jS$

\Downarrow

$I_L = -jV_s$

\Downarrow

$I_C = j4S$

\Downarrow

$I_C = j4V_s$

\Downarrow

$I = I_R + I_L + I_C = (3 + j3)V_s$

\Downarrow

$I = I_s$

\Downarrow

$I_s = 30$

\Downarrow

$I_s = 5$

\Downarrow

$I_s = 10$

\Downarrow

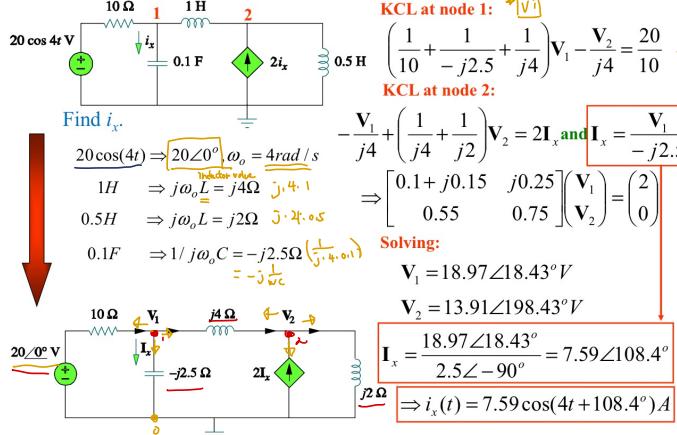
$I_s = 10\angle -45^\circ$

$$\frac{30}{3+j3}$$

$$= 5\angle -45^\circ$$

$$= \boxed{\quad}$$

Example 5.3 – Nodal Analysis

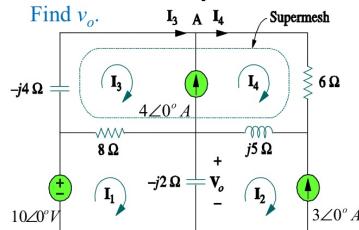


$$\begin{aligned} 20 \cos(4t) &\Rightarrow 20 \angle 0^\circ, \omega_o = 4 \text{ rad/s} \\ 1H &\Rightarrow j\omega_o L = j4\Omega \\ 0.5H &\Rightarrow j\omega_o L = j2\Omega \\ 0.1F &\Rightarrow 1/j\omega_o C = -j2.5\Omega \end{aligned}$$

$$V_1 = I_x R \Rightarrow I_x = \frac{V_1}{-j2.5}$$

$$\begin{pmatrix} 0.1 + j0.15 & -0.25 \\ -0.55 & 0.75 \end{pmatrix} \begin{pmatrix} V_1 \\ V_2 \end{pmatrix} = \begin{pmatrix} 2 \\ 0 \end{pmatrix}$$

Example 5.4 – Mesh Analysis



Mesh 1: $(8 - j2)I_1 + j2I_2 - 8I_3 = 10$

Mesh 2: $I_2 = -3$

Supermesh:

$$-8I_1 - j5I_2 + (8 - j4)I_3 + (6 + j5)I_4 = 0$$

Current source constraint between mesh 3 and 4: $I_4 = I_3 + 4$

From Mesh 1 and Mesh 2:

$$(8 - j2)I_1 - 8I_3 = 10 + j6$$

From Mesh 2 and Supermesh:

$$-8I_1 + (14 + j)I_3 = -j15 - 24 - j20$$

Then:

$$\begin{bmatrix} 8 - j2 & -8 \\ -8 & 14 + j \end{bmatrix} \begin{bmatrix} I_1 \\ I_3 \end{bmatrix} = \begin{bmatrix} 10 + j6 \\ -24 - j35 \end{bmatrix}$$

$$I_1 = \frac{(14 + j)(10 + j6) + 8(-24 - j35)}{(8 - j2)(14 + j) - 64}$$

$$\therefore I_1 = \frac{-58 - j186}{50 - j20} = 3.618 \angle 274.5^\circ A$$

$$= 0.284 - j3.607 A$$

$$V_o = -j2(I_1 - I_2) = -j2(3.284 - j3.607)$$

$$= 9.756 \angle 222.32^\circ V$$

circuit operates at $\omega = 50$ rad/s.

Solution:

Let

$$Z_1 = \text{Impedance of the } 2\text{-mF capacitor}$$

$$Z_2 = \text{Impedance of the } 3\text{-}\Omega \text{ resistor in series with the } 10\text{-mF capacitor}$$

$$Z_3 = \text{Impedance of the } 0.2\text{-H inductor in series with the } 8\text{-}\Omega \text{ resistor}$$

Then

$$Z_1 = \frac{1}{j\omega C} = \frac{1}{j50 \times 2 \times 10^{-3}} = -j10 \Omega$$

$$Z_2 = 3 + \frac{1}{j\omega C} = 3 + \frac{1}{j50 \times 10 \times 10^{-3}} = (3 - j2) \Omega$$

$$Z_3 = 8 + j\omega L = 8 + j50 \times 0.2 = (8 + j10) \Omega$$

The input impedance is

$$\begin{aligned} Z_{in} &= Z_1 \parallel Z_2 \parallel Z_3 = -j10 + \frac{(3 - j2)(8 + j10)}{11 + j8} \\ &= -j10 + \frac{(44 + j14)(11 - j8)}{11^2 + 8^2} = -j10 + 3.22 - j1.07 \Omega \end{aligned}$$

Thus,

$$Z_{in} = 3.22 - j11.07 \Omega$$

Example 9.11

Determine $v_o(t)$ in the circuit of Fig. 9.25.

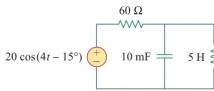


Figure 9.25
For Example 9.11.

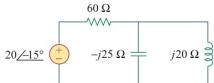
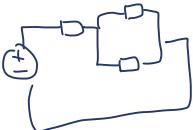


Figure 9.26
The frequency domain equivalent of the circuit in Fig. 9.25.



Solution:

To do the analysis in the frequency domain, we must first transform the time domain circuit in Fig. 9.25 to the phasor domain equivalent in Fig. 9.26. The transformation produces

$$\begin{aligned} v_s &= 20 \cos(4t - 15^\circ) \Rightarrow V_s = 20 \angle -15^\circ \text{ V}, \quad \omega = 4 \\ 10 \text{ mF} &\Rightarrow \frac{1}{j\omega C} = \frac{1}{j4 \times 10 \times 10^{-3}} = -j25 \Omega \\ 5 \text{ H} &\Rightarrow j\omega L = j4 \times 5 = j20 \Omega \end{aligned}$$

Let

$$Z_1 = \text{Impedance of the } 60\text{-}\Omega \text{ resistor}$$

$$Z_2 = \text{Impedance of the parallel combination of the } 10\text{-mF capacitor and the } 5\text{-H inductor}$$

Then $Z_1 = 60 \Omega$ and

$$Z_2 = -j25 \parallel j20 = \frac{-j25 \times j20}{-j25 + j20} = j100 \Omega$$

By the voltage-division principle,

$$\begin{aligned} V_o &= \frac{Z_2}{Z_1 + Z_2} V_s = \frac{j100}{60 + j100} (20 \angle -15^\circ) \\ &= (0.8575 \angle 30.96^\circ) (20 \angle -15^\circ) = 17.15 \angle 15.96^\circ \text{ V} \end{aligned}$$

We convert this to the time domain and obtain

$$v_o(t) = 17.15 \cos(4t + 15.96^\circ) \text{ V}$$

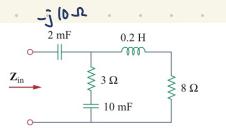


Figure 9.23
For Example 9.10.

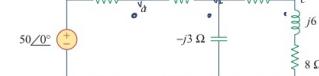
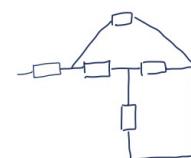


Figure 9.28
For Example 9.12.



Solution:

The delta network connected to nodes a , b , and c can be converted to the Y network of Fig. 9.29. We obtain the Y impedances as follows using Eq. (9.68):

$$\begin{aligned} Z_{an} &= \frac{j4(2 - j4)}{j4 + 2 - j4 + 8} = \frac{4(4 + j2)}{10} = (1.6 + j0.8) \Omega \\ Z_{bn} &= \frac{j4(8)}{10} = j3.2 \Omega, \quad Z_{cn} = \frac{8(2 - j4)}{10} = (1.6 - j3.2) \Omega \end{aligned}$$

The total impedance at the source terminals is

$$\begin{aligned} Z &= 12 + Z_{an} + (Z_{bn} - j3) \parallel (Z_{an} + j6 + 8) \\ &= 12 + 1.6 + j0.8 + (j0.2) \parallel (9.6 + j2.8) \\ &= 13.6 + j0.8 + \frac{j0.2(9.6 + j2.8)}{9.6 + j3} \\ &= 13.6 + j1 = 13.64 \angle 4.204^\circ \Omega \end{aligned}$$

The desired current is

$$I = \frac{V}{Z} = \frac{50 \angle 0^\circ}{13.64 \angle 4.204^\circ} = 3.666 \angle -4.204^\circ \text{ A}$$

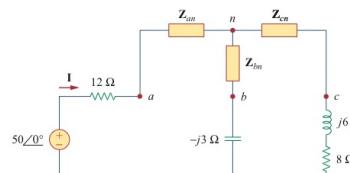


Figure 9.29
The circuit in Fig. 9.28 after delta-to-wye transformation.

