

# MARC TCHONA

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ELECTRICAL ENGINEERING JUNIOR

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## CONTACT

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## SKILLS

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Python (6 months)

MATLAB (6 months)

SolidWorks (6 months)

Ngspice, LTspice,

PSpice (3 years)

LVSIM (6 months)

LaTeX (6 months)

Arduino (2 years)

R (6 months)

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## PROFILE

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Junior Electrical Engineering student with hands-on experience in embedded systems, signal processing, and electronics design. Proven leadership in team projects and strong communication skills gained from diverse academic and cultural experiences. Committed to continuous learning and technical excellence.

## EDUCATION

**B.S. in Electrical Engineering** | South Dakota Mines

Expected Graduation: December 2025 | CCGPA: 3.358

- Dean's List, Spring 2022
- Relevant Coursework: Signals and Systems, Embedded Systems, Energy Systems, Circuit Design

## EXPERIENCE

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**Teacher Assistant** | South Dakota Mines | Aug 2024 – Present

- Supervised electronics labs for 30+ students, ensuring safety and efficiency.
- Debugged circuit wiring, increasing lab productivity.
- Graded coursework and provided personalized feedback to students

**Testing Engineer** | South Dakota Mines | Jun 2024 – Aug 2024

- Tested SPICE interface and gave constructive feedback for improvement
- Designed a responsive frontpage with HTML, CSS, and Photoshop on Electron.
- Designed digital to three phase signal converter circuit to improve safety in labs by ensuring reliable and controlled signal inputs.

