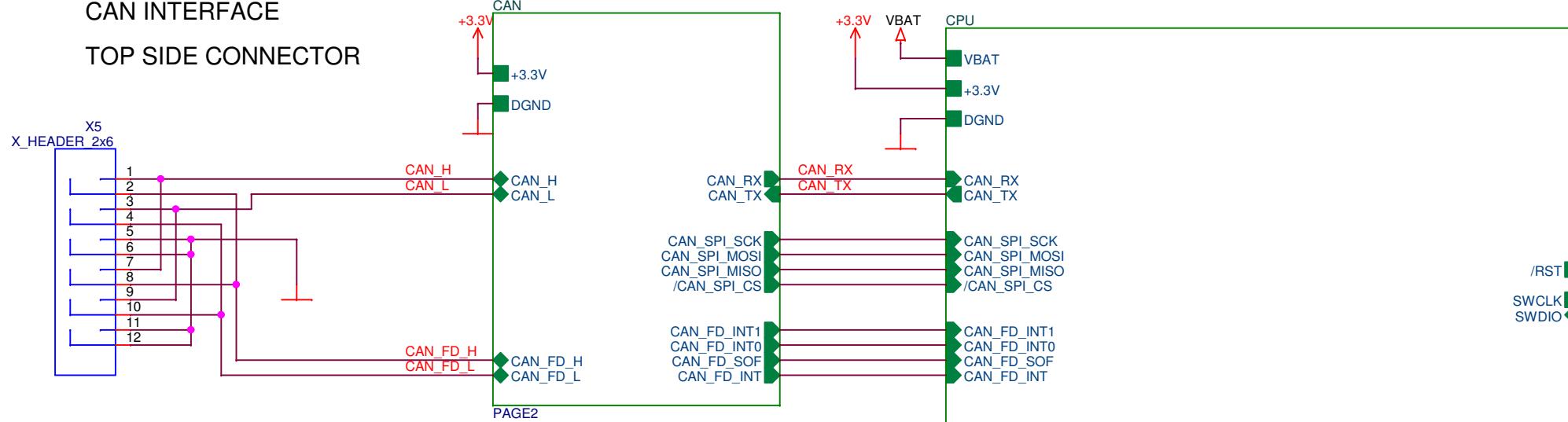


CAN INTERFACE

TOP SIDE CONNECTOR

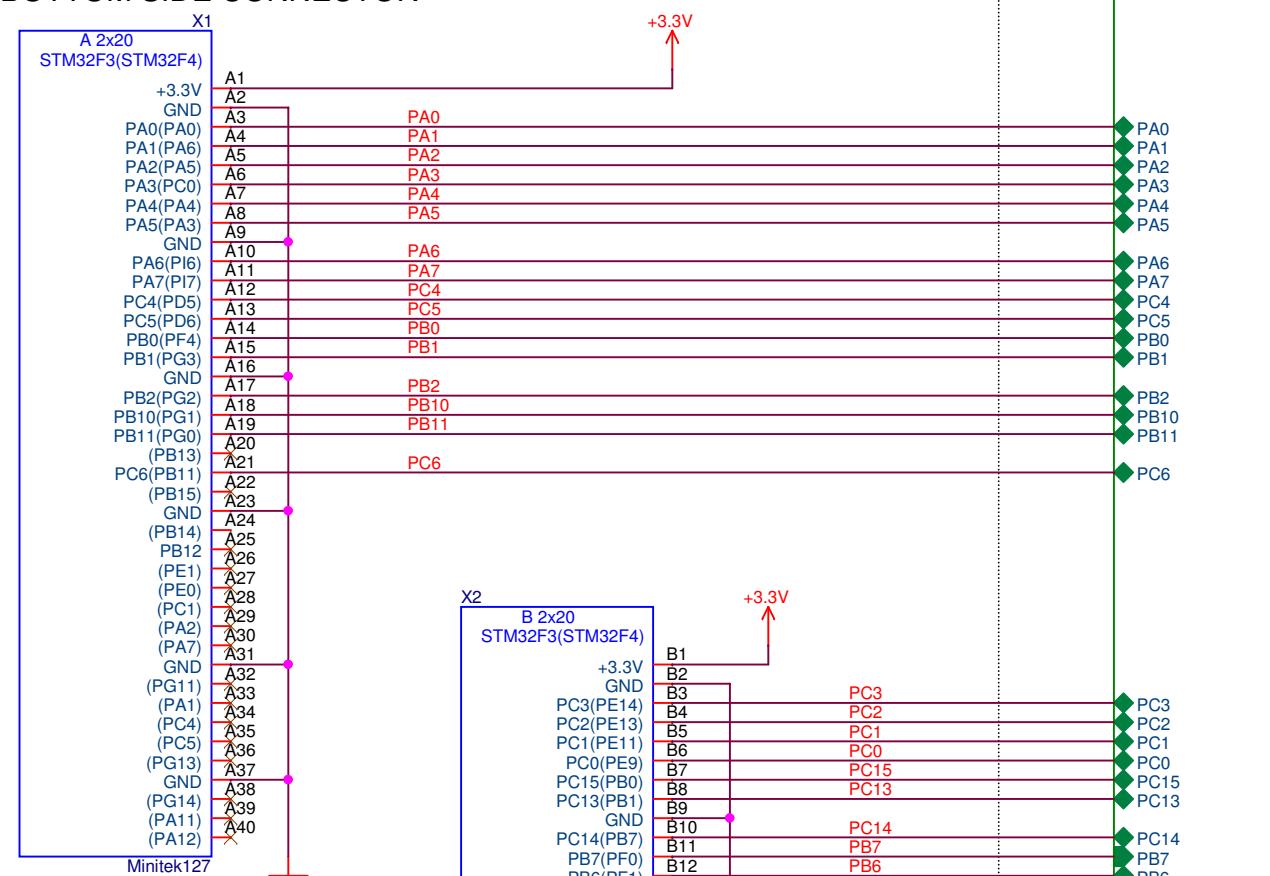


JTAG

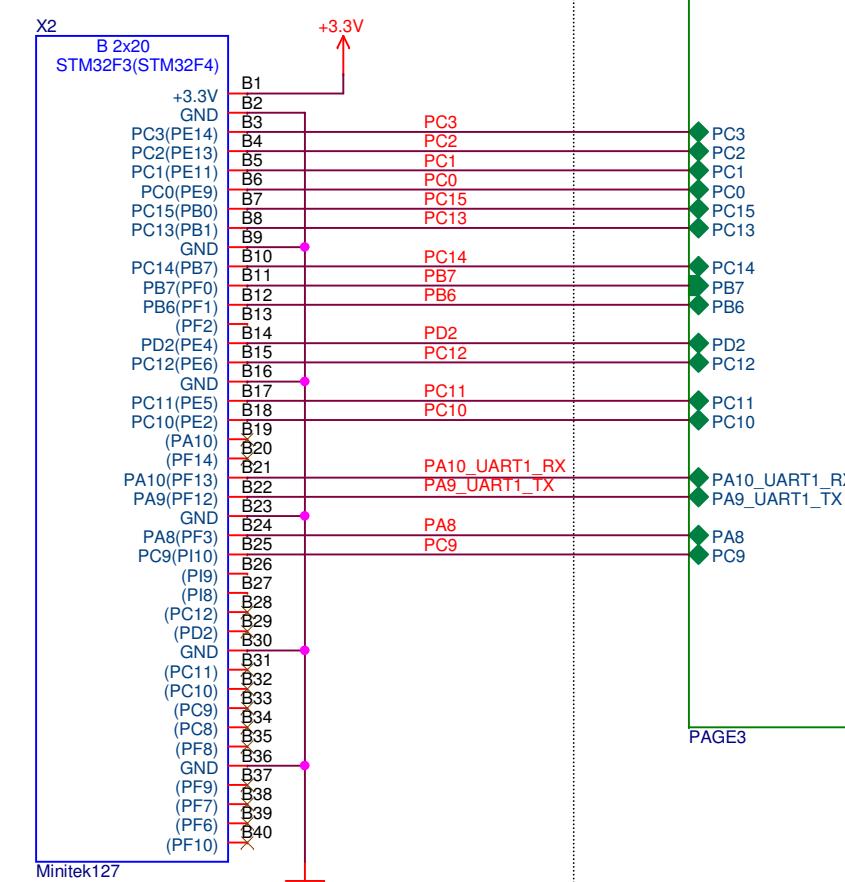
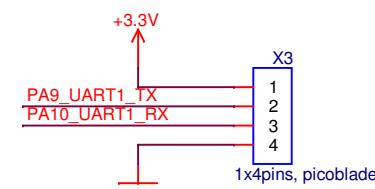


INTERFACE FOR FUNCTION BOARD

BOTTOM SIDE CONNECTOR



DEBUG UART



REF1
REF3

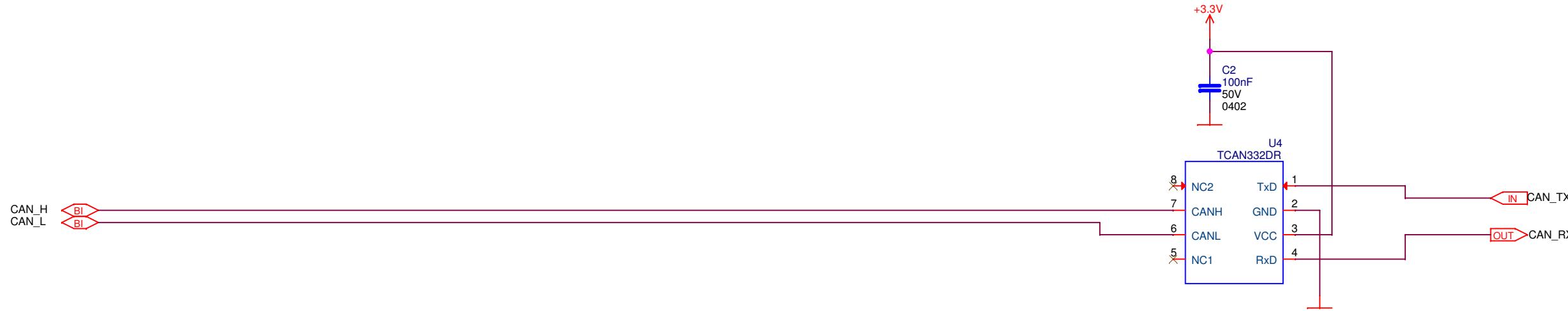
REF2
REF4

Rev no-Rev desc Sign Date
Rev E BRVPLI 20180425

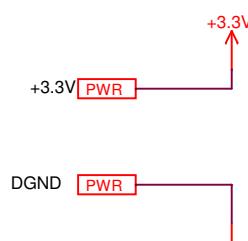
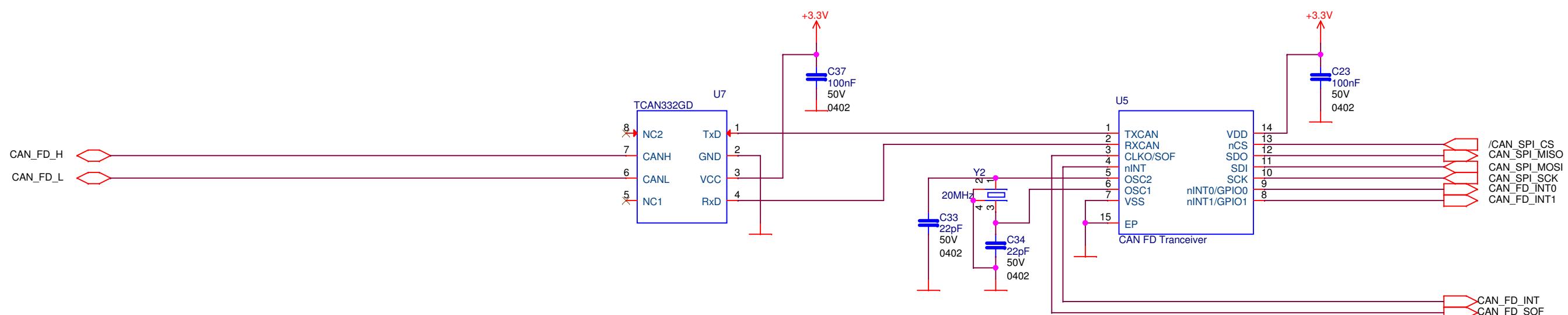
VOSS
Ingenjörsfirma AB
Br.Voss Ingenjörsfirma AB
Skaraborgsvägen 21
506 30 Borås
Tel. 033-10 84 00

Page Name	TOP	Variant Name
Design Name	SCHEMATIC	BOOT
Size	Document Number/Description	Designed
A3	CPU-NOD F3	BRV/
Date	Thursday, April 26, 2018	Checked
		Rev
		E

CAN INTERFACE



CAN FD INTERFACE



Rev no-Rev desc	Sign	Date
Rev E	BRV/PLI	20180425

