

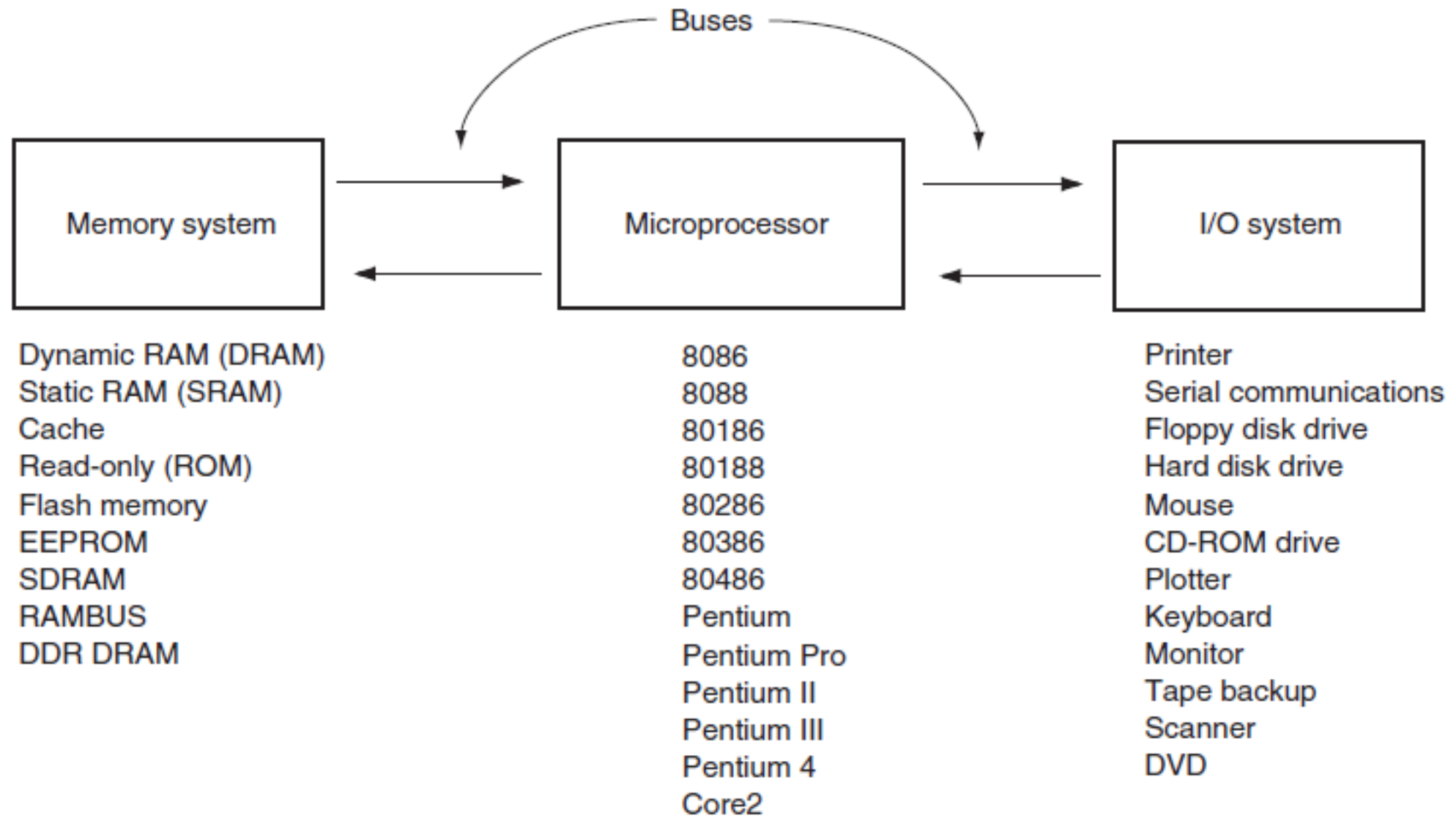
Chapter 1 Introduction to Microprocessor and the Computer

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Slides are mainly based on The Intel Microprocessors by Barry B. Brey,
2008

The Microprocessor based Computer System

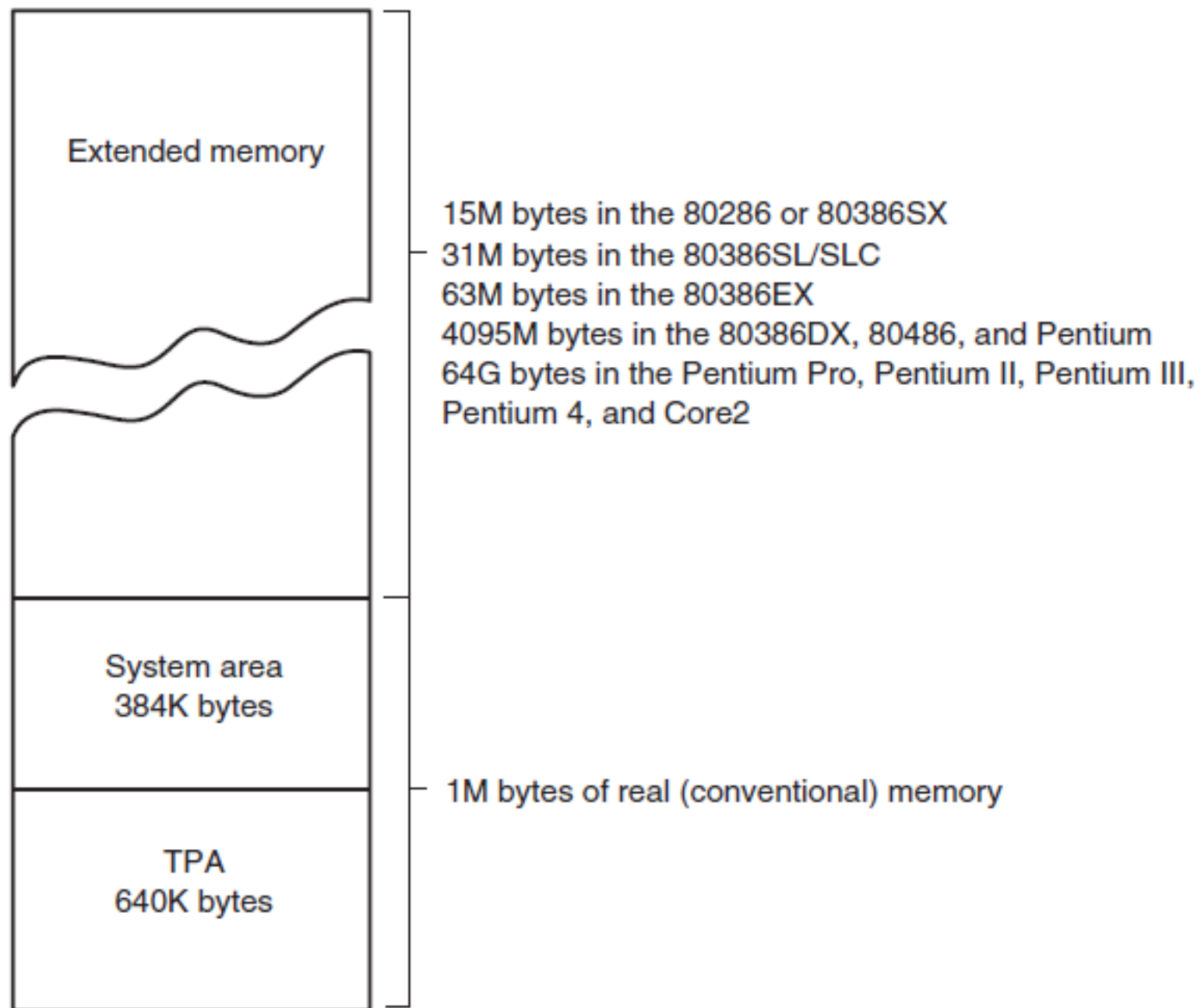
- Computer systems have undergone many changes recently. Machines that once filled large areas have been reduced to small desktop computer system because of the microprocessor.
- Here we are going to examine the structure of the MP-based personal computer system.



- Above is the diagram of the personal computer. There are three blocks connected by buses. A bus is a set of common connections that carry the same type of information.
- For instance, the address bus contains 20 or more connections, conveys the memory address to the memory.

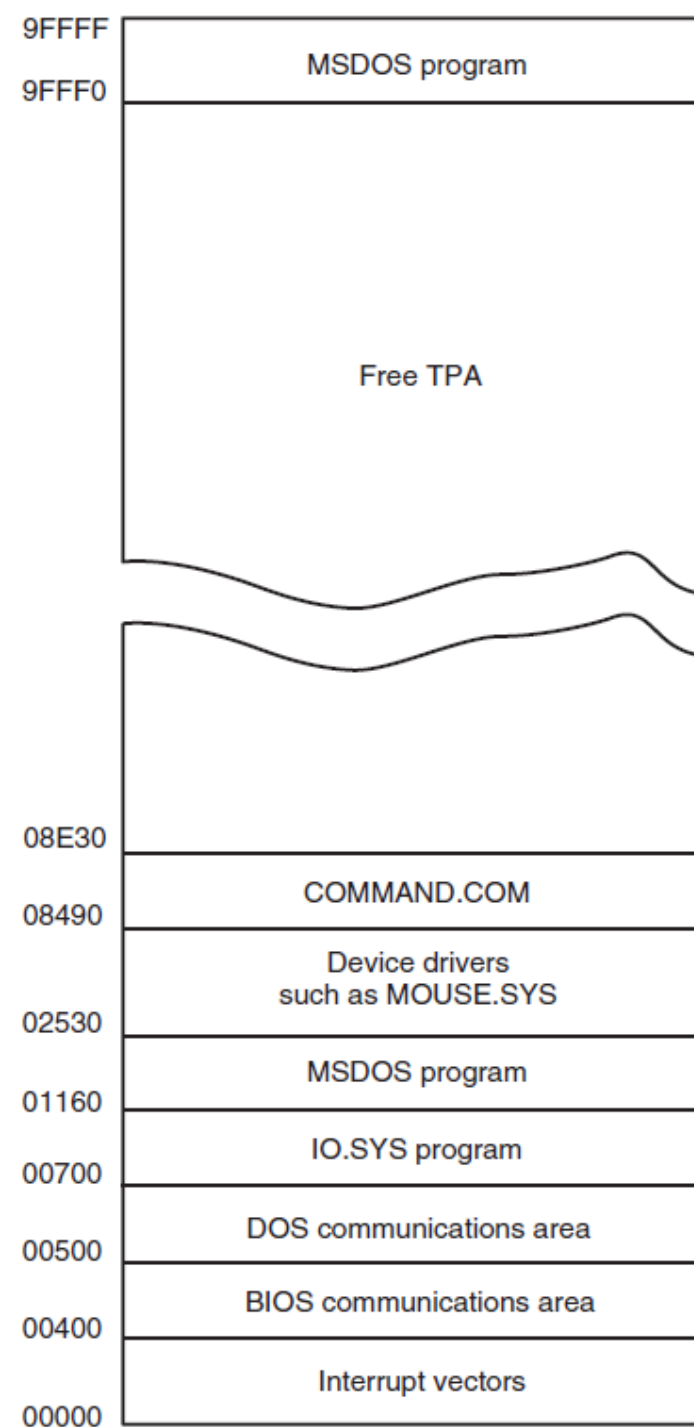
The Memory and the I/O System

- The memory system is divided into three main parts.
 - TPA (Transient Program Area)
 - System Area
 - XMS (Extended Memory System)



TPA (Transient Program Area)

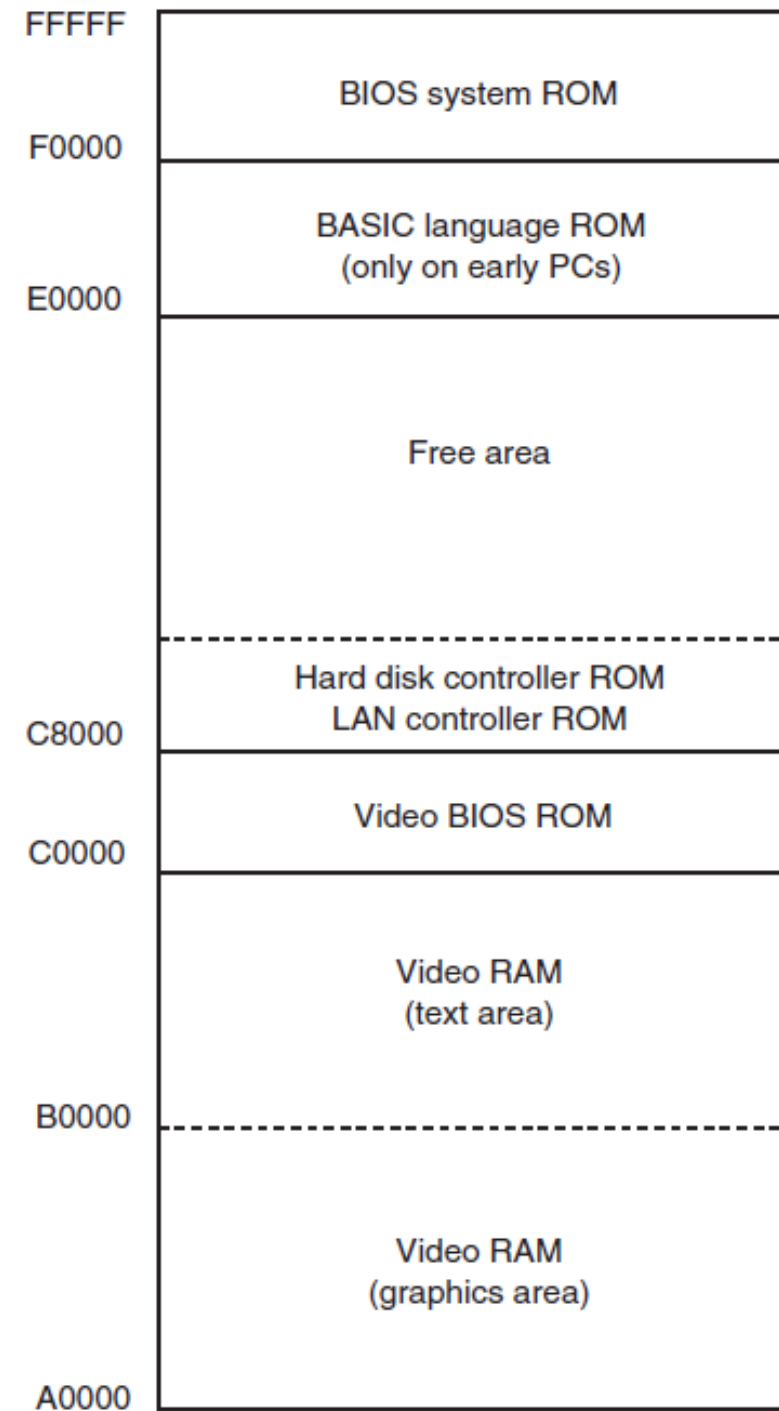
- TPA holds the DOS (Disk Operating System) and other programs that control the computer system. This is a DOS concept not really applicable to Windows.
- TPA also stores active and inactive DOS application programs.



- Note the hexadecimal memory address or memory locations. These are used to number each byte of the memory system.
 - 1234H or 0x1234
- The interrupt vectors access various features of DOS, BIOS and applications.
- The IO.SYS is the program that allows DOS to use the keyboard, video display, printers.
- Drivers are programs that control installable I/O devices such as mouse, disk or CDROM.
- Command.com controls the operation of the computer from the keyboard when operated in DOS mode.
- Never erase the command.com, IO.sys or msdos.sys to make room for other software or your computer will not function.

The System Area

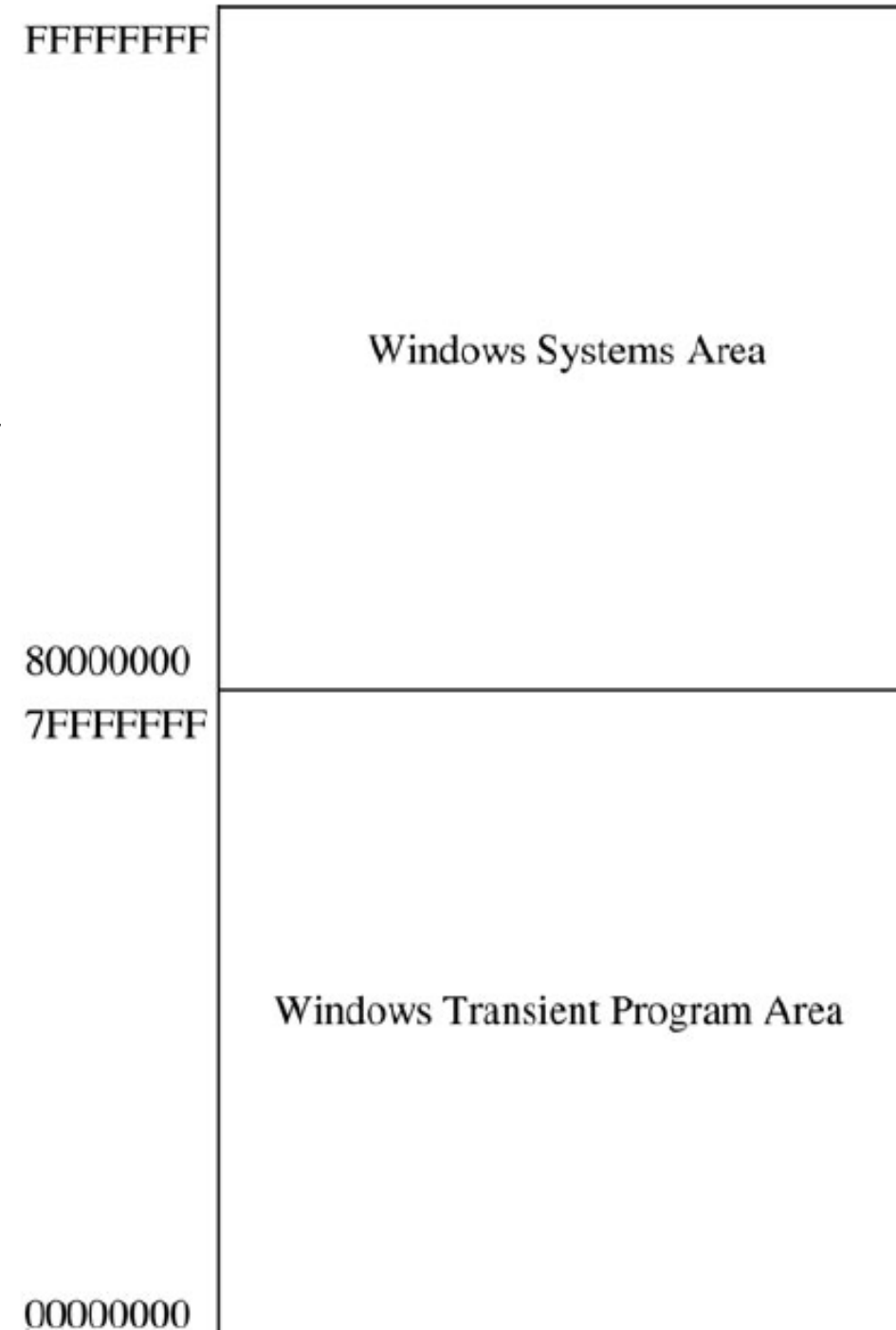
- The system area contains programs on either a read only memory (ROM) or flash memory and arease of read/write (RAM) memory for data storage.
- The first area of the system space contains video display RAM and video control programs on ROM or flash memory. This area starts at location A0000H and extends to location C7FFFH.
- The size and amount of memory used depends on the type of video display adapter attached to the system.



- The area at locations C8000H–DFFFFH is often open or free. This area is used for the expanded memory system (EMS) in a PC.
- Memory locations E0000H–EFFFFH contain the cassette BASIC language on ROM found in early IBM personal computer systems. This area is often open or free in newer computer systems.
- Finally, the system BIOS ROM is located in the top 64K bytes of the system area (F0000H–FFFFFH). This ROM controls the operation of the basic I/O devices connected to the computer system. It does not control the operation of the video system, which has its own BIOS ROM at location C0000H. The first part of the system BIOS (F0000H–F7FFFH) often contains programs that set up the computer; the second part contains procedures that control the basic I/O system.

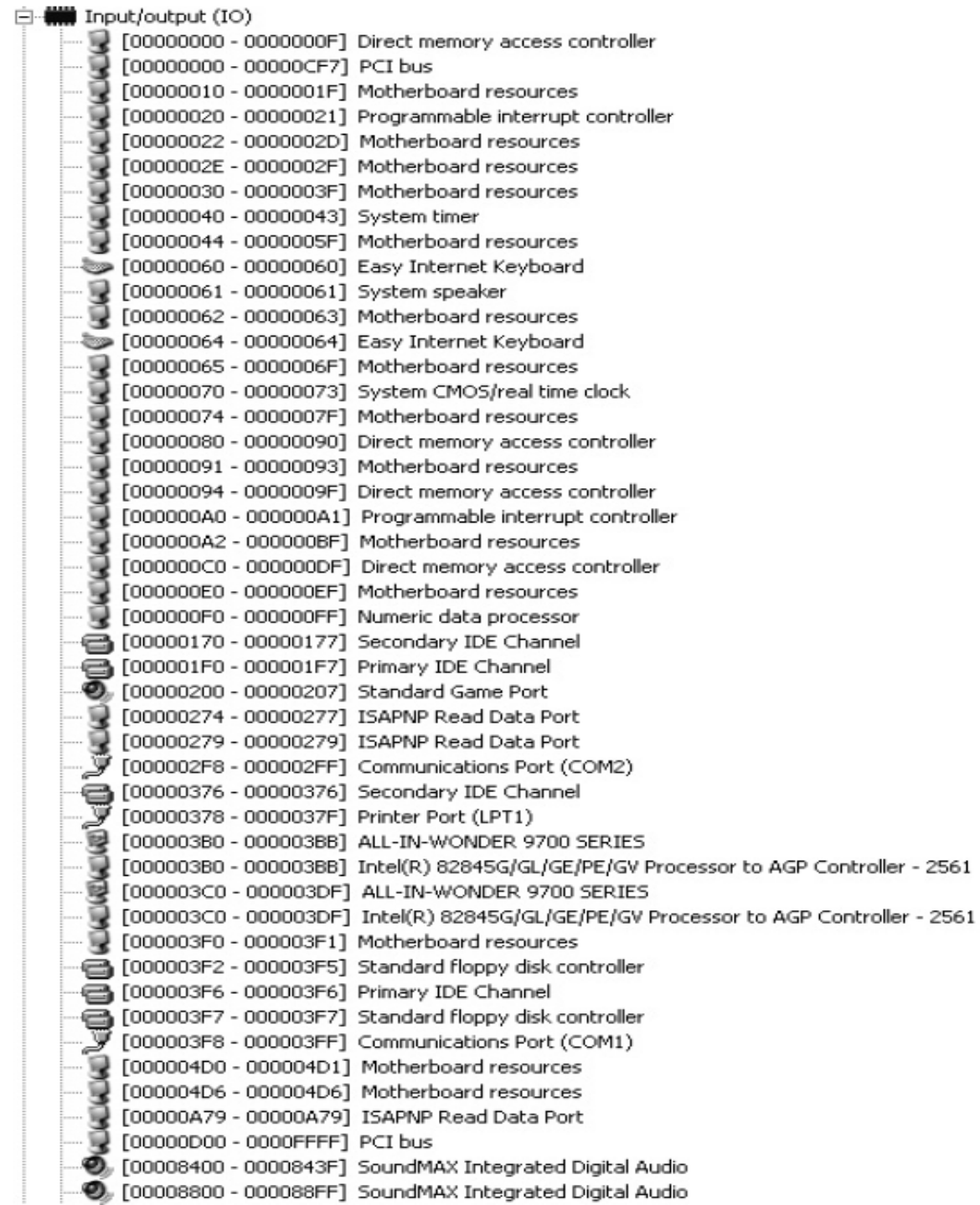
Windows Systems

- Note that the Tpa starts with memory address 00000000H does not mean that any program written for Windows will begin at this physical address.
- The processes use page tables. The tables define where in the physical memory each 4K-byte page of the process is located.
- In this way, applications can use up to 2G-byte memory even the computer has less memory.
- The system (OS) uses the harddisk drive in case the physical memory is not sufficient. (Virtual memory)



I/O Space

- The I/O space extends from I/O port 0000H to port FFFFH. An I/O port address is similar to a memory address, but instead of addressing memory, it addresses an I/O device.
 - You can check the Device Manager to see the list of I/O devices connected to your computer.



The screenshot displays the 'Input/output (IO)' tab in Windows Device Manager, showing a list of hardware components and their assigned I/O address ranges. The list includes various system components like the Direct memory access controller, PCI bus, Motherboard resources, Programmable interrupt controller, System timer, Easy Internet Keyboard, System speaker, System CMOS/real time clock, Numeric data processor, Secondary IDE Channel, Primary IDE Channel, Standard Game Port, ISAPNP Read Data Port, Communications Port (COM2), Printer Port (LPT1), ALL-IN-WONDER 9700 SERIES, Intel(R) 82845G/GL/GE/PE/GV Processor to AGP Controller - 2561, Standard floppy disk controller, and SoundMAX Integrated Digital Audio.

Address Range	Device Name
[00000000 - 0000000F]	Direct memory access controller
[00000000 - 00000CF7]	PCI bus
[00000010 - 0000001F]	Motherboard resources
[00000020 - 00000021]	Programmable interrupt controller
[00000022 - 0000002D]	Motherboard resources
[0000002E - 0000002F]	Motherboard resources
[00000030 - 0000003F]	Motherboard resources
[00000040 - 00000043]	System timer
[00000044 - 0000005F]	Motherboard resources
[00000060 - 00000060]	Easy Internet Keyboard
[00000061 - 00000061]	System speaker
[00000062 - 00000063]	Motherboard resources
[00000064 - 00000064]	Easy Internet Keyboard
[00000065 - 0000006F]	Motherboard resources
[00000070 - 00000073]	System CMOS/real time clock
[00000074 - 0000007F]	Motherboard resources
[00000080 - 00000090]	Direct memory access controller
[00000091 - 00000093]	Motherboard resources
[00000094 - 0000009F]	Direct memory access controller
[000000A0 - 000000A1]	Programmable interrupt controller
[000000A2 - 000000BF]	Motherboard resources
[000000C0 - 000000DF]	Direct memory access controller
[000000E0 - 000000EF]	Motherboard resources
[000000F0 - 000000FF]	Numeric data processor
[00000170 - 00000177]	Secondary IDE Channel
[000001F0 - 000001F7]	Primary IDE Channel
[00000200 - 00000207]	Standard Game Port
[00000274 - 00000277]	ISAPNP Read Data Port
[00000279 - 00000279]	ISAPNP Read Data Port
[000002F8 - 000002FF]	Communications Port (COM2)
[00000376 - 00000376]	Secondary IDE Channel
[00000378 - 0000037F]	Printer Port (LPT1)
[000003B0 - 000003BB]	ALL-IN-WONDER 9700 SERIES
[000003B0 - 000003BB]	Intel(R) 82845G/GL/GE/PE/GV Processor to AGP Controller - 2561
[000003C0 - 000003DF]	ALL-IN-WONDER 9700 SERIES
[000003C0 - 000003DF]	Intel(R) 82845G/GL/GE/PE/GV Processor to AGP Controller - 2561
[000003F0 - 000003F1]	Motherboard resources
[000003F2 - 000003F5]	Standard floppy disk controller
[000003F6 - 000003F6]	Primary IDE Channel
[000003F7 - 000003F7]	Standard floppy disk controller
[000003F8 - 000003FF]	Communications Port (COM1)
[000004D0 - 000004D1]	Motherboard resources
[000004D6 - 000004D6]	Motherboard resources
[00000A79 - 00000A79]	ISAPNP Read Data Port
[00000D00 - 0000FFFF]	PCI bus
[00008400 - 0000843F]	SoundMAX Integrated Digital Audio
[00008800 - 000088FF]	SoundMAX Integrated Digital Audio

- Many I/O devices are not directly addressed. Instead, the system BIOS ROM addresses these basic devices which can vary in location and function from one computer to another.
- Access to most I/O devices should always be made through Windows, DOS or BIOS function calls to maintain compatibility from one computer system to another.

The Microprocessor

- The MP is the controlling element in a computer system. It is also referred to as CPU (Central Processing Unit). It controls memory and I/O through a series of connections called buses.
- The buses select an I/O or memory device, transfer data between the I/O device or memory and the MP control the I/O and memory system.
 - Memory and I/O are controlled through instructions that are stored in the memory and executed by the MP.

- The MP performs three main tasks:
 1. Data transfer between itself and the memory or I/O system.
 2. Simple arithmetic and logic instructions
 3. Program flow via simple decisions.
- The power of MP is its capability to execute billions of millions (MIPS, FLOPS) of instructions per second from a program (software, a group of instructions) stored in the memory system.

Arithmetic and Logic Operations

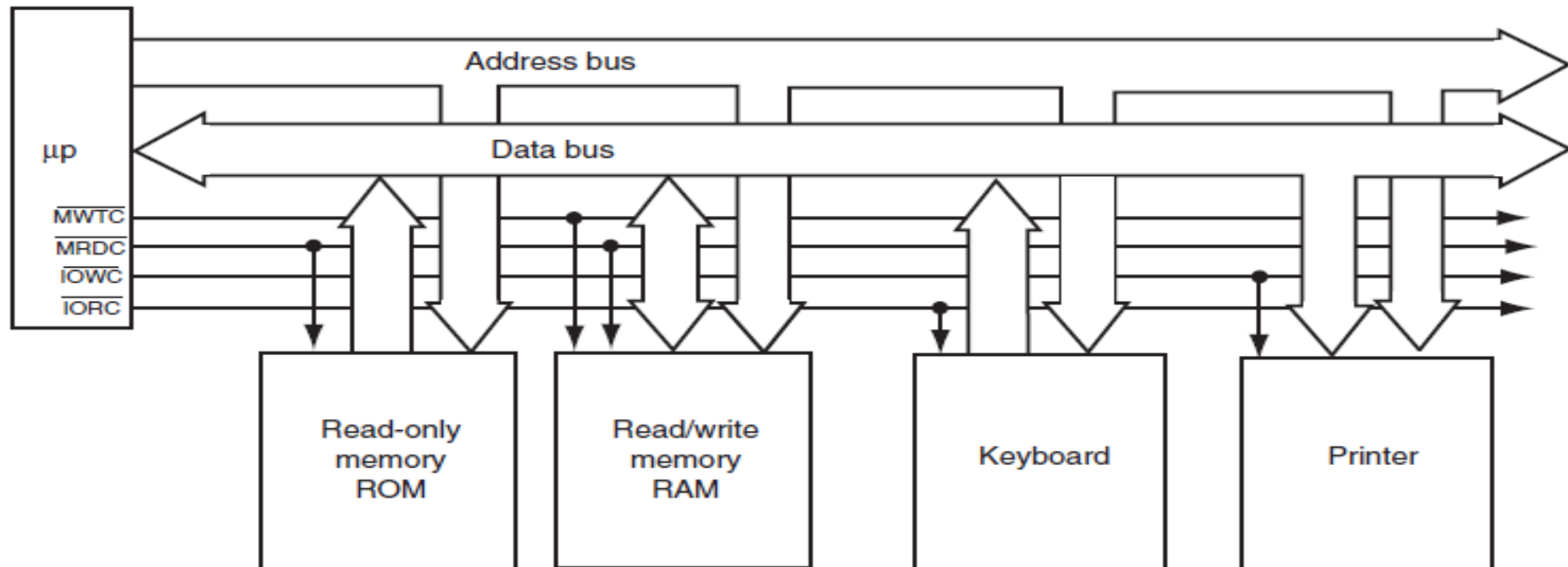
- These operations are very basic but using them, very complex problems are solved. Data are operated upon from the memory system or internal registers.
 - Addition, subtraction, multiplication, division, AND, OR, NOT, NEG, shift, rotate
- Data widths may vary:
 - Byte: 8 bits
 - Word: 16 bits
 - Double Word: 32 bits

Decisions

- Simple decisions are used to control the program flow which allows making decisions through numeric facts.
 - Zero (Test a number for zero or not zero)
 - Sign (Test a number for positive or negative)
 - Carry (Test for carry after addition or borrow after subtraction)
 - Parity (Test a number for an even or odd number of ones)
 - Overflow (Test for overflow which indicates an invalid result after signed addition or subtraction)

Buses

- A bus is a common group of wires that interconnect components in a computer system. They transfer address, data or control information between the MP and its memory and I/O systems.



- When addressing I/O, the 16 bit I/O address can select one of the 64 I/O devices from 0000H to FFFFH.
- On the other hand, when addressing memory, 20 bit addresses are used from 00000F to FFFFFH in order to address 1Mbyte of memory.
- The data bus transfers information between MP and its memory and I/O system. Data transfer vary in size from 8bits wide to 64 bits wide. 8086 can transfer 16 bits of data through their buses, Core2 can transfer 64 bits.

- The control bus contains lines that select the memory or I/O and cause them to perform a read or write operation. \overline{MRDC} , \overline{MWTC} , \overline{IORC} , \overline{IOWC} .
- Note that the overbar indicates that the control signal is *active low*; that is it is active when a logic zero appears on the control line.
 - For example, if $\overline{IOWC} = 0$, the MP is writing data from the data bus to an I/O device whose address appears on the address bus.

Number Systems

- Digits:
 - Base 10 :0-9 (decimal)
 - Base 2: 0-1 (binary)
 - Base 8: 0-7 (octal)
 - Base 16: 0-15 (hexadecimal) A=10, B=11, C=12, D=13, E=14, F=15
- Positional Notation
 - Positions to the left of the radix (number base) point -> positive power
 - Positions to the right of the radix (number base) point -> negative power

- Ex 1. Conversion from binary to decimal

Power	2^2	2^1	2^0	2^{-1}	2^{-2}	2^{-3}							
Weight	4	2	1	.5	.25	.125							
Number	1	1	0	.	1	0	1						
Numeric Value	4	+	2	+	0	+	.5	+	0	+	.125	=	6.625

- Ex 2. Conversion from base 6 to decimal

Power	6^1	6^0	6^{-1}				
Weight	6	1	.167				
Number	2	5	.2				
Numeric Value	12	+	5	+	.333	=	17.333

- Ex 3. Conversion from hexadecimal to decimal

Power	16^1	16^0	16^{-1}	
Weight	16	1	.0625	
Number	6	A	. C	
Number Value	$96 + 10 + .75 = 106.75$			

- Ex 4. Conversion from decimal (whole number)

2) <u>10</u>	remainder = 0	
2) <u>5</u>	remainder = 1	
2) <u>2</u>	remainder = 0	
2) <u>1</u>	remainder = 1	result = 1010
0		

- Ex 5. Conversion from decimal to octal

8) <u>10</u>	remainder = 2	
8) <u>1</u>	remainder = 1	result = 12
0		

- Ex 6. Conversion from decimal to hexadecimal

16) <u>109</u>	remainder = 13 (D)	
16) <u>6</u>	remainder = 6	result = 6D
0		

Converting from a decimal fraction

- Algorithm

1. Multiply the decimal fraction by the radix (base).
2. Save the whole number portion of the result as a digit (event it is a zero).
Note that the first result is immediately to the right of the radix point.
3. Repeat steps 1 and 2, us,ing the fractional part of step 2 until the fractional part of step 2 is zero.

- Ex 7. Conversion to binary

$$0.125 = X_2$$

$$0.125 * 2 = 0.25 \text{ digit is } 0$$

$$0.25 * 2 = 0.5 \text{ digit is } 0$$

$$0.5 * 2 = 1.0 \text{ digit is } 1$$

Result is 0.001_2

- Ex 8. Conversion to octal

$$0.125 = X_8$$

$$0.125 * 8 = 1.0 \text{ digit is } 1$$

Result is 0.1_8

- Ex 8. Conversion to hexadecimal

$$0.46875 = X_{16}$$

$$0.46875 * 16 = 0.75 \text{ digit is } 0$$

$$0.75 * 16 = 12.0 \text{ digit is } C$$

Result is $0.0C_{16}$

Binary Coded Hexadecimal

- BCH is used to represent hexadecimal data in binary code.
- Ex 10. 2AC = 0010 1010 1100
- Ex 11. 1000 0011 1101 . 1110 = 83D.E

<i>Hexadecimal Digit</i>	<i>BCH Code</i>
0	0000
1	0001
2	0010
3	0011
4	0100
5	0101
6	0110
7	0111
8	1000
9	1001
A	1010
B	1011
C	1100
D	1101
E	1110
F	1111

Complements

- Sometimes data are stored in complement form to represent negative numbers. There are two systems to represent negative data:
 - Radix complement
 - Radix-1 complement
- Ex 12. Represent 8 bit binary number 01001100 in one's complement to show it as a negative value.

	1		1		1		1		1		1		1		1		1
—	0		1		0		0		1		1		0		0		0
	1		0		1		1		0		0		1		1		1

- Ex 13. 5CD in radix-1 complement

15	15	15
5	C	D
<hr/>		
A	3	2

- Radix-1 complement is not used by itself; it is used as a step for finding radix complement which is used to represent negative numbers in modern computer systems.
- To find radix complement, first find radix-1 complement then add 1 to the result.
- Note that the problem with radix-1 complement is that a negative or positive zero exists; in the radix complement system only a positive zero can exist.

• Ex 14.

	1		1		1		1		1		1		1		1		1		1		
—	0		1		0		0		1		0		0		0		0		0		
	1		0		1		1		0		1		1		1		1		1		1's complement
+																	1				
	1		0		1		1		1		0		0		0		0		0		2's complement

• Ex 15.

	15		15		15		
—	3		4		5		15's complement
	C		B		A		
+					1		16's complement
	C		B		B		

- To prove that 01001000 is the inverse (negative) of a 10111000, add the two together to form an 8-digit result. The ninth digit is dropped and the result is zero.

Computer Data Formats

- Successful programming requires precise understanding of data formats.

ASCII and Unicode Data

- 7 bit code with the eighth and most significant bit used to hold parity (in old systems).
- If used with a printer, most significant 0 for alphanumeric printing, 1 for graphics printing.
- Since Windows, Unicode is used to store alphanumeric data (each character is 16 bit).

BCD (Binary Coded Decimal) Data

- BCD information is stored in either packed or unpacked forms. Packed BCD data are stored as two digits per byte and unpacked BCD data are stored as one digit per byte.
- The range of a BCD digit extends from 0000_2 to 1001_2 or 0-9 for decimal.
 - Unpacked BCD data are returned from a keypad or keyboard.
 - Packed BCD data are used for some of the instructions included for BCD addition and subtraction in the instruction set of the MP.

Decimal	Packed	Unpacked
12	0001 0010	0000 0001 0000 0010
623	0000 0110 0010 0011	0000 0110 0000 0010 0000 0011

- Ex 16.

;Unpacked BCD data (least-significant data first)

NUMB1 DB 3,4,5 ; defines number 543

;packed BCD

NUMB2 DB 3,45H; defines number 4503

;DB is define bytes

- Byte sized data
 - Byte-sized data are stored as unsigned and signed integers.

128	64	32	16	8	4	2	1

Unsigned byte

-128	64	32	16	8	4	2	1

Signed byte

- Whenever a number is two's complement, its sign changes from negative to positive or positive to negative.

+8	0	0	0	0	1	0	0	0	
-	1	1	1	1	0	1	1	1	1's complement
+								1	
-8	1	1	1	1	1	0	0	0	2's complement