

### EXAMPLE 1

**Description:** A simple sequence of five “add” instructions with no hazards.

```
ADD r1,r1,r2
ADD r3,r0,r2
ADD r4,r0,r2
ADD r5,r0,r2
ADD r6,r0,r2
```

### EXAMPLE 2

**Description:** A simple sequence of three “add” instructions with PIPE EXE data hazard.

```
ADD r2,r1,r3
ADD r3,r2,r1 //EXE HAZARD
ADD r1,r3,r2 //EXE HAZARD
```

### EXAMPLE 3

**Description:** A simple sequence of three “add” instructions with PIPE MEM data hazard.

```
ADD r2,r1,r3
ADD r4,r4,r1
ADD r3,r2,r2 // MEM HAZARD
```

### EXAMPLE 4

**Description:** A simple sequence of four instructions with no data hazard.

```
LW r1,0(r4)
LW r2,4(r4)
ADD r3,r1,r2
SW r3, 8(r4)
```