

## ÇANKAYA ÜNİVERŞİTESİ

Department of Computer Engineering

#### EXAM PROCTOR AND CLASS ASSIGNMENT SYSTEM

CENG408 - Innovative System Design and Development II

Students
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Advisor
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#### **EXAM PROCTOR AND CLASS ASSIGNMENT**

- Exam Proctor and Class Assignment System is developed to schedule exams with less effort and hence increase people's productivity.
- Main Features
- Automatic distribution of exams
- Get teacher's requests for exams
- Provide minimum overlap on student's exam calendar

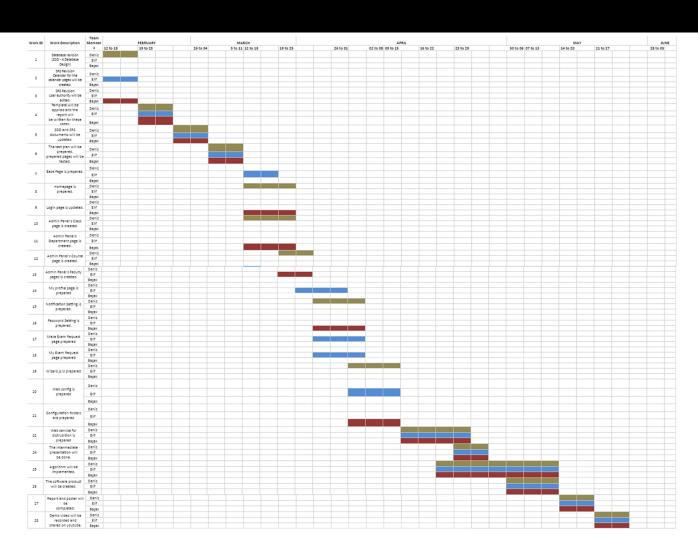


# MAIN CONTRIBUTION Pros and cons to similar projects

Systems Properties	Assigning Proctors to Exams with Scatter Search	Prototype System Development for Examination Proctor Assignment Problem Using Mixed Integer Programing	Exam Proctor and Class Assignment System		
Who has the authority to use the system	Only proctors	Only proctors  Student, teacher assistant, departm chair, dean, rector admin			
Acceptance of teacher's examination requests	It does not take teacher's request.	It does not take teacher's request.	r's It takes teacher's request; class type and number of assistant.		
Homogeneous assistant distribution in terms of supervising hours.	It provides homogeneous distribution.	It does not control.	It provides in a efficient manner.		
GUI	There is no information that GUI is designed in a user friendly manner.	There is no information that GUI is designed in a user friendly manner.	The system's GUI is designed in a user-friendly manner.		
Distributing exams for classes and their types	It does not provide	It does not provide	It provides		



#### **WORK PLAN**





### **SUCCESS CRITERION**

Success Criterion	How to Measure		
Successfully exam distribution with minimum conflicits	By looking at the results		
Assistant's participations hours are divided equally	By looking at the results		
Number and type of classes are distributed homogenously	By looking at the results		
Personal exam calendar can be viewed	Comparing viewed calendars with procedures' results		



#### **METHODS AND TECHNOLOGIES**

.NET, JSON, HTML, CSS, JS, AJAX, C# MSSQL – Stored Procedures

Windows Service

Web Service - SMS

Coloring Algorithms Heuristic Algorithms



# POTENTIAL RISKS (Predicted Risks)

Risks	Probability	Impact	Mitigation Plan
SCHEDULE: If phases of project will take more time or be late than planned, the testing will be delayed	High	High	Beforehand, team members are able to control to task and communicate with each other.
DEFECTS: Defects can be found at the end of each iteration.(life cycle of project)	Medium	Low	Due to our development methodology, expected defects can be handled as we planned
SCOPE: Scope is defined in our documentation	Medium	Medium	For review all over the project duration makes an ordered list.
Delayed Test Depending on New Issues	Medium	Medium	If new defects are discovered, the defect management procedure is in place to provide a resolution



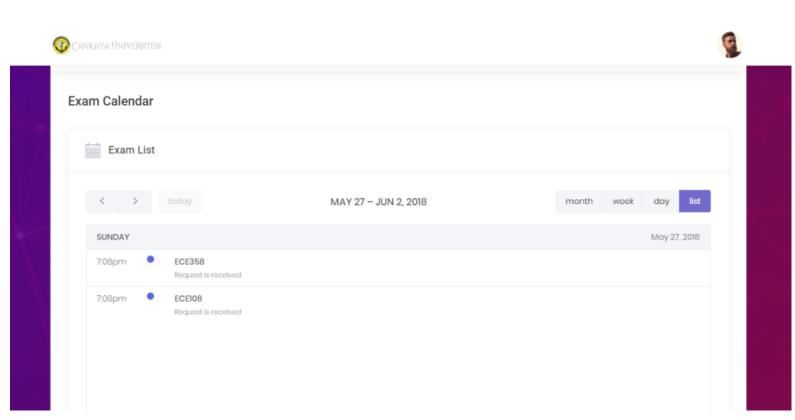
### **OBTAINED RESULTS**

В	F	G	H	I I	J	K	Q	T	U
Bölüm Adı	Ders Adı	nci Sinif (	Sınav Süresi	Sınav Başlama Zamanı	Sınav Bitiş Zamanı	Sınav Yeri	Sınava Giren Öğrenci Sayıs	Asistant Adı	Asistant Soyadı
lectrical and Electronics Engineering	EE154	1	90 dk	2018-05-08 10:30:00.000	2018-05-08 12:00:00.000	NA02	112	Ali	Dursun
lectrical and Electronics Engineering	EE154	1	90 dk	2018-05-08 10:30:00.000	2018-05-08 12:00:00.000	NA02	112	İlyas Muzaffer	GÜLBAY
Electrical and Electronics Engineering	EE200	2	90 dk	2018-05-08 12:05:00.000	2018-05-08 13:35:00.000	LB01	1	İlyas Muzaffer	GÜLBAY
lectrical and Electronics Engineering	EE202	2	90 dk	2018-05-09 10:30:00.000	2018-05-09 12:00:00.000	HA04	80	Mehtap	YORULMAZ
lectrical and Electronics Engineering	EE202	2	90 dk	2018-05-09 10:30:00.000	2018-05-09 12:00:00.000	HA04	80	Ali	Dursun
lectrical and Electronics Engineering	EE206	2	90 dk	2018-05-10 10:30:00.000	2018-05-10 12:00:00.000	H129 (Amfi-5)	81	Serkan	ZOR
lectrical and Electronics Engineering	EE206	2	90 dk	2018-05-10 10:30:00.000	2018-05-10 12:00:00.000	H129 (Amfi-5)	81	Mehtap	YORULMAZ
Electrical and Electronics Engineering	EE208	2	90 dk	2018-05-11 10:30:00.000	2018-05-11 12:00:00.000	NA01	98	Neşe	MEVSIM
lectrical and Electronics Engineering	EE208	2	90 dk	2018-05-11 10:30:00.000	2018-05-11 12:00:00.000	NA01	98	Serkan	ZOR
lectrical and Electronics Engineering	EE210	2	60 dk	2018-05-12 10:30:00.000	2018-05-12 11:30:00.000	H129 (Amfi-5)	96	İlyas Muzaffer	GÜLBAY
lectrical and Electronics Engineering	EE210	2	60 dk	2018-05-12 10:30:00.000	2018-05-12 11:30:00.000	H129 (Amfi-5)	96	Neşe	MEVSİM
lectrical and Electronics Engineering	EE214	2	60 dk	2018-05-13 10:30:00.000	2018-05-13 11:30:00.000	MA02	69	Mehtap	YORULMAZ
lectrical and Electronics Engineering	EE214	2	60 dk	2018-05-13 10:30:00.000	2018-05-13 11:30:00.000	MA02	69	Ali	Dursun
lectrical and Electronics Engineering	EE304	3	60 dk	2018-05-08 13:40:00.000	2018-05-08 14:40:00.000	LB05	49	Neşe	MEVSİM
lectrical and Electronics Engineering	EE304	3	60 dk	2018-05-08 13:40:00.000	2018-05-08 14:40:00.000	LB05	49	Serkan	ZOR
lectrical and Electronics Engineering	EE310	3	60 dk	2018-05-09 12:05:00.000	2018-05-09 13:05:00.000	H130 (Amfi-6)	44	Ali	Dursun
lectrical and Electronics Engineering	EE310	3	60 dk	2018-05-09 12:05:00.000	2018-05-09 13:05:00.000	H130 (Amfi-6)	44	İlyas Muzaffer	GÜLBAY
lectrical and Electronics Engineering	EE322	3	60 dk	2018-05-10 12:05:00.000	2018-05-10 13:05:00.000	LB05	50	Serkan	ZOR
lectrical and Electronics Engineering	EE322	3	60 dk	2018-05-10 12:05:00.000	2018-05-10 13:05:00.000	LB05	50	Mehtap	YORULMAZ
lectrical and Electronics Engineering	EE324	3	60 dk	2018-05-11 12:05:00.000	2018-05-11 13:05:00.000	LB05	49	İlyas Muzaffer	GÜLBAY
lectrical and Electronics Engineering	EE324	3	60 dk	2018-05-11 12:05:00.000	2018-05-11 13:05:00.000	LB05	49	Neşe	MEVSİM
lectrical and Electronics Engineering	EE402	4	60 dk	2018-05-08 14:45:00.000	2018-05-08 15:45:00.000	LB01	7	Ali	Dursun
lectrical and Electronics Engineering	EE408	4	60 dk	2018-05-09 13:10:00.000	2018-05-09 14:10:00.000	LB01	6	Mehtap	YORULMAZ
lectrical and Electronics Engineering	EE456	4	60 dk	2018-05-10 13:10:00.000	2018-05-10 14:10:00.000	NA01	88	Neşe	MEVSİM
lectrical and Electronics Engineering	EE456	4	60 dk	2018-05-10 13:10:00.000	2018-05-10 14:10:00.000	NA01	88	Serkan	ZOR
lectrical and Electronics Engineering	EE458	4	60 dk	2018-05-11 13:10:00.000	2018-05-11 14:10:00.000	LB01	13	İlyas Muzaffer	GÜLBAY
lectrical and Electronics Engineering	EE459	4	60 dk	2018-05-12 11:35:00.000	2018-05-12 12:35:00.000	LA14	43	Ali	Dursun
lectrical and Electronics Engineering	EE459	4	60 dk	2018-05-12 11:35:00.000	2018-05-12 12:35:00.000	LA14	43	Mehtap	YORULMAZ
lectrical and Electronics Engineering	EE462	4	60 dk	2018-05-13 11:35:00.000	2018-05-13 12:35:00.000	MB03	32	Serkan	ZOR
Industrial Engineering	IE101	1	90 dk	2018-05-01 12:35:00.000	2018-05-01 14:05:00.000	HA04	78	Özgür	BULUT
Industrial Engineering	IE200	2	100 dk	2018-05-04 12:05:00.000	2018-05-04 13:45:00.000	MB04	2	Aslı	KOCA

After distribution, result table example



## **OBTAINED RESULTS (Continue)**



After distribution, homepage example



#### **ACKNOWLEDGEMENTS**

Also, we would like to thank to company engineer; Emrah Yıldırım who helped us during this project.





#### THANK YOU FOR LISTENING

