

[illegible]

The diagrams illustrate the power supply and decoupling network for the PLL and DPHY blocks of the BLU18PG121SN1D device.

**Top Diagram (PLL and DPHY Power Supply):** This diagram shows the power supply for the PLL and DPHY blocks. The PLL block (FB1) is powered by a +3.3V supply through a resistor R26 (0R) and a capacitor C45 (1uF) to GNDREF. The DPHY block (FB2) is powered by a +1V2 supply through a resistor R70 (0R) and a capacitor C46 (100nF) to GNDREF. The PLL block is also connected to a +1V2 supply through a resistor R69 (0R) and a capacitor C47 (1uF) to GNDREF. The DPHY block is connected to a +1V2 supply through a resistor R70 (0R) and a capacitor C48 (100nF) to GNDREF. The PLL block is also connected to a +1V2 supply through a resistor R69 (0R) and a capacitor C47 (1uF) to GNDREF. The DPHY block is connected to a +1V2 supply through a resistor R70 (0R) and a capacitor C48 (100nF) to GNDREF.

**Bottom Diagram (PLL and DPHY Power Supply):** This diagram shows the power supply for the PLL and DPHY blocks. The PLL block (FB3) is powered by a +3.3V supply through a resistor R26 (0R) and a capacitor C45 (1uF) to GNDREF. The DPHY block (FB2) is powered by a +1V2 supply through a resistor R70 (0R) and a capacitor C46 (100nF) to GNDREF. The PLL block is also connected to a +1V2 supply through a resistor R69 (0R) and a capacitor C47 (1uF) to GNDREF. The DPHY block is connected to a +1V2 supply through a resistor R70 (0R) and a capacitor C48 (100nF) to GNDREF. The PLL block is also connected to a +1V2 supply through a resistor R69 (0R) and a capacitor C47 (1uF) to GNDREF. The DPHY block is connected to a +1V2 supply through a resistor R70 (0R) and a capacitor C48 (100nF) to GNDREF.