

**SDI deserializers**

U2A GS2971A Semtech

U2B GS2971A Semtech

U2C GS2971A Semtech

**Programming interface**

J6 GRP052VWVN-RC

**GS\_I2C**

J5 GRP041VWVN-RC

**Logotypes**

Logo N1 antimicro\_logo

Logo N2 oshwa\_logo

**SDI output**

U3 GS2988-INE3 Semtech

**SDI input**

J2

**Filtering**

R30 105R

C58 33u

**RESET**

R84 2k2

S1 B3U-1000P

**MIPI connector**

J3 WL69715014522\_ONE-SIDE

**I2C to SPI bridge**

U5 SC18IS602BIPW

**I2S connector**

J4 GRP052VWVN-RC

**Pull-up resistors**

R85 2k2

R86 2k2

R87 2k2

R94 2k2

The schematic diagram illustrates the LED driver circuit for the L64700. It features five parallel LED branches, each consisting of a series resistor and a diode. The LEDs are labeled LOCKED, LOCKED\_CL, CDDNE, USER\_LED, and DATA\_ERROR. The resistors are R33, R31, R88, R93, and R64, all with a value of 100R. The diodes are D1, D2, D4, and D5, all with a value of 1N4148. The ground connection is labeled LG\_L29K-G2J1-24-Z and GNDREF.

Figure 1: SW1 and SW2 SW-DIP\_x06 pin connections. The diagram shows two 6-pin DIP switches, SW1 and SW2, connected to various pins of the SW1 and SW2 SW-DIP\_x06 components. SW1 is connected to pins 12, 11, 10, 9, 8, and 7. SW2 is connected to pins 12, 11, 10, 9, 8, and 7. The connections are as follows: SW1 pin 12 to USER\_SW, SW1 pin 11 to SDO\_EN\_DIS, SW1 pin 10 to Audio\_EN\_DIS, SW1 pin 9 to IOPROC\_EN\_DIS, SW1 pin 8 to 20MHz\_Limit, and SW1 pin 7 to SMPLE\_BYPASS. SW2 pin 12 to DVB\_ASI, SW2 pin 11 to SW\_EN, SW2 pin 10 to TIM\_961, SW2 pin 9 to RTC\_BYP, SW2 pin 8 to STANDBY, and SW2 pin 7 to JTAG\_HOST. The diagram also shows connections for R68, R76, R78, R80, R82, R75, R77, R79, R81, R83, R85, and R86.